SPEC® CFP2006 Result

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Dell Inc.

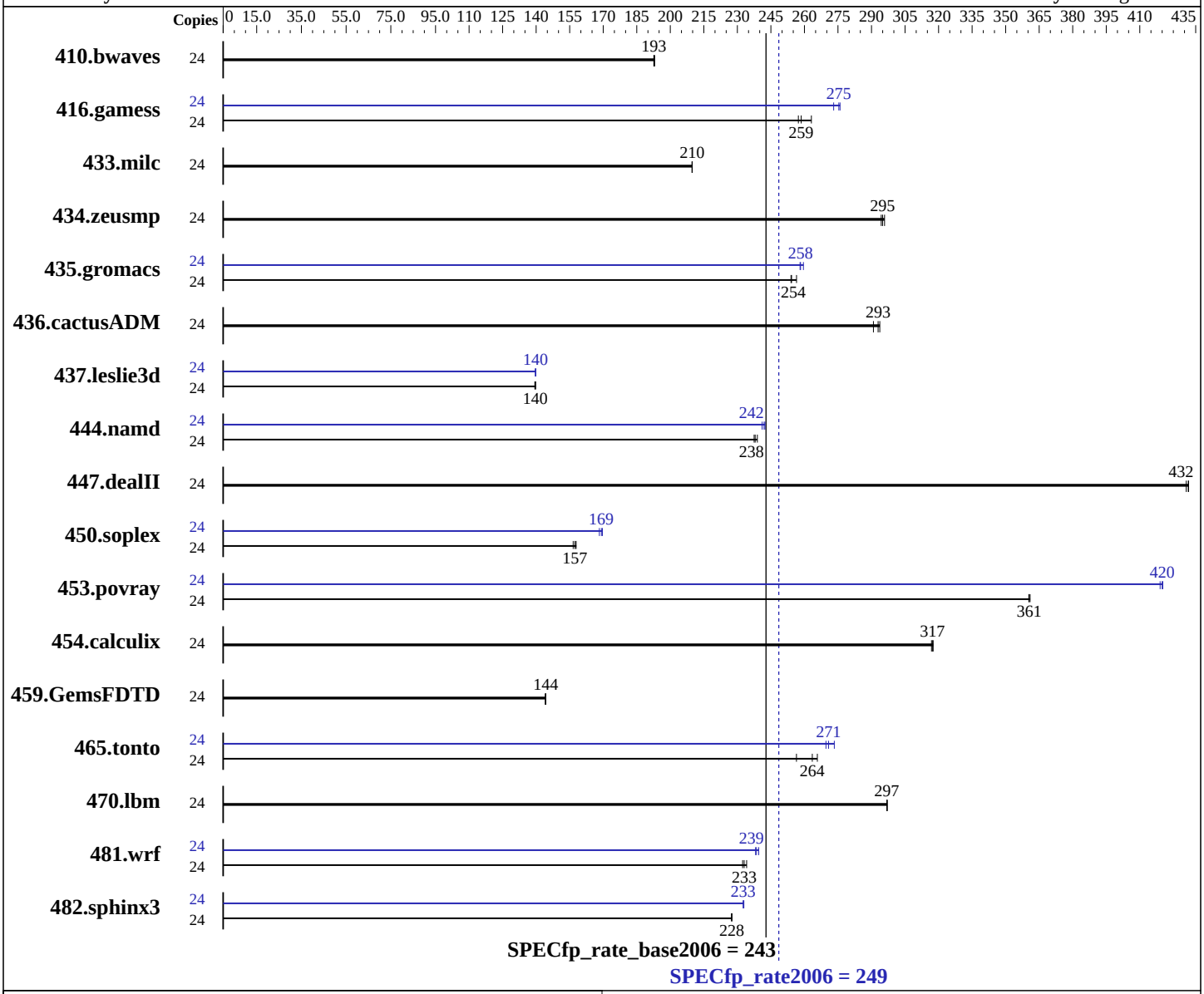
PowerEdge R710 (Intel Xeon X5650, 2.66 GHz)

SPECfp®_rate2006 = 249

SPECfp_rate_base2006 = 243

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Aug-2011
Hardware Availability: Mar-2010
Software Availability: Aug-2011



Hardware

CPU Name:	Intel Xeon X5650
CPU Characteristics:	Intel Turbo Boost Technology up to 3.06 GHz
CPU MHz:	2667
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ and Fortran Intel 64 Compiler XE for applications running on IA32 and Intel 64 12.1.0.225 Build 20110803
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit

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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem: 1 x 146 GB 15000 RPM SAS
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	1690	193	**1693**	**193**	1693	193	24	1690	193	**1693**	**193**	1693	193
416.gamess	24	1786	263	1826	257	**1818**	**259**	24	1721	273	**1706**	**275**	1703	276
433.milc	24	**1050**	**210**	1050	210	1050	210	24	**1050**	**210**	1050	210	1050	210
434.zeusmp	24	738	296	742	294	**740**	**295**	24	738	296	742	294	**740**	**295**
435.gromacs	24	**674**	**254**	668	256	675	254	24	664	258	660	260	**663**	**258**
436.cactusADM	24	977	294	**979**	**293**	986	291	24	977	294	**979**	**293**	986	291
437.leslie3d	24	1614	140	1618	139	**1616**	**140**	24	**1614**	**140**	1613	140	1616	140
444.namd	24	806	239	810	237	**809**	**238**	24	798	241	794	242	**796**	**242**
447.dealII	24	636	432	637	431	**636**	**432**	24	636	432	637	431	**636**	**432**
450.soplex	24	1268	158	1278	157	**1272**	**157**	24	1180	170	1190	168	**1183**	**169**
453.povray	24	354	361	**354**	**361**	354	360	24	**304**	**420**	304	420	305	419
454.calculix	24	**624**	**317**	625	317	623	318	24	**624**	**317**	625	317	623	318
459.GemsFDTD	24	1768	144	1764	144	**1765**	**144**	24	1768	144	1764	144	**1765**	**144**
465.tonto	24	889	266	921	256	**896**	**264**	24	864	273	876	270	**872**	**271**
470.lbm	24	1111	297	1110	297	**1111**	**297**	24	1111	297	1110	297	**1111**	**297**
481.wrf	24	1154	232	**1150**	**233**	1145	234	24	**1124**	**239**	1126	238	1119	240
482.sphinx3	24	2057	227	2056	228	**2056**	**228**	24	2008	233	**2010**	**233**	2011	233

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
 'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
 echo 10800 > /proc/sys/vm/nr_hugepages
 export HUGETLB_MORECORE=yes
 export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

Platform Notes

```
Power Management = Maximum Performance (Default = Active Power Controller)
```

General Notes

```
environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/root/cpu2006/smartheap:/root/cpu2006/ic12.1-libs/ia32/:/root/cpu2006/ic12.1-libs/intel64"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RHEL5.5 with binutils-2.17.50.0.6-14.el5
 Stack size set to unlimited using "ulimit -s unlimited"

 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>

 The Dell PowerEdge R710 and
 the Bull NovaScale R460 F2 models are electronically equivalent.
 The results have been measured on a Dell PowerEdge R710 model
```

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main

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Base Portability Flags (Continued)

459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`
482.sphinx3: `-DSPEC_CPU_LP64`

Base Optimization Flags

C benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32 -ansi-alias -opt-mem-layout-trans=3`

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32 -ansi-alias -opt-mem-layout-trans=3`

Fortran benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`

Benchmarks using both Fortran and C:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32 -ansi-alias -opt-mem-layout-trans=3`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m64`

450.soplex: `icpc -m32`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3 -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

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Peak Optimization Flags (Continued)

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -opt-prefetch -static -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110920.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110920.xml

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Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 22:25:49 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 20 September 2011.