



# SPEC® CINT2006 Result

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## Acer Incorporated

Acer AW2000ht-AW170ht F1 (Intel Xeon L5630, 2.13GHz)

**SPECint®\_rate2006 = 195**

**SPECint\_rate\_base2006 = 184**

CPU2006 license: 97

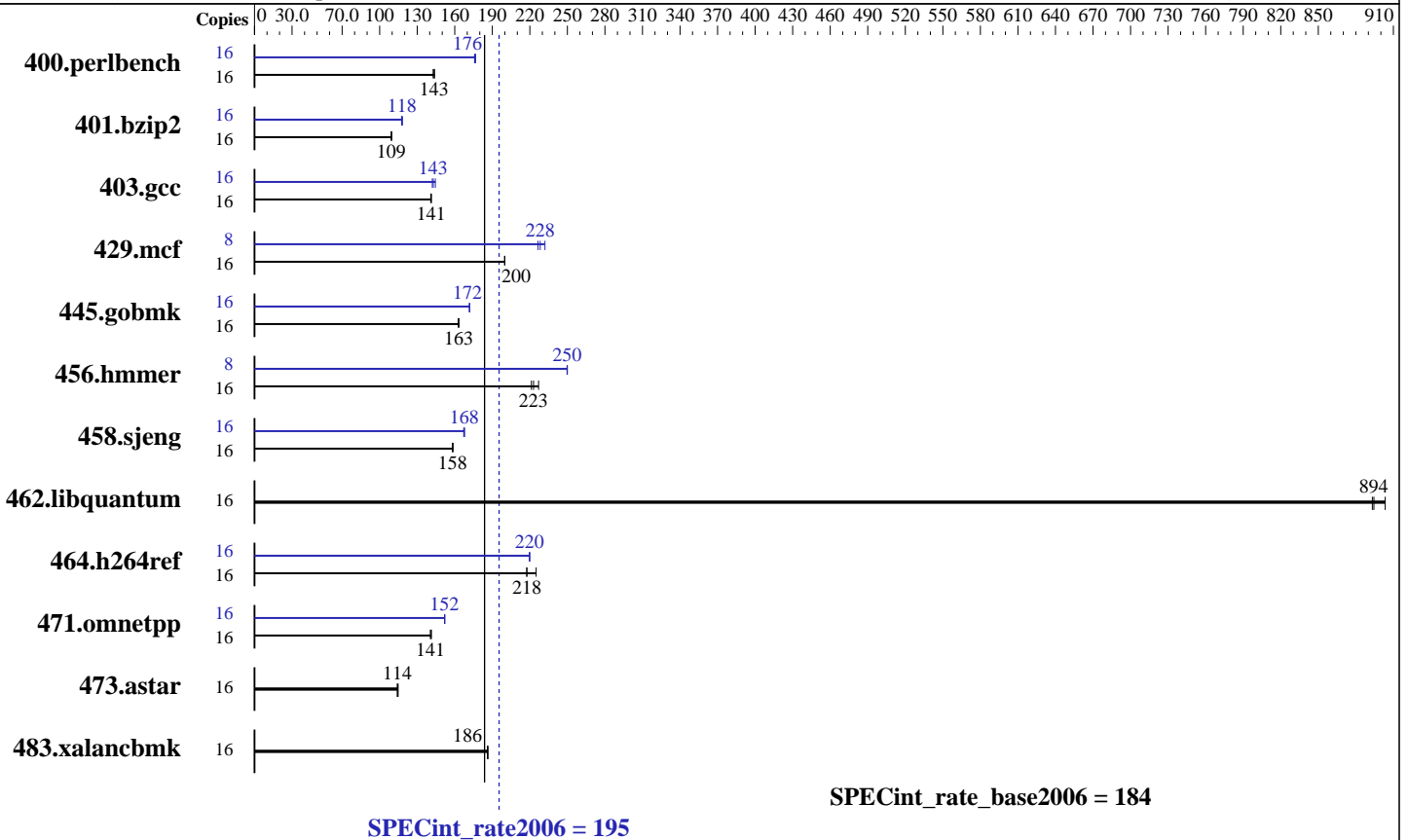
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Aug-2011

Hardware Availability: Aug-2010

Software Availability: Jan-2011



### Hardware

CPU Name: Intel Xeon L5630  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.40 GHz  
 CPU MHz: 2133  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12 x 4 GB 2Rx8 PC3-10600R-9, ECC)  
 Disk Subsystem: 1 x 1000 GB SATA 7200RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64) SP1, Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	<b><u>1089</u></b>	<b><u>143</u></b>	1087	144	1095	143	16	885	177	<b><u>887</u></b>	<b><u>176</u></b>	889	176
401.bzip2	16	1409	110	1412	109	<b><u>1412</u></b>	<b><u>109</u></b>	16	<b><u>1310</u></b>	<b><u>118</u></b>	1306	118	1313	118
403.gcc	16	<b><u>912</u></b>	<b><u>141</u></b>	912	141	915	141	16	907	142	891	145	<b><u>902</u></b>	<b><u>143</u></b>
429.mcf	16	730	200	729	200	<b><u>730</u></b>	<b><u>200</u></b>	8	<b><u>320</u></b>	<b><u>228</u></b>	322	227	314	232
445.gobmk	16	<b><u>1029</u></b>	<b><u>163</u></b>	1031	163	1028	163	16	977	172	<b><u>977</u></b>	<b><u>172</u></b>	976	172
456.hammer	16	675	221	<b><u>670</u></b>	<b><u>223</u></b>	658	227	8	<b><u>299</u></b>	<b><u>250</u></b>	299	250	299	250
458.sjeng	16	<b><u>1223</u></b>	<b><u>158</u></b>	1223	158	1221	159	16	1154	168	1158	167	<b><u>1155</u></b>	<b><u>168</u></b>
462.libquantum	16	<b><u>371</u></b>	<b><u>894</u></b>	367	903	371	893	16	<b><u>371</u></b>	<b><u>894</u></b>	367	903	371	893
464.h264ref	16	1628	218	1573	225	<b><u>1627</u></b>	<b><u>218</u></b>	16	1612	220	<b><u>1611</u></b>	<b><u>220</u></b>	1611	220
471.omnetpp	16	<b><u>711</u></b>	<b><u>141</u></b>	711	141	707	141	16	658	152	658	152	<b><u>658</u></b>	<b><u>152</u></b>
473.astar	16	979	115	<b><u>983</u></b>	<b><u>114</u></b>	984	114	16	979	115	<b><u>983</u></b>	<b><u>114</u></b>	984	114
483.xalancbmk	16	592	186	593	186	<b><u>592</u></b>	<b><u>186</u></b>	16	592	186	593	186	<b><u>592</u></b>	<b><u>186</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size  
Large pages were disabled for this run

## Platform Notes

BIOS Settings:  
Fan speed = full speed (Default = Balanced)  
Data Reuse = Disabled (Default = Enabled)

## General Notes

Binaries compiled on RHEL5.5  
The Acer AW2000h-AW170h F1, Gateway GW2000h-GW170h F1, Acer AW2000ht-AW170ht F1 and Gateway GW2000ht-GW170ht F1 are electronically equivalent. This result was measured on Gateway GW2000ht-GW170ht F1.

## Base Compiler Invocation

C benchmarks:  
icc -m32

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:  
icpc -m32



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs

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## Peak Optimization Flags (Continued)

471.omnetpp (continued):  
-L/smartheap -lsmartheap  
  
473.astar: basepeak = yes  
  
483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:  
  
403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>  
<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>  
<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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