



# SPEC® CFP2006 Result

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Dell Inc.

SPECfp®2006 = 50.7

PowerEdge R610 (Intel Xeon L5640, 2.26 GHz)

SPECfp\_base2006 = 46.7

CPU2006 license: 55

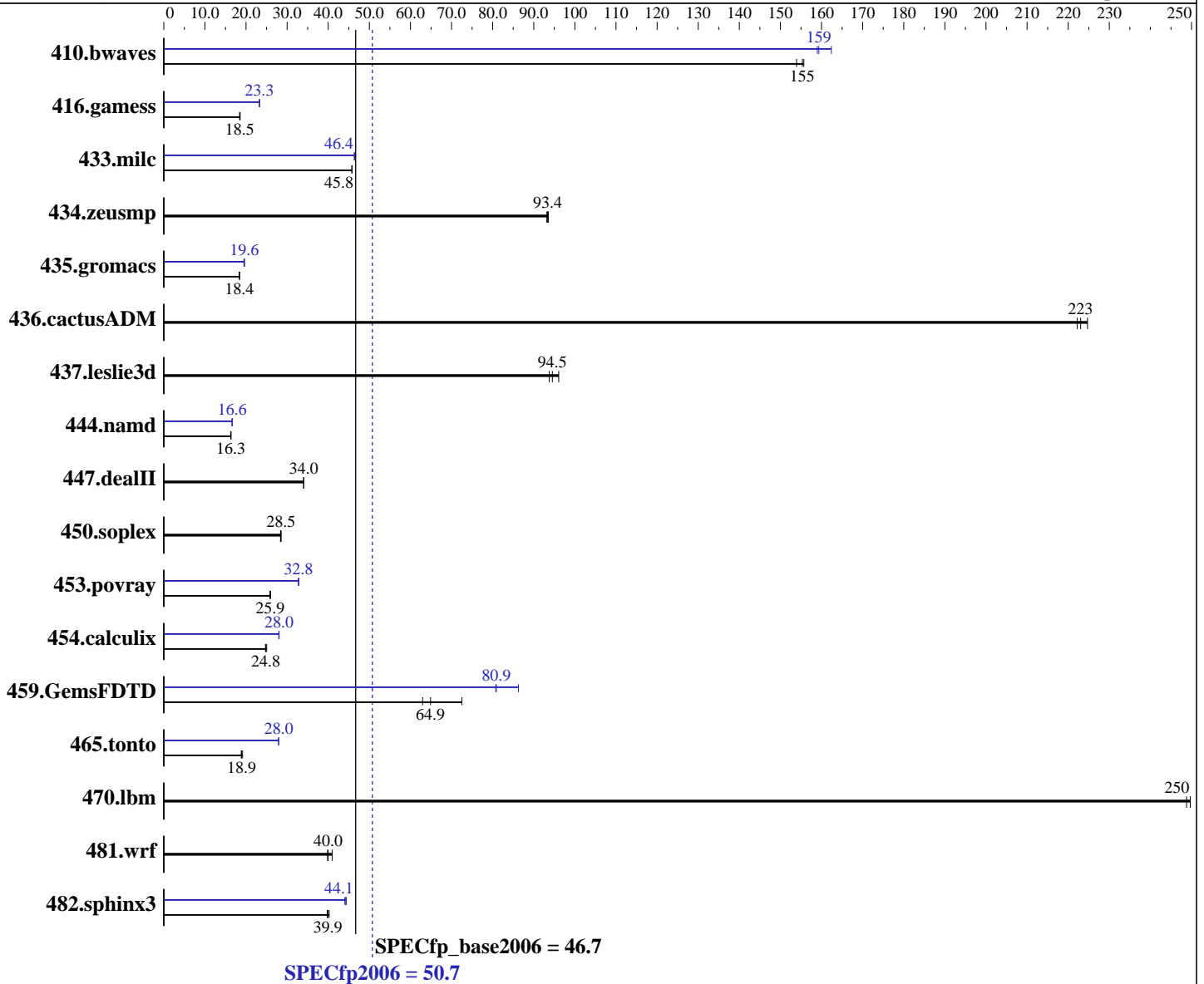
Test date: May-2011

Test sponsor: Dell Inc.

Hardware Availability: Mar-2010

Tested by: Dell Inc.

Software Availability: Apr-2011



## Hardware

CPU Name: Intel Xeon L5640  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz  
 CPU MHz: 2267  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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## Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86\_64), Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0 Update 3  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit

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L3 Cache: 12 MB I+D on chip per chip  
Other Cache: None  
Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)  
Disk Subsystem: 1 x 146 GB 15000 RPM SAS  
Other Hardware: None

Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	88.3	154	87.3	156	<b>87.5</b>	<b>155</b>	85.5	159	83.7	162	<b>85.3</b>	<b>159</b>
416.gamess	1054	18.6	<b>1060</b>	<b>18.5</b>	1061	18.5	841	23.3	840	23.3	<b>841</b>	<b>23.3</b>
433.milc	<b>201</b>	<b>45.8</b>	201	45.7	200	45.8	<b>198</b>	<b>46.4</b>	198	46.4	198	46.4
434.zeusmp	97.3	93.6	<b>97.5</b>	<b>93.4</b>	97.7	93.2	97.3	93.6	<b>97.5</b>	<b>93.4</b>	97.7	93.2
435.gromacs	387	18.4	<b>388</b>	<b>18.4</b>	388	18.4	364	19.6	<b>364</b>	<b>19.6</b>	366	19.5
436.cactusADM	<b>53.6</b>	<b>223</b>	53.8	222	53.2	225	<b>53.6</b>	<b>223</b>	53.8	222	53.2	225
437.leslie3d	100	93.8	<b>99.4</b>	<b>94.5</b>	97.8	96.1	100	93.8	<b>99.4</b>	<b>94.5</b>	97.8	96.1
444.namd	491	16.3	492	16.3	<b>491</b>	<b>16.3</b>	482	16.6	483	16.6	<b>482</b>	<b>16.6</b>
447.dealII	<b>336</b>	<b>34.0</b>	336	34.0	336	34.1	<b>336</b>	<b>34.0</b>	336	34.0	336	34.1
450.soplex	293	28.5	<b>293</b>	<b>28.5</b>	294	28.4	293	28.5	<b>293</b>	<b>28.5</b>	294	28.4
453.povray	<b>206</b>	<b>25.9</b>	205	26.0	206	25.9	162	32.9	<b>162</b>	<b>32.8</b>	162	32.7
454.calculix	334	24.7	330	25.0	<b>332</b>	<b>24.8</b>	<b>294</b>	<b>28.0</b>	295	28.0	294	28.0
459.GemsFDTD	169	63.0	146	72.5	<b>164</b>	<b>64.9</b>	123	86.3	<b>131</b>	<b>80.9</b>	131	80.8
465.tonto	<b>520</b>	<b>18.9</b>	513	19.2	522	18.8	352	27.9	352	28.0	<b>352</b>	<b>28.0</b>
470.lbm	<b>55.0</b>	<b>250</b>	55.0	250	55.2	249	<b>55.0</b>	<b>250</b>	55.0	250	55.2	249
481.wrf	<b>279</b>	<b>40.0</b>	273	41.0	280	39.8	<b>279</b>	<b>40.0</b>	273	41.0	280	39.8
482.sphinx3	490	39.8	<b>488</b>	<b>39.9</b>	485	40.2	<b>442</b>	<b>44.1</b>	442	44.1	439	44.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages  
echo 900 > /proc/sys/vm/nr\_hugepages  
export HUGETLB\_MORECORE=yes  
export LD\_PRELOAD=/usr/lib64/libhugetlbfs.so

## Platform Notes

BIOS Settings:  
Power Management = Maximum Performance (Default = Active Power Controller)  
Data Reuse = Disabled (Default = Enabled)  
Logical Processor = Disabled (Default = Enabled)



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## General Notes

OMP\_NUM\_THREADS set to number of cores  
The Dell PowerEdge R610 and the Bull NovaScale R440 F2 models are electronically equivalent. The results have been measured on a Dell PowerEdge R610 model  
Binaries were compiled on RHEL5.5

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.deallI: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

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## Base Optimization Flags (Continued)

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel

C++ benchmarks:

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## Peak Optimization Flags (Continued)

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

### Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -parallel  
-static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

### Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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