



SPEC® CFP2006 Result

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ASUSTeK Computer Inc.
(Test Sponsor: Intel Corporation)

SPECfp®2006 = 31.4

ASUSTek M4A89GTD PRO/USB3 (Phenom II X4 970)

SPECfp_base2006 = 30.5

CPU2006 license: 13

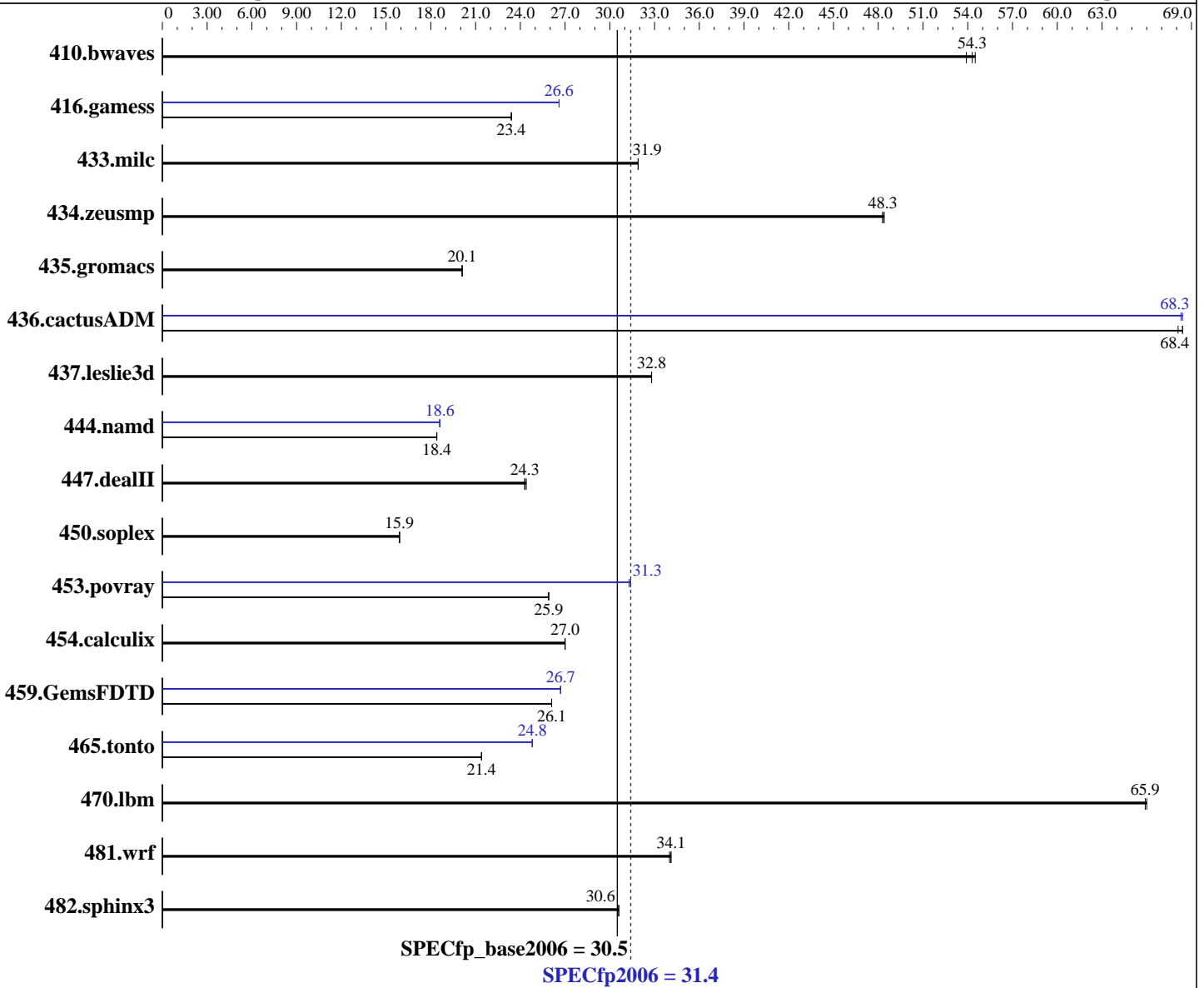
Test date: Jun-2011

Test sponsor: Intel Corporation

Hardware Availability: Feb-2011

Tested by: Intel Corporation

Software Availability: Apr-2011



Hardware

CPU Name: AMD Phenom II X4 970
 CPU Characteristics:
 CPU MHz: 3500
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 64 KB I + 64 KB D on chip per core
 Secondary Cache: 512 KB I+D on chip per core

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Software

Operating System: Windows 7 Ultimate (64-bit)
 Compiler: Intel C++ Compiler XE for Intel 64
 Version 12.0.3.176 Build 20110309
 Intel Visual Fortran Compiler XE for Intel 64
 Version 12.0.3.176 Build 20110309
 Microsoft Visual Studio 2008 Professional SP1
 (for libraries)

Auto Parallel: Yes
 File System: NTFS

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L3 Cache: 6 MB I+D on chip per chip
Other Cache: None
Memory: 4 GB (2 x 2 GB 2Rx4 PC3-10600U-9)
Disk Subsystem: 1 TB Seagate SATA, 7200 RPM
Other Hardware: None

System State: Default
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: SmartHeap Library Version 9.01 from <http://www.microquill.com/>

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	252	53.9	250	54.3	249	54.5	252	53.9	250	54.3	249	54.5
416.gamess	837	23.4	838	23.4	838	23.4	737	26.6	737	26.6	737	26.6
433.milc	288	31.9	288	31.9	288	31.9	288	31.9	288	31.9	288	31.9
434.zeusmp	189	48.3	188	48.3	188	48.4	189	48.3	188	48.3	188	48.4
435.gromacs	356	20.1	356	20.1	356	20.1	356	20.1	356	20.1	356	20.1
436.cactusADM	176	68.1	175	68.4	175	68.4	175	68.3	175	68.4	175	68.3
437.leslie3d	286	32.8	287	32.8	287	32.8	286	32.8	287	32.8	287	32.8
444.namd	435	18.4	435	18.4	435	18.4	431	18.6	431	18.6	431	18.6
447.dealII	470	24.4	471	24.3	472	24.3	470	24.4	471	24.3	472	24.3
450.soplex	525	15.9	525	15.9	525	15.9	525	15.9	525	15.9	525	15.9
453.povray	205	25.9	205	25.9	206	25.9	169	31.4	170	31.3	170	31.3
454.calculix	305	27.0	305	27.0	305	27.0	305	27.0	305	27.0	305	27.0
459.GemsFDTD	407	26.1	407	26.1	407	26.1	397	26.7	397	26.7	397	26.7
465.tonto	460	21.4	459	21.4	459	21.4	396	24.8	396	24.8	396	24.8
470.lbm	208	65.9	209	65.9	208	66.0	208	65.9	209	65.9	208	66.0
481.wrf	328	34.1	328	34.0	328	34.1	328	34.1	328	34.0	328	34.1
482.sphinx3	637	30.6	638	30.6	637	30.6	637	30.6	638	30.6	637	30.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply
OMP_NUM_THREADS set to number of processors cores
KMP_AFFINITY set to granularity=fine,scatter

Base Compiler Invocation

C benchmarks:
icl -Qvc9 -Qstd=c99

C++ benchmarks:
icl -Qvc9

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc9 -Qstd=c99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64 -names:lowercase
416.gamess: -DSPEC_CPU_P64
433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 -names:lowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 /TP
447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -names:lowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:
/arch:SSE3 -Qipo -O3 -Qparallel -Qansi-alias -Qopt-prefetch
-Qauto-ilp32 /F1000000000

C++ benchmarks:
/arch:SSE3 -Qipo -O3 -Qparallel -Qansi-alias -Qopt-prefetch
-Qcxx-features -Qauto-ilp32 -Qprec-div- /F1000000000 shlw64M.lib
-link /FORCE:MULTIPLE

Fortran benchmarks:
/arch:SSE3 -Qipo -O3 -Qparallel -Qansi-alias -Qopt-prefetch
/F1000000000

Benchmarks using both Fortran and C:
/arch:SSE3 -Qipo -O3 -Qparallel -Qansi-alias -Qopt-prefetch
-Qauto-ilp32 /F1000000000



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Peak Compiler Invocation

C benchmarks:
icl -Qvc9 -Qstd=c99
C++ benchmarks:
icl -Qvc9
Fortran benchmarks:
ifort
Benchmarks using both Fortran and C:
icl -Qvc9 -Qstd=c99 ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes
C++ benchmarks:
444.namd: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Oa -Qauto-ilp32 /F1000000000
sh1W64M.lib -link /FORCE:MULTIPLE
447.dealII: basepeak = yes
450.soplex: basepeak = yes
453.povray: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll4 -Qansi-alias -Qauto-ilp32
/F1000000000 sh1W64M.lib -link /FORCE:MULTIPLE
Fortran benchmarks:
410.bwaves: basepeak = yes
416.gamess: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qansi-alias
-Qscalar-rep- /F1000000000

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Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Qopt-prefetch -Qparallel
/F1000000000

465.tonto: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll4 -Qauto -Qinline-calloc
/F1000000000

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel -Qunroll2
-Qauto-ilp32 /F1000000000

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.html>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.20110719.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.xml>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.20110719.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 22:25:42 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 20 September 2011.