



SPEC[®] CFP2006 Result

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ASUSTeK Computer Inc.
(Test Sponsor: Intel Corporation)

SPECfp[®]2006 = 22.1

ASUSTek M4A89GTD PRO/USB3 (Athlon II X2 220)

SPECfp_base2006 = 21.5

CPU2006 license: 13

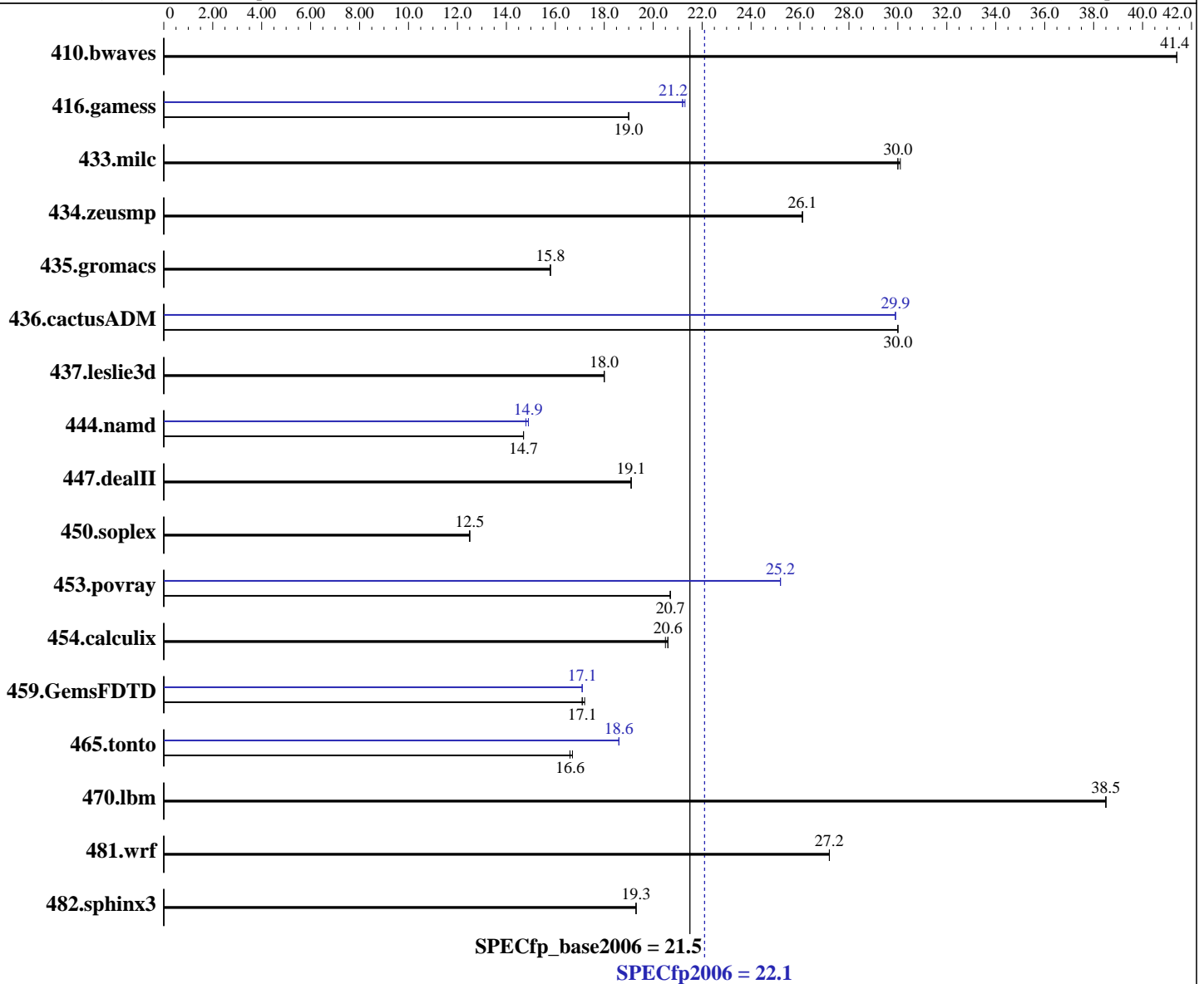
Test date: Jun-2011

Test sponsor: Intel Corporation

Hardware Availability: Feb-2011

Tested by: Intel Corporation

Software Availability: Apr-2011



Hardware	
CPU Name:	AMD Athlon II X2 220
CPU Characteristics:	
CPU MHz:	2800
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	512 KB I+D on chip per core

Software	
Operating System:	Windows 7 Ultimate (64-bit)
Compiler:	Intel C++ Compiler XE for Intel 64 Version 12.0.3.176 Build 20110309 Intel Visual Fortran Compiler XE for Intel 64 Version 12.0.3.176 Build 20110309 Microsoft Visual Studio 2008 Professional SP1 (for libraries)
Auto Parallel:	Yes
File System:	NTFS

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L3 Cache: None
Other Cache: None
Memory: 4 GB (2 x 2 GB 2Rx4 PC3-10600U-9)
Disk Subsystem: 1 TB Seagate SATA, 7200 RPM
Other Hardware: None

System State: Default
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: SmartHeap Library Version 9.01 from <http://www.microquill.com/>

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	328	41.4	328	41.4	<u>328</u>	<u>41.4</u>	328	41.4	328	41.4	<u>328</u>	<u>41.4</u>
416.gamess	<u>1030</u>	<u>19.0</u>	1030	19.0	1028	19.0	921	21.3	<u>923</u>	<u>21.2</u>	923	21.2
433.milc	<u>306</u>	<u>30.0</u>	305	30.1	306	30.0	<u>306</u>	<u>30.0</u>	305	30.1	306	30.0
434.zeusmp	<u>349</u>	<u>26.1</u>	349	26.1	349	26.1	<u>349</u>	<u>26.1</u>	349	26.1	349	26.1
435.gromacs	<u>452</u>	<u>15.8</u>	452	15.8	452	15.8	<u>452</u>	<u>15.8</u>	452	15.8	452	15.8
436.cactusADM	<u>398</u>	<u>30.0</u>	398	30.0	399	30.0	400	29.9	<u>400</u>	<u>29.9</u>	400	29.9
437.leslie3d	523	18.0	523	18.0	<u>523</u>	<u>18.0</u>	523	18.0	523	18.0	<u>523</u>	<u>18.0</u>
444.namd	<u>546</u>	<u>14.7</u>	546	14.7	546	14.7	540	14.8	540	14.9	<u>540</u>	<u>14.9</u>
447.dealII	<u>600</u>	<u>19.1</u>	600	19.1	600	19.1	<u>600</u>	<u>19.1</u>	600	19.1	600	19.1
450.soplex	<u>666</u>	<u>12.5</u>	666	12.5	665	12.5	<u>666</u>	<u>12.5</u>	666	12.5	665	12.5
453.povray	256	20.7	<u>256</u>	<u>20.7</u>	256	20.7	211	25.2	<u>211</u>	<u>25.2</u>	211	25.2
454.calculix	401	20.6	<u>401</u>	<u>20.6</u>	402	20.5	401	20.6	<u>401</u>	<u>20.6</u>	402	20.5
459.GemsFDTD	<u>619</u>	<u>17.1</u>	619	17.2	619	17.1	620	17.1	619	17.1	<u>619</u>	<u>17.1</u>
465.tonto	<u>591</u>	<u>16.6</u>	591	16.6	591	16.7	<u>530</u>	<u>18.6</u>	530	18.6	530	18.6
470.lbm	<u>357</u>	<u>38.5</u>	357	38.5	357	38.5	<u>357</u>	<u>38.5</u>	357	38.5	357	38.5
481.wrf	<u>411</u>	<u>27.2</u>	410	27.2	411	27.2	<u>411</u>	<u>27.2</u>	410	27.2	411	27.2
482.sphinx3	1010	19.3	<u>1010</u>	<u>19.3</u>	1011	19.3	<u>1010</u>	<u>19.3</u>	<u>1010</u>	<u>19.3</u>	1011	19.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply
OMP_NUM_THREADS set to number of processors cores
KMP_AFFINITY set to granularity=fine,scatter

Base Compiler Invocation

C benchmarks:
icl -Qvc9 -Qstd=c99

C++ benchmarks:
icl -Qvc9

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc9 -Qstd=c99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64 -names:lowercase
416.gamess: -DSPEC_CPU_P64
433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 -names:lowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 /TP
447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -names:lowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:
/arch:SSE3 -Qipo -O3 -Qparallel -Qansi-alias -Qopt-prefetch
-Qauto-ilp32 /F1000000000

C++ benchmarks:
/arch:SSE3 -Qipo -O3 -Qparallel -Qansi-alias -Qopt-prefetch
-Qcxx-features -Qauto-ilp32 -Qprec-div- /F1000000000 shlw64M.lib
-link /FORCE:MULTIPLE

Fortran benchmarks:
/arch:SSE3 -Qipo -O3 -Qparallel -Qansi-alias -Qopt-prefetch
/F1000000000

Benchmarks using both Fortran and C:
/arch:SSE3 -Qipo -O3 -Qparallel -Qansi-alias -Qopt-prefetch
-Qauto-ilp32 /F1000000000



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Peak Compiler Invocation

C benchmarks:
icl -Qvc9 -Qstd=c99
C++ benchmarks:
icl -Qvc9
Fortran benchmarks:
ifort
Benchmarks using both Fortran and C:
icl -Qvc9 -Qstd=c99 ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes
C++ benchmarks:
444.namd: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Oa -Qauto-ilp32 /F1000000000
sh1W64M.lib -link /FORCE:MULTIPLE
447.dealII: basepeak = yes
450.soplex: basepeak = yes
453.povray: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll4 -Qansi-alias -Qauto-ilp32
/F1000000000 sh1W64M.lib -link /FORCE:MULTIPLE
Fortran benchmarks:
410.bwaves: basepeak = yes
416.gamess: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qansi-alias
-Qscalar-rep- /F1000000000

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Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Qopt-prefetch -Qparallel
/F1000000000

465.tonto: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll4 -Qauto -Qinline-calloc
/F1000000000

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel -Qunroll2
-Qauto-ilp32 /F1000000000

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.html>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.20110719.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.xml>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.20110719.xml>

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For other inquiries, please contact webmaster@spec.org.

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