SPEC® CINT2006 Result

Cisco Systems

SPECint®_rate2006 =   509

Cisco UCS C460 M2 (Intel Xeon E7-4807, 1.87 GHz)

SPECint_rate_base2006 = 480

CPU2006 license: 9019
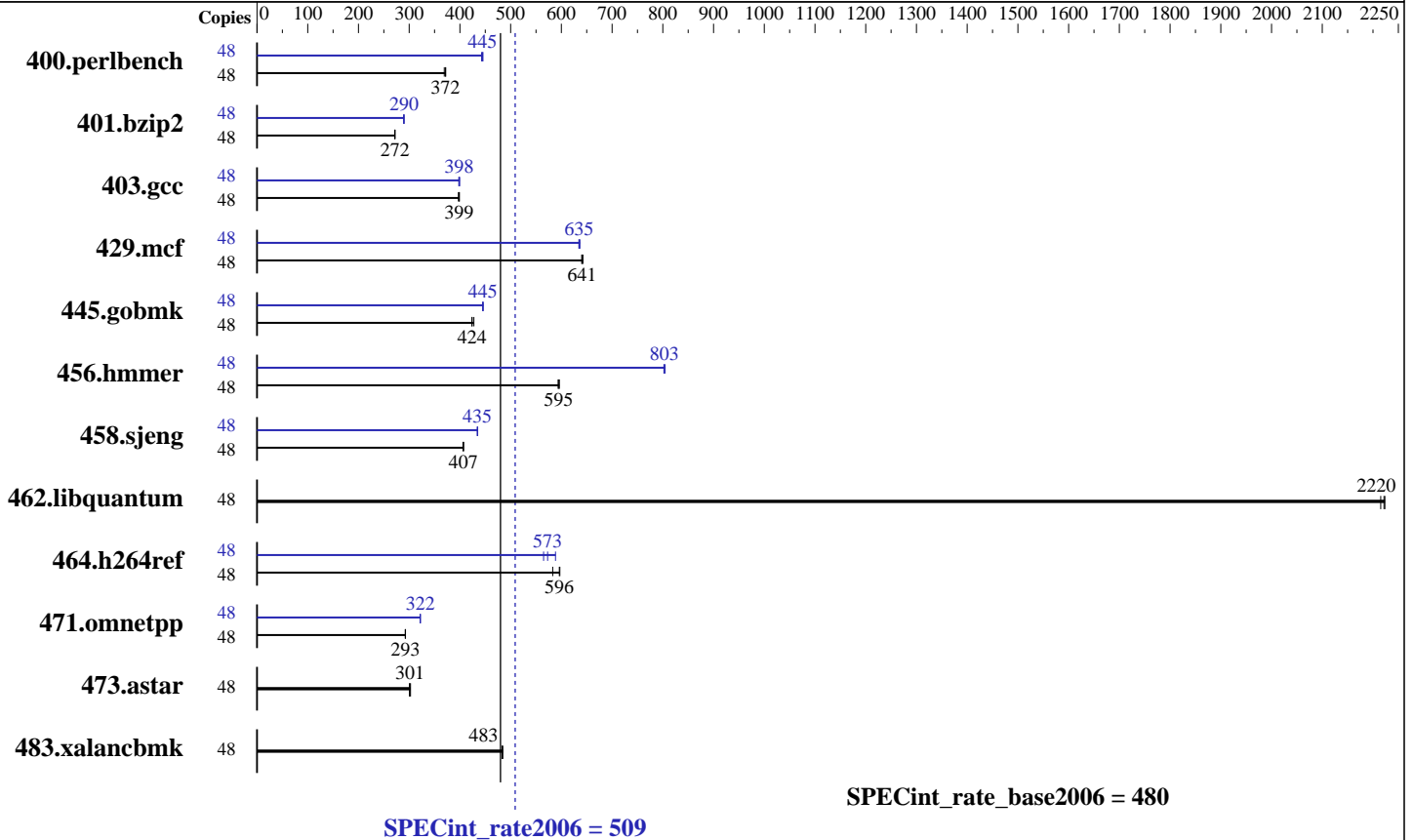
Test sponsor: Cisco Systems

Tested by: Cisco Systems

Test date: Jul-2011

Hardware Availability: May-2011

Software Availability: Mar-2011

SPECint_rate2006 = 509

SPECint_rate_base2006 = 480

Hardware

CPU Name:	Intel Xeon E7-4807
CPU Characteristics:	None
CPU MHz:	1867
FPU:	Integrated
CPU(s) enabled:	24 cores, 4 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2,3,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	18 MB I+D on chip per chip
Other Cache:	None
Memory:	1 TB (64 x 16 GB 4Rx4 PC3-8500R-9, ECC, running at 800 MHz)
Disk Subsystem:	146 GB SAS, 15K RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.1 Beta Kernel 2.6.32-130.el6.x86_64
Compiler:	Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

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Results Table

Benchmark	Copies	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Copies	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
400.perlbench	48	1261	372	**1262**	**372**	1270	369	48	**1055**	**445**	1053	445	1059	443
401.bzip2	48	1703	272	**1703**	**272**	1706	271	48	1603	289	1598	290	**1598**	**290**
403.gcc	48	969	399	**969**	**399**	973	397	48	971	398	**970**	**398**	967	400
429.mcf	48	684	640	**683**	**641**	681	643	48	**689**	**635**	688	637	690	635
445.gobmk	48	**1188**	**424**	1178	428	1188	424	48	**1131**	**445**	1129	446	1131	445
456.hmmer	48	751	596	755	594	**753**	**595**	48	557	805	558	802	**558**	**803**
458.sjeng	48	1426	407	**1427**	**407**	1431	406	48	1335	435	**1337**	**435**	1337	434
462.libquantum	48	**448**	**2220**	449	2220	447	2220	48	**448**	**2220**	449	2220	447	2220
464.h264ref	48	1779	597	1822	583	**1782**	**596**	48	1880	565	**1854**	**573**	1805	588
471.omnetpp	48	**1025**	**293**	1025	293	1027	292	48	933	322	931	322	**932**	**322**
473.astar	48	1113	303	**1118**	**301**	1121	300	48	1113	303	**1118**	**301**	1121	300
483.xalancbmk	48	685	483	**685**	**483**	684	485	48	685	483	**685**	**483**	684	485

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
ulimit -s unlimited was used to set the stacksize to unlimited prior to run
Large pages were disabled for this run
```

Platform Notes

```
BIOS Configuration : Data Reuse Optimization = Disabled
```

General Notes

```
Binaries compiled on RHEL5.5 with
binutils-2.17.50.0.6-14.el5
```

Base Compiler Invocation

C benchmarks:
```
icc -m32
```

C++ benchmarks:
```
icpc -m32
```

SPEC CINT2006 Result

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Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
   462.libquantum: -DSPEC_CPU_LINUX
   483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
    -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT
```

C++ benchmarks:

```
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
    -L/smartheap -lsmartheap
    -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT
```

Base Other Flags

C benchmarks:

```
       403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
    icc  -m32
```

```
    400.perlbench: icc -m64
```

```
       401.bzip2: icc -m64
```

```
      456.hmmer: icc -m64
```

```
       458.sjeng: icc -m64
```

C++ benchmarks:
```
    icpc -m32
```

Peak Portability Flags

```
    400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
       401.bzip2: -DSPEC_CPU_LP64
```

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CPU2006 license: 9019		**Test date:**	Jul-2011
Test sponsor: Cisco Systems		**Hardware Availability:**	May-2011
Tested by: Cisco Systems		**Software Availability:**	Mar-2011

Peak Portability Flags (Continued)

```
    456.hmmer: -DSPEC_CPU_LP64
    458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
  400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

      401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -opt-prefetch -auto-ilp32 -ansi-alias
                 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

        403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div
                 -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

        429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -ansi-alias -auto-ilp32

      445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
                 -ansi-alias -auto-ilp32

      456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
                 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

      458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -unroll4 -auto-ilp32
                 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

     464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -unroll2 -ansi-alias
```

C++ benchmarks:

```
    471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
                 -L/smartheap -lsmartheap
```

Continued on next page

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CPU2006 license: 9019
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Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings.20110808.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings.20110808.xml

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Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 23:56:18 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 2 August 2011.