



SPEC[®] CFP2006 Result

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Acer Incorporated

Acer AW2000ht-AW170ht F1 (Intel Xeon X5672, 3.2 GHz)

SPECfp[®]_rate2006 = 229

SPECfp_rate_base2006 = 223

CPU2006 license: 97

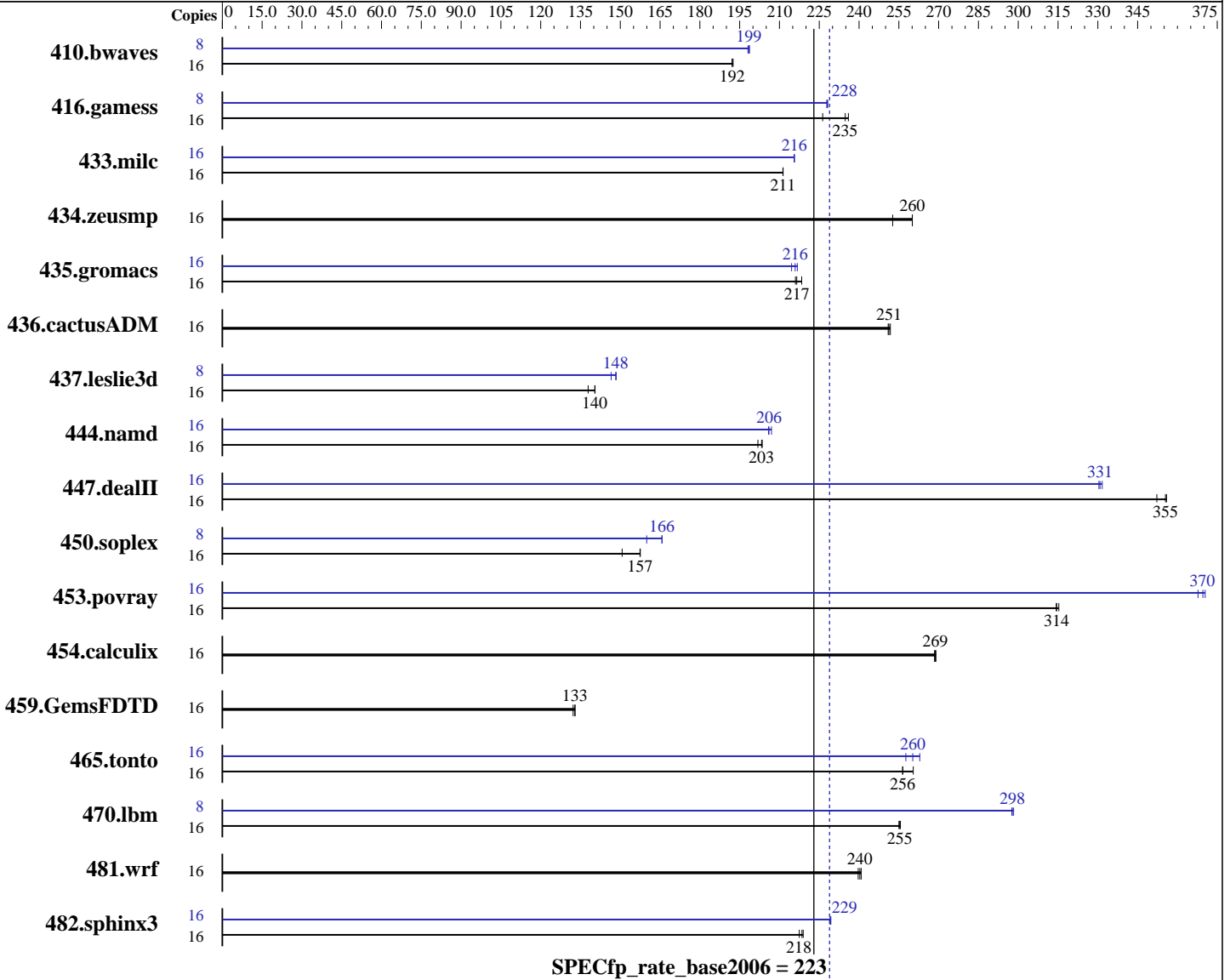
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Jun-2011

Hardware Availability: Feb-2011

Software Availability: Jan-2011



Hardware

CPU Name: Intel Xeon X5672
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
 CPU MHz: 3200
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) SP1, Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB 2Rx8 PC3-10600R-9, ECC)
Disk Subsystem: 1 x 1000 GB SATA 7200RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---------------|--------|-------------|------------|-------------|------------|-------------|------------|--------|------------|------------|-------------|------------|-------------|------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 16 | 1129 | 193 | <u>1132</u> | <u>192</u> | 1132 | 192 | 8 | <u>548</u> | <u>199</u> | 547 | 199 | 549 | 198 |
| 416.gamess | 16 | 1328 | 236 | 1384 | 226 | <u>1334</u> | <u>235</u> | 8 | <u>687</u> | <u>228</u> | 686 | 228 | 688 | 228 |
| 433.milc | 16 | <u>695</u> | <u>211</u> | 695 | 211 | 695 | 211 | 16 | 682 | 216 | <u>681</u> | <u>216</u> | 681 | 216 |
| 434.zeusmp | 16 | 560 | 260 | <u>560</u> | <u>260</u> | 576 | 253 | 16 | 560 | 260 | <u>560</u> | <u>260</u> | 576 | 253 |
| 435.gromacs | 16 | 523 | 218 | 529 | 216 | <u>528</u> | <u>217</u> | 16 | 532 | 215 | <u>529</u> | <u>216</u> | 527 | 217 |
| 436.cactusADM | 16 | 759 | 252 | 762 | 251 | <u>761</u> | <u>251</u> | 16 | 759 | 252 | 762 | 251 | <u>761</u> | <u>251</u> |
| 437.leslie3d | 16 | <u>1071</u> | <u>140</u> | 1071 | 140 | 1091 | 138 | 8 | 513 | 147 | <u>507</u> | <u>148</u> | 507 | 148 |
| 444.namd | 16 | 631 | 204 | <u>631</u> | <u>203</u> | 636 | 202 | 16 | 620 | 207 | <u>623</u> | <u>206</u> | 623 | 206 |
| 447.dealII | 16 | 520 | 352 | <u>515</u> | <u>355</u> | 514 | 356 | 16 | 554 | 330 | <u>553</u> | <u>331</u> | 552 | 332 |
| 450.soplex | 16 | 886 | 151 | <u>848</u> | <u>157</u> | 847 | 158 | 8 | 417 | 160 | 403 | 166 | <u>403</u> | <u>166</u> |
| 453.povray | 16 | 271 | 314 | 270 | 315 | <u>271</u> | <u>314</u> | 16 | <u>230</u> | <u>370</u> | 230 | 370 | 231 | 368 |
| 454.calculix | 16 | 491 | 269 | <u>491</u> | <u>269</u> | 492 | 268 | 16 | 491 | 269 | <u>491</u> | <u>269</u> | 492 | 268 |
| 459.GemsFDTD | 16 | 1277 | 133 | 1284 | 132 | <u>1278</u> | <u>133</u> | 16 | 1277 | 133 | 1284 | 132 | <u>1278</u> | <u>133</u> |
| 465.tonto | 16 | <u>614</u> | <u>256</u> | 605 | 260 | 614 | 256 | 16 | 611 | 258 | <u>605</u> | <u>260</u> | 599 | 263 |
| 470.lbm | 16 | 860 | 256 | <u>861</u> | <u>255</u> | 862 | 255 | 8 | 369 | 298 | <u>369</u> | <u>298</u> | 369 | 298 |
| 481.wrf | 16 | 742 | 241 | 746 | 240 | <u>744</u> | <u>240</u> | 16 | 742 | 241 | 746 | 240 | <u>744</u> | <u>240</u> |
| 482.sphinx3 | 16 | 1434 | 217 | <u>1427</u> | <u>218</u> | 1424 | 219 | 16 | 1360 | 229 | <u>1361</u> | <u>229</u> | 1361 | 229 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
Large pages were disabled for this run

Platform Notes

BIOS Settings:
Fan speed = full speed (Default = Energy Saving)
Data Reuse = Disabled (Default = Enabled)



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General Notes

Binaries compiled on RHEL5.5

The Acer AW2000h-AW170h F1, Gateway GW2000h-GW170h F1, Acer AW2000ht-AW170ht F1 and Gateway GW2000ht-GW170ht F1 are electronically equivalent. This result was measured on Gateway GW2000ht-GW170ht F1.

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

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Base Optimization Flags (Continued)

Fortran benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static`

Benchmarks using both Fortran and C:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias`

Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m64`

`482.sphinx3:icc -m32`

C++ benchmarks (except as noted below):

`icpc -m64`

`450.soplex:icpc -m32`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
 416.gamess: `-DSPEC_CPU_LP64`
 433.milc: `-DSPEC_CPU_LP64`
 434.zeusmp: `-DSPEC_CPU_LP64`
 435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
 436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
 437.leslie3d: `-DSPEC_CPU_LP64`
 444.namd: `-DSPEC_CPU_LP64`
 447.dealII: `-DSPEC_CPU_LP64`
 453.povray: `-DSPEC_CPU_LP64`
 454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
 459.GemsFDTD: `-DSPEC_CPU_LP64`
 465.tonto: `-DSPEC_CPU_LP64`
 470.lbm: `-DSPEC_CPU_LP64`
 481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`



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Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-ansi-alias -opt-prefetch -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

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Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-static -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 21:32:05 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 8 July 2011.