



SPEC® CINT2006 Result

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ScaleMP

SPECint_rate2006 = Not Run

vSMP Foundation (Intel Xeon X5650, 2.66 GHz)

SPECint_rate_base2006 = 13600

CPU2006 license: 2929

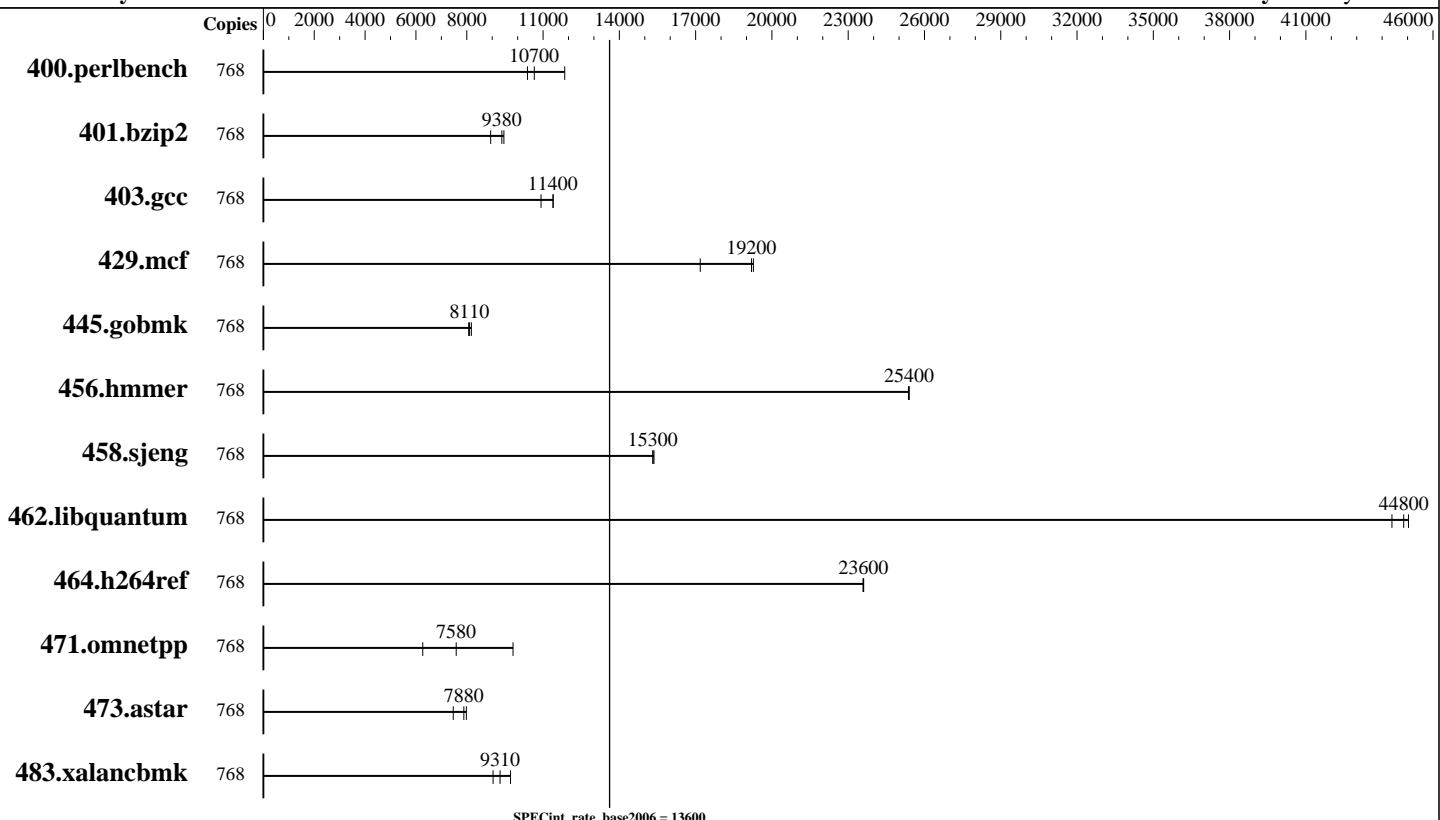
Test sponsor: ScaleMP

Tested by: ScaleMP

Test date: Jun-2011

Hardware Availability: Mar-2010

Software Availability: May-2011



Hardware

CPU Name: Intel Xeon X5650
 CPU Characteristics: Intel Turbo Boost Technology is not enabled
 CPU MHz: 2667
 FPU: Integrated
 CPU(s) enabled: 768 cores, 128 chips, 6 cores/chip
 CPU(s) orderable: 2 to 64 blades with 2 chips per blade
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: 540 GB I+D off chip per system
 Memory: 6 TB (64 x 12 x 8 GB 2Rx4 PC3-10600R-9, ECC)
 Disk Subsystem: 6 TB ramfs
 Other Hardware: None

Software

Operating System: CentOS release 5.5 (Final)
 Compiler: Kernel: 2.6.32.19-13.vSMP
 Auto Parallel: Intel C Compiler for applications running on IA-32, Version 11.1.074
 File System: Intel Fortran Compiler for applications running on IA-32, Version 11.1.074
 System State: No
 Base Pointers: xfs
 Peak Pointers: Multi-user, run level 3
 Other Software: 32-bit
 Not Applicable
 Hoard (libhoard) 3.7.1, released Feb. 08, 2008
 ScaleMP vSMP Foundation 3.5.175.15, released May. 30, 2011



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Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|----------------|--------|------------|--------------|------------|--------------|------------|--------------|--------|---------|-------|---------|-------|---------|-------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 768 | 704 | 10700 | 722 | 10400 | 633 | 11900 | | | | | | | |
| 401.bzip2 | 768 | 790 | 9380 | 783 | 9460 | 829 | 8940 | | | | | | | |
| 403.gcc | 768 | 542 | 11400 | 543 | 11400 | 566 | 10900 | | | | | | | |
| 429.mcf | 768 | 408 | 17200 | 363 | 19300 | 365 | 19200 | | | | | | | |
| 445.gobmk | 768 | 993 | 8110 | 998 | 8070 | 985 | 8180 | | | | | | | |
| 456.hammer | 768 | 282 | 25400 | 282 | 25400 | 282 | 25400 | | | | | | | |
| 458.sjeng | 768 | 607 | 15300 | 607 | 15300 | 605 | 15400 | | | | | | | |
| 462.libquantum | 768 | 355 | 44800 | 359 | 44400 | 353 | 45000 | | | | | | | |
| 464.h264ref | 768 | 720 | 23600 | 720 | 23600 | 721 | 23600 | | | | | | | |
| 471.omnetpp | 768 | 489 | 9820 | 633 | 7580 | 766 | 6270 | | | | | | | |
| 473.astar | 768 | 675 | 7980 | 722 | 7470 | 684 | 7880 | | | | | | | |
| 483.xalancbmk | 768 | 569 | 9310 | 545 | 9720 | 587 | 9030 | | | | | | | |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

```
ramfs file-system setup:  
mkdir /ramfs  
mount /dev/ram -t ramfs /ramfs -o noatime
```

Platform Notes

BIOS Settings

Hyper-Threading Technology set to ON. Disabled by vSMP Foundation.

ScaleMP

vSMP Foundation: 3.5.175.15, released May. 30, 2011
See <http://www.scalemp.com>

Hardware Details:

System was aggregated using 64 X HP BL490c G6 blades.
The servers were connected with Mellanox InfiniBand QDR and a QDR switch.

CPU Characteristics:

Intel Turbo Boost Technology not enabled:
As the prerequisites listed below for enablement of this technology did not exist.

- Hardware: Enabling Turbo Boost Technology require BIOS setting.
- Software: OS needs to be ACPI-aware and set P0 power state.



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Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
/ramfs/SPEC2006/cpu2006/libboard.so

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revH.20110629.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revH.20110629.xml>



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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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