SPEC® CFP2006 Result
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Dell Inc.

PowerEdge T110 II (Intel Xeon E3-1220, 3.10 GHz)

$SPECfp^{®}\_rate2006 =$ 101

$SPECfp\_rate\_base2006 =$ 104
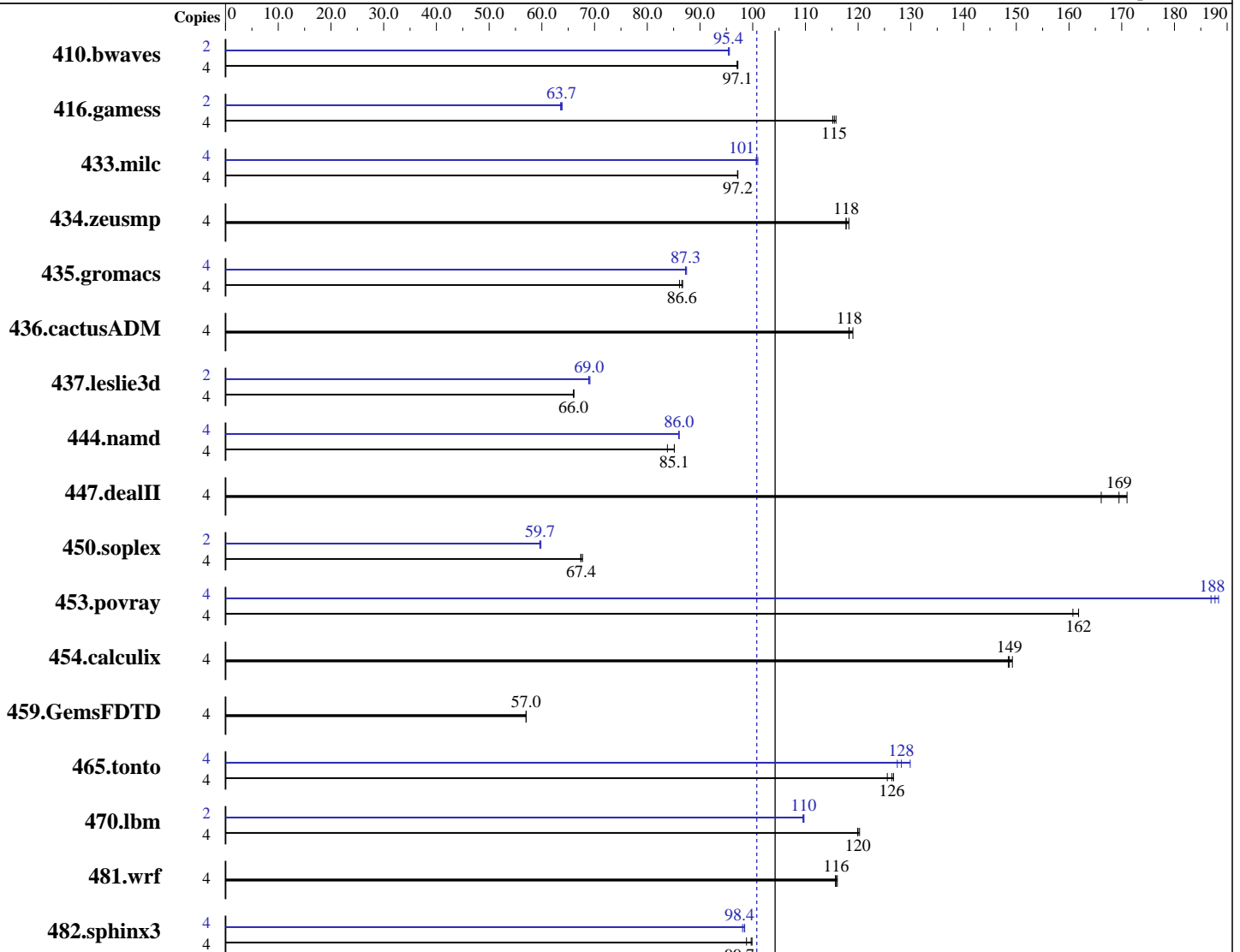
CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Mar-2011
Hardware Availability: May-2011
Software Availability: Apr-2011

SPECfp_rate_base2006 = 104
SPECfp_rate2006 = 101

Hardware

CPU Name:	Intel Xeon E3-1220
CPU Characteristics:	Intel Turbo Boost Technology up to 3.40 GHz
CPU MHz:	3100
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit

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L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	8 GB (4 x 2 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem:	1 x 146 GB 15000 RPM SAS
Other Hardware:	None

Peak Pointers:	32/64-bit
Other Software:	None

Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	559	97.2	**560**	**97.1**	560	97.0	2	**285**	**95.4**	284	95.6	285	95.4
416.gamess	4	680	115	676	116	**678**	**115**	2	616	63.6	613	63.8	**615**	**63.7**
433.milc	4	378	97.2	**378**	**97.2**	378	97.1	4	364	101	365	101	**364**	**101**
434.zeusmp	4	308	118	**309**	**118**	309	118	4	308	118	**309**	**118**	309	118
435.gromacs	4	**330**	**86.6**	332	86.1	329	86.7	4	**327**	**87.3**	327	87.2	327	87.5
436.cactusADM	4	402	119	**404**	**118**	404	118	4	402	119	**404**	**118**	404	118
437.leslie3d	4	570	66.0	569	66.1	**570**	**66.0**	2	273	68.9	**273**	**69.0**	272	69.1
444.namd	4	**377**	**85.1**	383	83.8	377	85.1	4	373	86.0	373	86.0	**373**	**86.0**
447.dealII	4	275	166	268	171	**270**	**169**	4	275	166	268	171	**270**	**169**
450.soplex	4	495	67.4	493	67.7	**495**	**67.4**	2	280	59.6	**280**	**59.7**	279	59.8
453.povray	4	132	162	**132**	**162**	132	161	4	113	188	**113**	**188**	114	187
454.calculix	4	222	148	221	149	**222**	**149**	4	222	148	221	149	**222**	**149**
459.GemsFDTD	4	744	57.0	**744**	**57.0**	745	57.0	4	744	57.0	**744**	**57.0**	745	57.0
465.tonto	4	314	126	**311**	**126**	311	127	4	309	127	303	130	**307**	**128**
470.lbm	4	457	120	**458**	**120**	458	120	2	250	110	251	110	**251**	**110**
481.wrf	4	385	116	**386**	**116**	386	116	4	385	116	**386**	**116**	386	116
482.sphinx3	4	789	98.8	781	99.9	**782**	**99.7**	4	792	98.5	795	98.1	**792**	**98.4**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
 'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
 'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
 echo 3600> /proc/sys/vm/nr_hugepages
 export HUGETLB_MORECORE=yes
 export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

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$SPECfp\_rate2006 = \quad 101$

$SPECfp\_rate\_base2006 = \quad 104$

Platform Notes

```
Power Management = Maximum Performance (Default = Active Power Controller)
```

General Notes

```
The Dell PowerEdge T110 II and
the Bull NovaScale T810B F2 models are electronically equivalent.
The results have been measured on a Dell PowerEdge T110 II model
Binaries were compiled on RHEL5.5
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
  436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
   437.leslie3d: -DSPEC_CPU_LP64
      444.namd: -DSPEC_CPU_LP64
     447.dealII: -DSPEC_CPU_LP64
     450.soplex: -DSPEC_CPU_LP64
     453.povray: -DSPEC_CPU_LP64
   454.calculix: -DSPEC_CPU_LP64 -nofor_main
  459.GemsFDTD: -DSPEC_CPU_LP64
     465.tonto: -DSPEC_CPU_LP64
      470.lbm: -DSPEC_CPU_LP64
      481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
   482.sphinx3: -DSPEC_CPU_LP64
```

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CPU2006 license: 55

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Test date: Mar-2011

Hardware Availability: May-2011

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Base Optimization Flags

C benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -ansi-alias
```

C++ benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -ansi-alias
```

Fortran benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static
```

Benchmarks using both Fortran and C:
```
-xAVX -ipo -O3 -no-prec-div -static -ansi-alias
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc   -m64
```

    482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
```
icpc  -m64
```

     450.soplex: `icpc -m32`

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Peak Portability Flags

```
     410.bwaves: -DSPEC_CPU_LP64
     416.gamess: -DSPEC_CPU_LP64
       433.milc: -DSPEC_CPU_LP64
     434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
  436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
      444.namd: -DSPEC_CPU_LP64
     447.dealII: -DSPEC_CPU_LP64
     453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
   459.GemsFDTD: -DSPEC_CPU_LP64
       465.tonto: -DSPEC_CPU_LP64
```

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Peak Portability Flags (Continued)

```
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
          -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
         -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
         -ansi-alias -opt-prefetch -auto-ilp32

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2
```

C++ benchmarks:

```
444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
          -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
          -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
            -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
            -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT
```

Fortran benchmarks:

```
410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
            -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div
              -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

459.GemsFDTD: basepeak = yes
```

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Peak Optimization Flags (Continued)

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
   -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
   -inline-calloc -opt-malloc-options=3
   -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
   -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
   -static -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.xml

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