



# SPEC® CINT2006 Result

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Fujitsu

**SPECint®\_rate2006 = 82.4**

PRIMERGY TX120 S3, Intel Core i3-2120, 3.30 GHz

**SPECint\_rate\_base2006 = 79.0**

CPU2006 license: 19

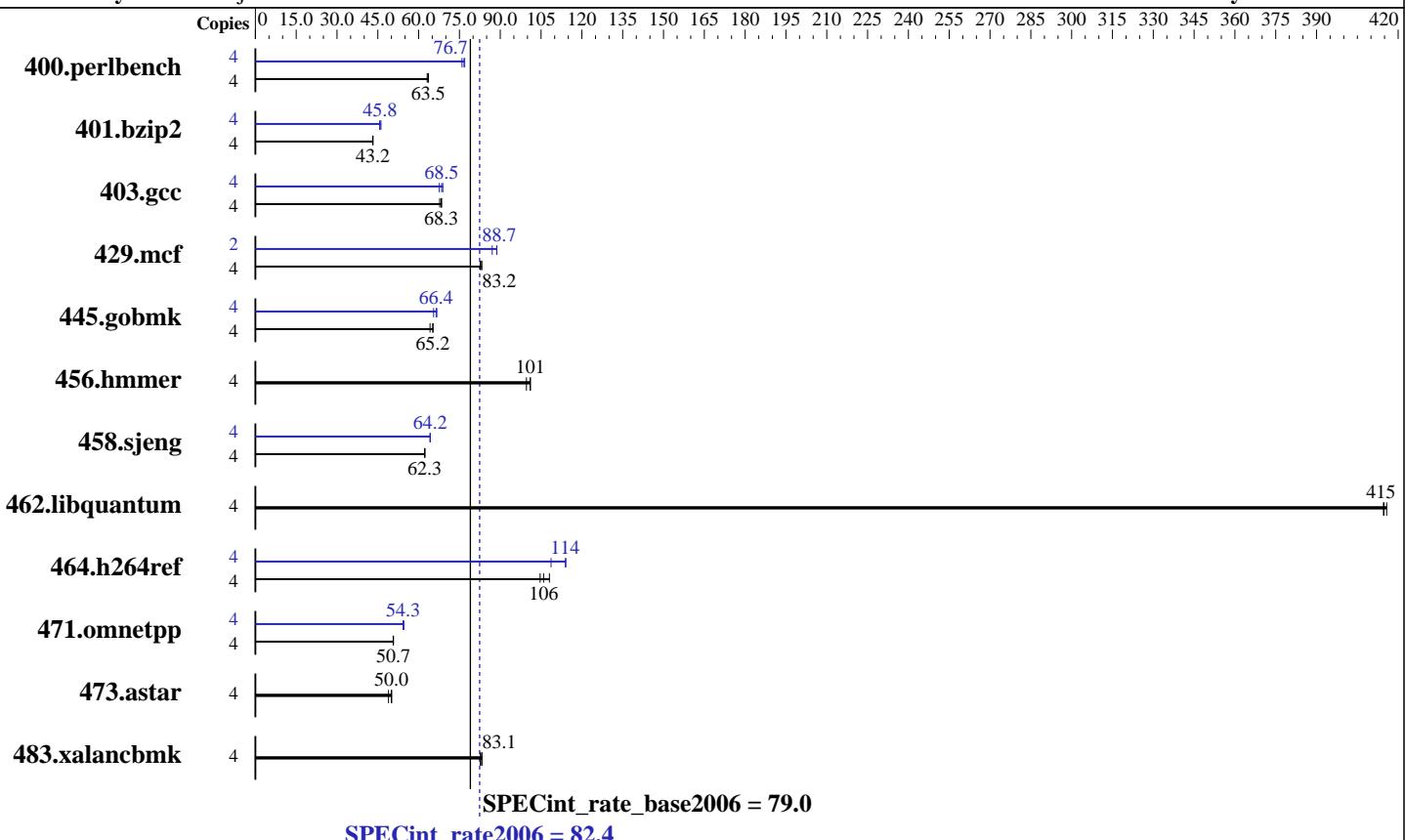
**Test date:** May-2011

**Test sponsor:** Fujitsu

**Hardware Availability:** Jun-2011

**Tested by:** Fujitsu

**Software Availability:** Jan-2011



## Hardware

CPU Name:	Intel Core i3-2120
CPU Characteristics:	
CPU MHz:	3300
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	3 MB I+D on chip per chip
Other Cache:	None
Memory:	8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC)
Disk Subsystem:	1 x SATA, 300 GB, 7200 RPM
Other Hardware:	None

## Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	615	63.6	619	63.1	<b>615</b>	<b>63.5</b>	4	508	76.9	<b>510</b>	<b>76.7</b>	515	75.9
401.bzip2	4	895	43.1	893	43.2	<b>894</b>	<b>43.2</b>	4	836	46.2	845	45.7	<b>842</b>	<b>45.8</b>
403.gcc	4	<b>472</b>	<b>68.3</b>	470	68.5	475	67.7	4	<b>470</b>	<b>68.5</b>	467	68.9	476	67.6
429.mcf	4	439	83.2	<b>439</b>	<b>83.2</b>	442	82.6	2	210	87.0	<b>206</b>	<b>88.7</b>	205	88.8
445.gobmk	4	654	64.2	643	65.3	<b>644</b>	<b>65.2</b>	4	641	65.5	<b>632</b>	<b>66.4</b>	629	66.7
456.hammer	4	<b>369</b>	<b>101</b>	375	99.6	369	101	4	<b>369</b>	<b>101</b>	375	99.6	369	101
458.sjeng	4	<b>777</b>	<b>62.3</b>	778	62.2	777	62.3	4	753	64.3	<b>753</b>	<b>64.2</b>	754	64.2
462.libquantum	4	<b>200</b>	<b>415</b>	199	416	200	415	4	<b>200</b>	<b>415</b>	199	416	200	415
464.h264ref	4	<b>836</b>	<b>106</b>	846	105	819	108	4	776	114	815	109	<b>777</b>	<b>114</b>
471.omnetpp	4	493	50.7	494	50.6	<b>493</b>	<b>50.7</b>	4	460	54.3	458	54.6	<b>460</b>	<b>54.3</b>
473.astar	4	574	48.9	<b>562</b>	<b>50.0</b>	561	50.1	4	574	48.9	<b>562</b>	<b>50.0</b>	561	50.1
483.xalancbmk	4	331	83.3	<b>332</b>	<b>83.1</b>	334	82.7	4	331	83.3	<b>332</b>	<b>83.1</b>	334	82.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
Large pages were not enabled for this run

## General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>

Binaries were compiled on RHEL5.5

This result was measured on the PRIMERGY TX140 S1. The PRIMERGY TX120 S3 and the PRIMERGY TX140 S1 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:

icc -m32

C++ benchmarks:

icpc -m32



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch  
-B /usr/share/libhugetlbfsl -Wl,-hugetlbfsl-link=BDT
```

C++ benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfsl -Wl,-hugetlbfsl-link=BDT
```

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

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## Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2)  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
-auto-ilp32 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xAVX -ipo -O3 -no-prec-div  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias  
-auto-ilp32

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

456.hmmr: basepeak = yes

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll14  
-auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll12  
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias  
-opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/smartheap -lsmartheap

473.astar: basepeak = yes

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## Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 18:32:39 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 7 June 2011.