



SPEC® CFP2006 Result

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Dell Inc.

SPECfp®2006 = 59.0

PowerEdge R910 (Intel Xeon E7-8837, 2.67 GHz)

SPECfp_base2006 = 54.4

CPU2006 license: 55

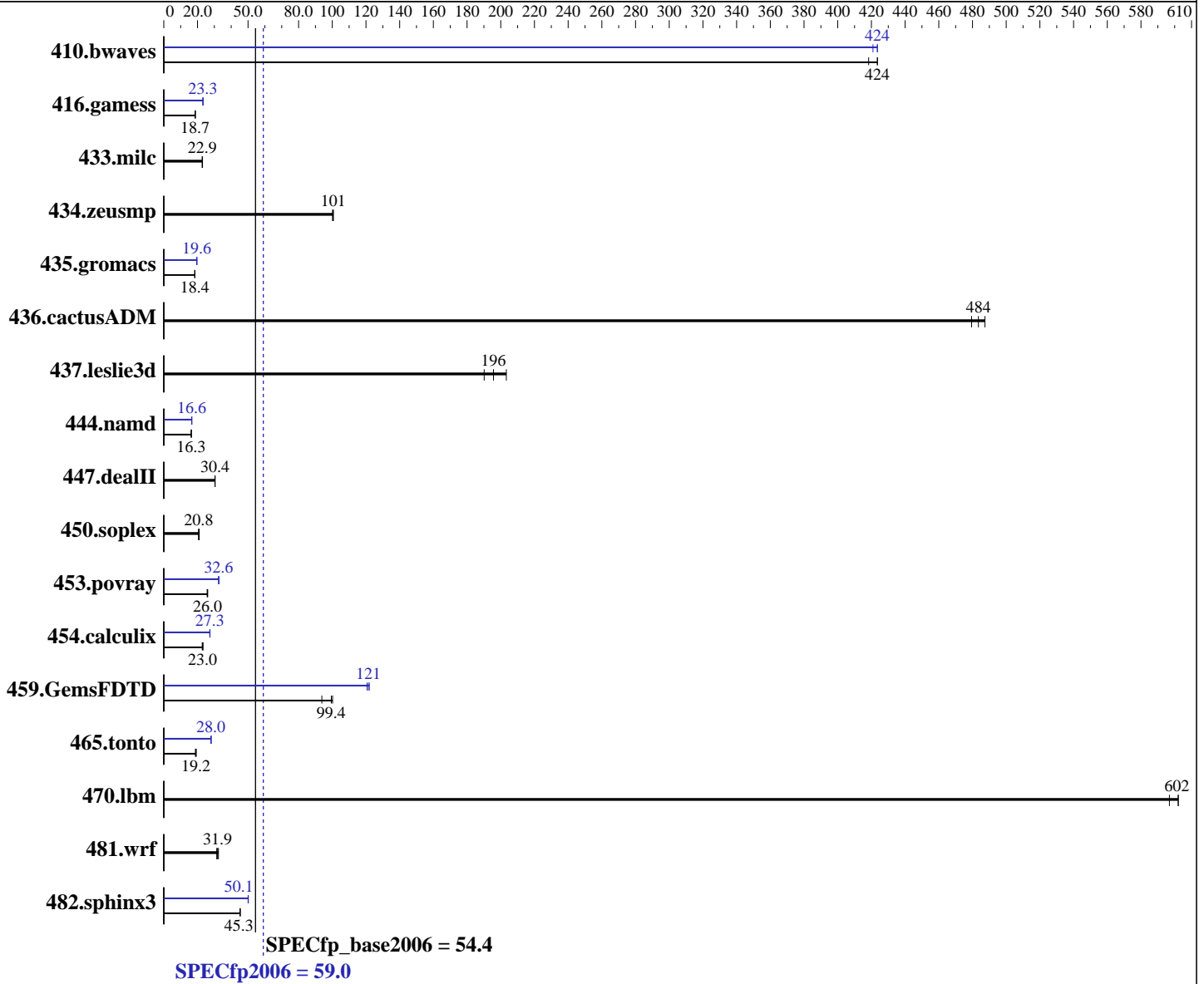
Test date: Mar-2011

Test sponsor: Dell Inc.

Hardware Availability: Apr-2011

Tested by: Dell Inc.

Software Availability: Apr-2011



Hardware

CPU Name: Intel Xeon E7-8837
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
 CPU MHz: 2667
 FPU: Integrated
 CPU(s) enabled: 32 cores, 4 chips, 8 cores/chip
 CPU(s) orderable: 2,4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0 Update 3
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

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L3 Cache: 24 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (64 x 8 GB 4Rx8 PC3-8500R-7, ECC)
Disk Subsystem: 1 x 500 GB 7200 RPM SAS 6Gb
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	32.5	418	32.1	424	<u>32.1</u>	<u>424</u>	32.1	424	<u>32.1</u>	<u>424</u>	32.3	421
416.gamess	1048	18.7	1050	18.7	<u>1049</u>	<u>18.7</u>	841	23.3	<u>842</u>	<u>23.3</u>	843	23.2
433.milc	403	22.8	399	23.0	<u>402</u>	<u>22.9</u>	403	22.8	399	23.0	<u>402</u>	<u>22.9</u>
434.zeusmp	90.9	100	<u>90.5</u>	<u>101</u>	90.5	101	90.9	100	<u>90.5</u>	<u>101</u>	90.5	101
435.gromacs	387	18.5	389	18.4	<u>387</u>	<u>18.4</u>	363	19.7	<u>364</u>	<u>19.6</u>	366	19.5
436.cactusADM	24.5	487	<u>24.7</u>	<u>484</u>	24.9	479	24.5	487	<u>24.7</u>	<u>484</u>	24.9	479
437.leslie3d	<u>48.0</u>	<u>196</u>	49.4	190	46.2	203	<u>48.0</u>	<u>196</u>	49.4	190	46.2	203
444.namd	492	16.3	492	16.3	<u>492</u>	<u>16.3</u>	<u>483</u>	<u>16.6</u>	483	16.6	483	16.6
447.dealII	376	30.4	<u>376</u>	<u>30.4</u>	376	30.4	376	30.4	<u>376</u>	<u>30.4</u>	376	30.4
450.soplex	396	21.0	<u>401</u>	<u>20.8</u>	405	20.6	396	21.0	<u>401</u>	<u>20.8</u>	405	20.6
453.povray	204	26.0	<u>205</u>	<u>26.0</u>	207	25.7	<u>163</u>	<u>32.6</u>	163	32.7	164	32.5
454.calculix	354	23.3	<u>358</u>	<u>23.0</u>	362	22.8	<u>302</u>	<u>27.3</u>	301	27.4	302	27.3
459.GemsFDTD	113	93.8	106	100	<u>107</u>	<u>99.4</u>	87.0	122	88.0	121	<u>87.4</u>	<u>121</u>
465.tonto	513	19.2	525	18.7	<u>514</u>	<u>19.2</u>	351	28.0	<u>351</u>	<u>28.0</u>	351	28.1
470.lbm	<u>22.8</u>	<u>602</u>	22.8	602	23.0	597	<u>22.8</u>	<u>602</u>	22.8	602	23.0	597
481.wrf	344	32.4	356	31.4	<u>350</u>	<u>31.9</u>	344	32.4	356	31.4	<u>350</u>	<u>31.9</u>
482.sphinx3	430	45.4	<u>430</u>	<u>45.3</u>	431	45.3	388	50.2	390	50.0	<u>389</u>	<u>50.1</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

Platform Notes

BIOS Settings:
Power Management = Maximum Performance (Default = Active Power Controller)
Node Interleaving = Enabled (Default = Disabled)



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General Notes

The Dell PowerEdge R910 and the Bull NovaScale R480 F2 models are electronically equivalent. The results have been measured on a Dell PowerEdge R910 model.
OMP_NUM_THREADS set to number of cores
Binaries were compiled on RHEL5.5

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

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Base Optimization Flags (Continued)

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

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Peak Optimization Flags (Continued)

447.dealll: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -parallel
-static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.xml>



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