



# SPEC® CINT2006 Result

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## IBM Corporation

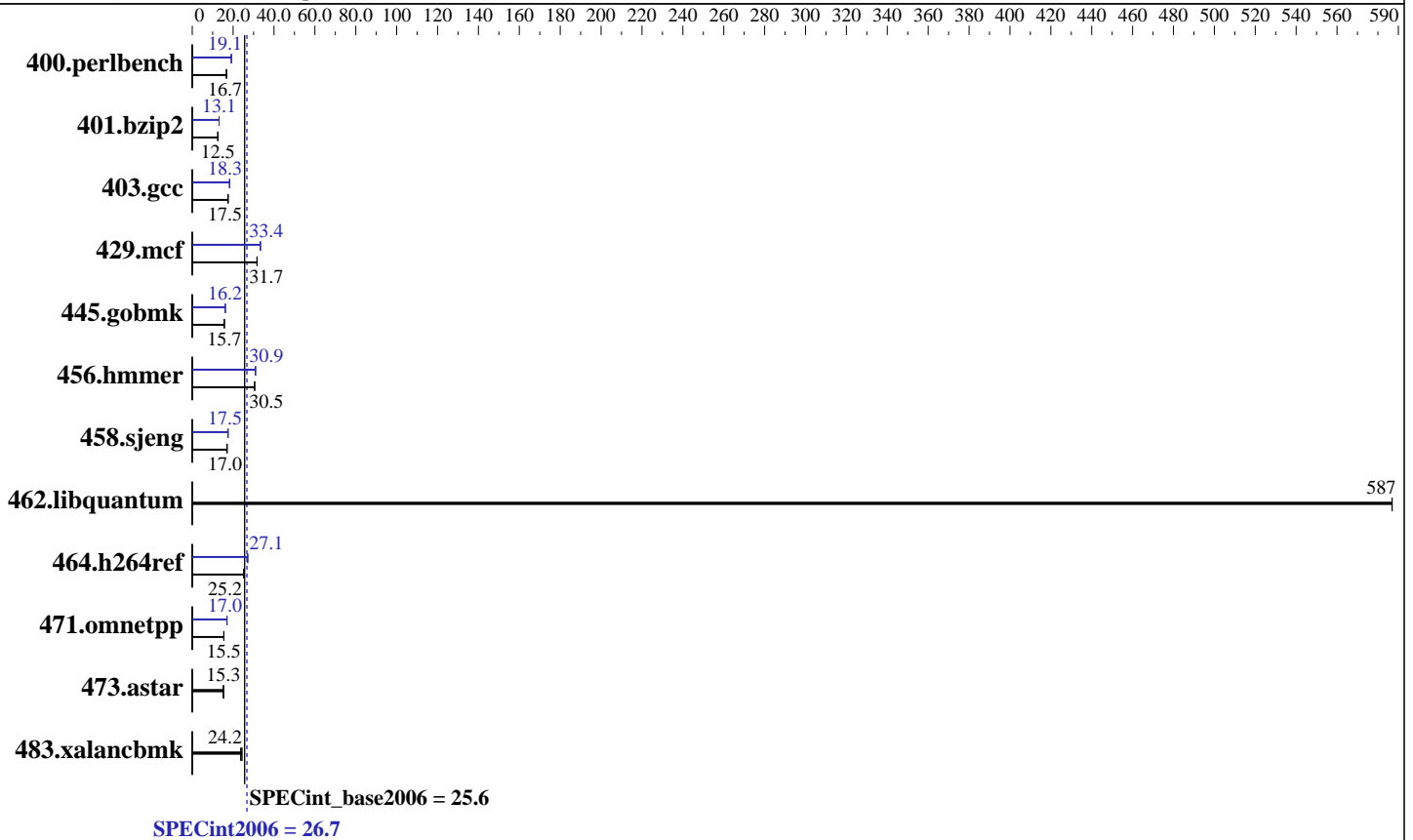
**SPECint®2006 = 26.7**

### IBM System x3500 M3 (Intel Xeon E5606)

**SPECint\_base2006 = 25.6**

CPU2006 license: 11  
Test sponsor: IBM Corporation  
Tested by: IBM Corporation

Test date: Apr-2011  
Hardware Availability: Feb-2011  
Software Availability: Jan-2011



**Hardware**

CPU Name: Intel Xeon E5606  
 CPU Characteristics: 2133  
 CPU MHz: Integrated  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 96 GB (12 x 8 GB 2Rx4 PC3-10600R-9, ECC, running at 1067 MHz)  
 Disk Subsystem: 1 x 146 GB SAS, 15000 RPM  
 Other Hardware: None

**Software**

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86\_64), Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	583	16.8	<b>584</b>	<b>16.7</b>	584	16.7	511	19.1	510	19.2	<b>511</b>	<b>19.1</b>
401.bzip2	770	12.5	<b>770</b>	<b>12.5</b>	770	12.5	735	13.1	<b>734</b>	<b>13.1</b>	734	13.1
403.gcc	458	17.6	461	17.5	<b>459</b>	<b>17.5</b>	<b>441</b>	<b>18.3</b>	441	18.3	441	18.3
429.mcf	<b>288</b>	<b>31.7</b>	288	31.7	288	31.7	273	33.4	<b>273</b>	<b>33.4</b>	274	33.3
445.gobmk	669	15.7	<b>669</b>	<b>15.7</b>	669	15.7	<b>649</b>	<b>16.2</b>	649	16.2	649	16.2
456.hammer	<b>306</b>	<b>30.5</b>	306	30.5	306	30.5	302	30.9	<b>302</b>	<b>30.9</b>	301	30.9
458.sjeng	711	17.0	710	17.0	<b>710</b>	<b>17.0</b>	691	17.5	691	17.5	<b>691</b>	<b>17.5</b>
462.libquantum	35.3	587	<b>35.3</b>	<b>587</b>	35.3	587	35.3	587	<b>35.3</b>	<b>587</b>	35.3	587
464.h264ref	880	25.1	<b>878</b>	<b>25.2</b>	877	25.2	816	27.1	<b>816</b>	<b>27.1</b>	816	27.1
471.omnetpp	403	15.5	<b>404</b>	<b>15.5</b>	405	15.4	<b>368</b>	<b>17.0</b>	367	17.0	368	17.0
473.astar	458	15.3	<b>460</b>	<b>15.3</b>	463	15.2	458	15.3	<b>460</b>	<b>15.3</b>	463	15.2
483.xalancbmk	283	24.3	292	23.7	<b>285</b>	<b>24.2</b>	283	24.3	292	23.7	<b>285</b>	<b>24.2</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

## Platform Notes

CPU C-State enabled in BIOS  
 Data Reuse disabled in BIOS  
 Demand Scrub disabled in BIOS

## General Notes

Binaries compiled on RHEL5.5  
 OMP\_NUM\_THREADS set to number of cores

## Base Compiler Invocation

C benchmarks:  
 icc -m64

C++ benchmarks:  
 icpc -m64



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## Base Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

## Base Optimization Flags

C benchmarks:

```

-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

```

C++ benchmarks:

```

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap64
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

```

## Base Other Flags

C benchmarks:

```

403.gcc: -Dalloca=_alloca

```

## Peak Compiler Invocation

C benchmarks (except as noted below):

```

icc -m64

400.perlbench: icc -m32

429.mcf: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

```

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## Peak Compiler Invocation (Continued)

C++ benchmarks (except as noted below):

icpc -m64

471.omnetpp: icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

473.astar: -DSPEC\_CPU\_LP64

483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32  
-opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc  
-opt-malloc-options=3 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-auto-ilp32 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

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## Peak Optimization Flags (Continued)

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs  
-L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.20110420.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.20110420.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.  
Report generated on Wed Jul 23 20:14:47 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 24 May 2011.