SPEC® CFP2006 Result
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Intel Corporation

$SPECfp®\_rate2006 = \quad 104$

Intel DH67BLB3 Motherboard (Intel Core i5-2500K)

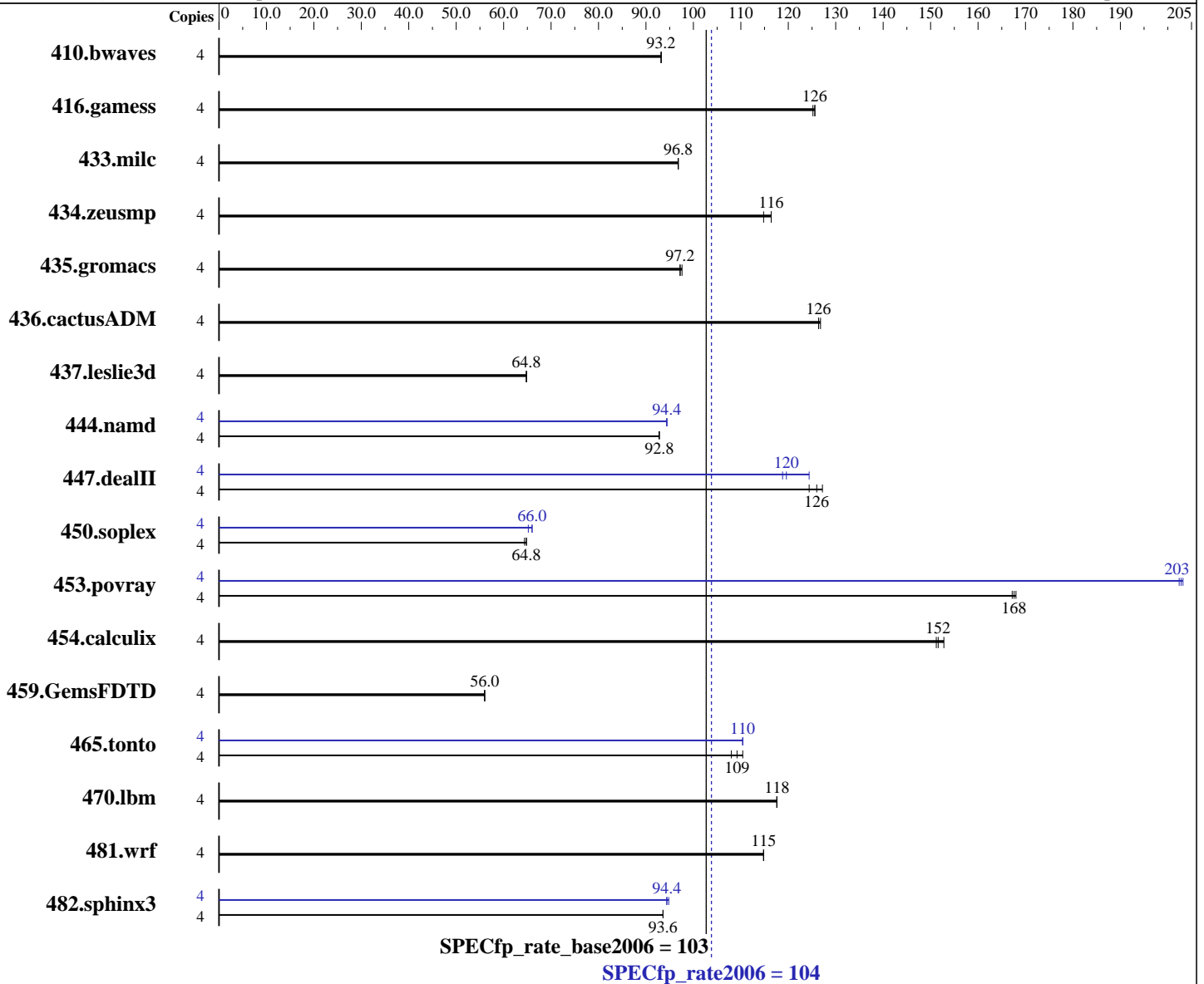$SPECfp\_rate\_base2006 = \quad 103$

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Apr-2011
Hardware Availability: Mar-2011
Software Availability: Apr-2011

Copies		
410.bwaves	4	93.2
416.gamess	4	126
433.milc	4	96.8
434.zeusmp	4	116
435.gromacs	4	97.2
436.cactusADM	4	126
437.leslie3d	4	64.8
444.namd	4 / 4	94.4 / 92.8
447.dealII	4 / 4	120 / 126
450.soplex	4 / 4	66.0 / 64.8
453.povray	4 / 4	203 / 168
454.calculix	4	152
459.GemsFDTD	4	56.0
465.tonto	4 / 4	110 / 109
470.lbm	4	118
481.wrf	4	115
482.sphinx3	4 / 4	94.4 / 93.6

SPECfp_rate_base2006 = 103

SPECfp_rate2006 = 104

Hardware

CPU Name: Intel Core i5-2500K
CPU Characteristics: Intel Turbo Boost Technology up to 3.7 GHz
CPU MHz: 3300
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
Continued on next page

Software

Operating System: Windows 7 Ultimate (64-bit)
Compiler: Intel C++ Compiler XE for Intel64
Version 12.0.3.163 Build 20110217
Intel Visual Fortran Compiler XE for Intel64
Version 12.0.3.163 Build 20110217
Microsoft Visual Studio 2008 Professional SP1
(for libraries)
Auto Parallel: No
File System: NTFS
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L3 Cache:	6 MB I+D on chip per chip
Other Cache:	None
Memory:	8 GB (2 x 4 GB 2Rx8 PC3-10600U-9)
Disk Subsystem:	Seagate 1 TB SATA, 7200 RPM
Other Hardware:	None

System State:	Default
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	SmartHeap Library Version 9.01 from http://www.microquill.com/

Results Table

Benchmark		Base							Peak					
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	583	93.2	**583**	**93.2**	582	93.2	4	583	93.2	**583**	**93.2**	582	93.2
416.gamess	4	625	125	624	126	**624**	**126**	4	625	125	624	126	**624**	**126**
433.milc	4	380	96.8	**380**	**96.8**	380	96.8	4	380	96.8	**380**	**96.8**	380	96.8
434.zeusmp	4	**313**	**116**	313	116	317	115	4	**313**	**116**	313	116	317	115
435.gromacs	4	293	97.6	**294**	**97.2**	294	97.2	4	293	97.6	**294**	**97.2**	294	97.2
436.cactusADM	4	379	126	377	127	**379**	**126**	4	379	126	377	127	**379**	**126**
437.leslie3d	4	579	64.8	579	64.8	**579**	**64.8**	4	579	64.8	579	64.8	**579**	**64.8**
444.namd	4	**346**	**92.8**	346	92.8	346	92.8	4	**341**	**94.4**	341	94.4	340	94.4
447.dealII	4	367	124	360	127	**363**	**126**	4	368	124	385	119	**382**	**120**
450.soplex	4	517	64.4	516	64.8	**516**	**64.8**	4	511	65.2	507	66.0	**507**	**66.0**
453.povray	4	127	167	127	168	**127**	**168**	4	**105**	**203**	105	203	105	202
454.calculix	4	218	151	**218**	**152**	216	153	4	218	151	**218**	**152**	216	153
459.GemsFDTD	4	**758**	**56.0**	757	56.0	759	56.0	4	**758**	**56.0**	757	56.0	759	56.0
465.tonto	4	357	110	**361**	**109**	364	108	4	357	110	357	110	**357**	**110**
470.lbm	4	468	118	468	118	**468**	**118**	4	468	118	468	118	**468**	**118**
481.wrf	4	389	115	390	115	**389**	**115**	4	389	115	390	115	**389**	**115**
482.sphinx3	4	**833**	**93.6**	833	93.6	832	93.6	4	827	94.4	**827**	**94.4**	824	94.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
    The start command with the /affinity switch was used to bind processes to cores

General Notes

    Tested systems can be used with Shin-G ATX case,
    PC Power and Cooling 1200W power supply

Base Compiler Invocation

    C benchmarks:
        icl -Qvc9 -Qstd=c99

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Base Compiler Invocation (Continued)

C++ benchmarks:
```
icl -Qvc9
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc9 -Qstd=c99 ifort
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_P64 -names:lowercase
    416.gamess: -DSPEC_CPU_P64
      433.milc: -DSPEC_CPU_P64
    434.zeusmp: -DSPEC_CPU_P64
    435.gromacs: -DSPEC_CPU_P64
  436.cactusADM: -DSPEC_CPU_P64 /names:lowercase /assume:underscore
    437.leslie3d: -DSPEC_CPU_P64
      444.namd: -DSPEC_CPU_P64 /TP
     447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
    450.soplex: -DSPEC_CPU_P64
    453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
    454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER /names:lowercase
  459.GemsFDTD: -DSPEC_CPU_P64
     465.tonto: -DSPEC_CPU_P64
      470.lbm: -DSPEC_CPU_P64
       481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
    482.sphinx3: -DSPEC_CPU_P64
```

Base Optimization Flags

C benchmarks:
```
-QxAVX -Qipo -O3 -Qprec-div- -Qansi-alias -Qauto-ilp32 /F1000000000
        -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-QxAVX -Qipo -O3 -Qprec-div- -Qansi-alias -Qcxx-features
-Qauto-ilp32 /F1000000000 shlW64M.lib          -link /FORCE:MULTIPLE
```

Fortran benchmarks:
```
-QxAVX -Qipo -O3 -Qprec-div- -Qansi-alias /F1000000000
        -link /FORCE:MULTIPLE
```

Benchmarks using both Fortran and C:
```
-QxAVX -Qipo -O3 -Qprec-div- -Qansi-alias -Qauto-ilp32 /F1000000000
        -link /FORCE:MULTIPLE
```

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Peak Compiler Invocation

C benchmarks:
```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:
```
icl -Qvc9
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc9 -Qstd=c99 ifort
```

Peak Portability Flags

```
Same as Base Portability Flags
```

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: -QxAVX -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
-Qauto-ilp32 /F1000000000   -link /FORCE:MULTIPLE

C++ benchmarks:

444.namd: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Oa -Qauto-ilp32 /F1000000000 shlW64M.lib
-link /FORCE:MULTIPLE

447.dealII: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qunroll2 -Qansi-alias -Qscalar-rep-
-Qauto-ilp32 /F1000000000 shlW64M.lib
-link /FORCE:MULTIPLE

450.soplex: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qauto-ilp32 /F1000000000 shlW64M.lib
-link /FORCE:MULTIPLE

453.povray: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32 /F1000000000
shlW64M.lib   -link /FORCE:MULTIPLE

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Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qunroll4 -Qauto /F1000000000
-link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.html
http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.xml
http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
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