



SPEC[®] CFP2006 Result

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Dell Inc.

SPECfp[®]2006 = 61.0

PowerEdge T710 (Intel Xeon X5675, 3.06 GHz)

SPECfp_base2006 = 57.8

CPU2006 license: 55

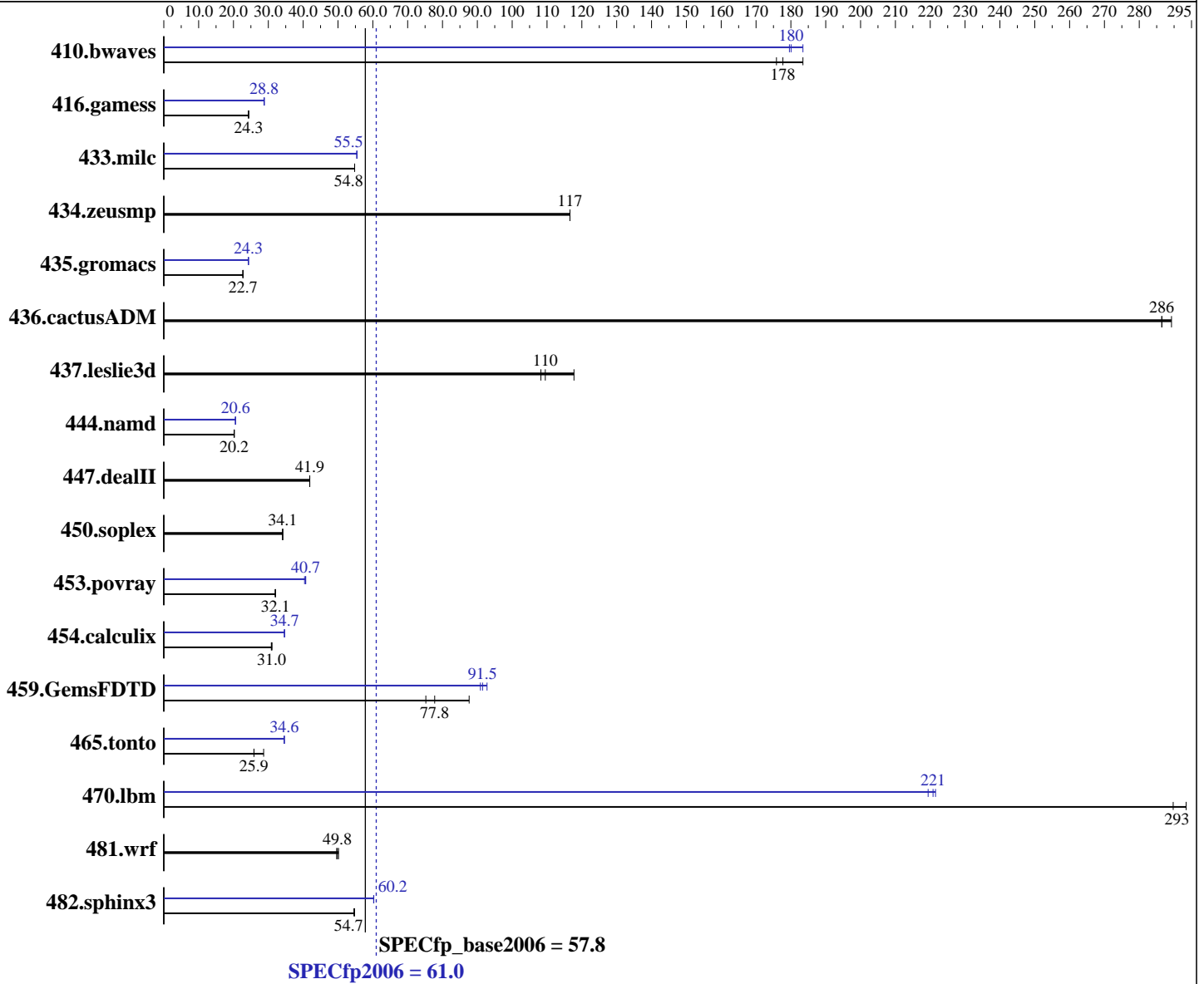
Test date: Mar-2011

Test sponsor: Dell Inc.

Hardware Availability: Feb-2011

Tested by: Dell Inc.

Software Availability: Apr-2011



Hardware

CPU Name: Intel Xeon X5675
 CPU Characteristics: Intel Turbo Boost Technology up to 3.46 GHz
 CPU MHz: 3067
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0 Update 3
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

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L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)
 Disk Subsystem: 1 x 146 GB 15000 RPM SAS
 Other Hardware: None

Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<u>76.5</u>	<u>178</u>	74.1	183	77.3	176	<u>75.5</u>	<u>180</u>	74.1	183	75.7	180
416.gamess	803	24.4	806	24.3	<u>805</u>	<u>24.3</u>	<u>679</u>	<u>28.8</u>	681	28.8	679	28.9
433.milc	<u>168</u>	<u>54.8</u>	168	54.8	168	54.8	166	55.4	166	55.5	<u>166</u>	<u>55.5</u>
434.zeusmp	78.1	117	<u>78.1</u>	<u>117</u>	78.1	117	78.1	117	<u>78.1</u>	<u>117</u>	78.1	117
435.gromacs	314	22.8	315	22.7	<u>314</u>	<u>22.7</u>	294	24.3	<u>294</u>	<u>24.3</u>	294	24.3
436.cactusADM	<u>41.7</u>	<u>286</u>	41.3	289	41.7	286	<u>41.7</u>	<u>286</u>	41.3	289	41.7	286
437.leslie3d	86.8	108	<u>85.8</u>	<u>110</u>	79.8	118	86.8	108	<u>85.8</u>	<u>110</u>	79.8	118
444.namd	397	20.2	<u>397</u>	<u>20.2</u>	397	20.2	<u>390</u>	<u>20.6</u>	390	20.6	390	20.6
447.dealII	273	41.9	<u>273</u>	<u>41.9</u>	273	41.9	273	41.9	<u>273</u>	<u>41.9</u>	273	41.9
450.soplex	<u>244</u>	<u>34.1</u>	244	34.1	245	34.1	<u>244</u>	<u>34.1</u>	244	34.1	245	34.1
453.povray	166	32.1	167	31.9	<u>166</u>	<u>32.1</u>	131	40.7	<u>131</u>	<u>40.7</u>	132	40.4
454.calculix	266	31.0	<u>266</u>	<u>31.0</u>	267	30.9	238	34.7	<u>238</u>	<u>34.7</u>	238	34.6
459.GemsFDTD	<u>136</u>	<u>77.8</u>	121	87.6	141	75.2	117	90.9	114	92.8	<u>116</u>	<u>91.5</u>
465.tonto	380	25.9	<u>380</u>	<u>25.9</u>	343	28.7	<u>285</u>	<u>34.6</u>	285	34.6	284	34.6
470.lbm	47.4	290	46.8	293	<u>46.8</u>	<u>293</u>	62.6	219	62.0	222	<u>62.2</u>	<u>221</u>
481.wrf	<u>224</u>	<u>49.8</u>	225	49.6	223	50.2	<u>224</u>	<u>49.8</u>	225	49.6	223	50.2
482.sphinx3	<u>356</u>	<u>54.7</u>	356	54.8	357	54.6	323	60.3	324	60.2	<u>324</u>	<u>60.2</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

Platform Notes

BIOS Settings:
 Power Management = Maximum Performance (Default = Active Power Controller)
 Data Reuse = Disabled (Default = Enabled)
 Logical Processor = Disabled (Default = Enabled)



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General Notes

OMP_NUM_THREADS set to number of cores
The Dell PowerEdge T710 and the Bull NovaScale T860 F2 models are electronically equivalent. The results have been measured on a Dell PowerEdge T710 model. Binaries were compiled on RHEL5.5

Base Compiler Invocation

C benchmarks:
icc -m64
C++ benchmarks:
icpc -m64
Fortran benchmarks:
ifort -m64
Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

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Base Optimization Flags (Continued)

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -parallel
-ansi-alias -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

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Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -parallel
-static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-alloc
-opt-malloc-options=3 -auto -unroll4
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110426.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110426.xml>

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