



SPEC® CINT2006 Result

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Bull SAS

SPECint®_rate2006 = 914

NovaScale R480 F2 (Intel Xeon E7-4850, 2.00 GHz)

SPECint_rate_base2006 = 851

CPU2006 license: 20

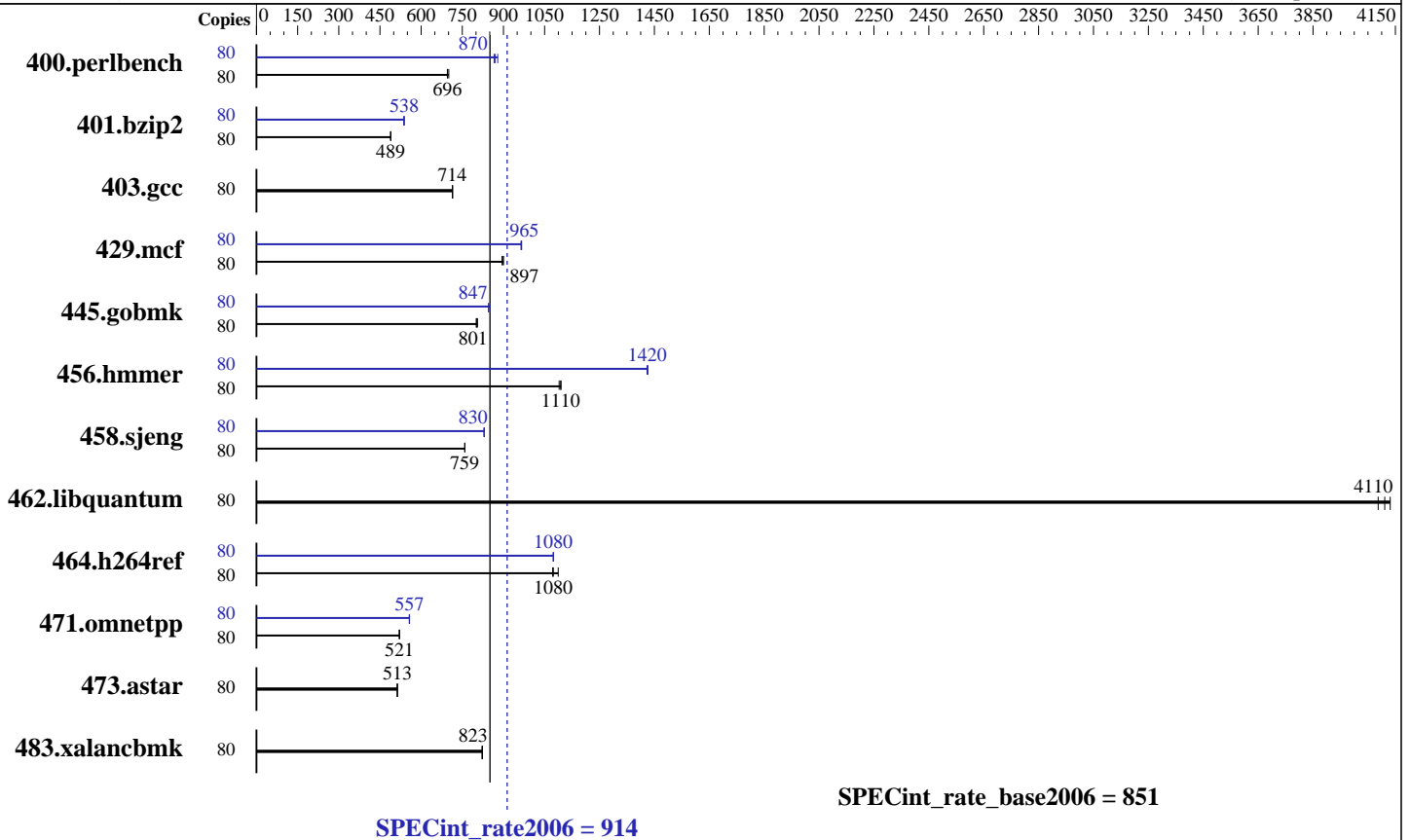
Test sponsor: Bull SAS

Tested by: Dell Inc.

Test date: Mar-2011

Hardware Availability: Apr-2011

Software Availability: Apr-2011



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Hardware

CPU Name: Intel Xeon E7-4850
 CPU Characteristics: Intel Turbo Boost Technology up to 2.40 GHz
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 40 cores, 4 chips, 10 cores/chip, 2 threads/core
 CPU(s) orderable: 2,4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 24 MB I+D on chip per chip
 Other Cache: None
 Memory: 512 GB (64 x 8 GB 4Rx8 PC3-8500R-7, ECC)
 Disk Subsystem: 1 x 500 GB 7200 RPM SAS 6Gb
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	80	<u>1123</u>	<u>696</u>	1115	701	1124	696	80	889	879	<u>899</u>	<u>870</u>	903	866
401.bzip2	80	<u>1578</u>	<u>489</u>	1579	489	1578	489	80	1438	537	<u>1434</u>	<u>538</u>	1434	538
403.gcc	80	<u>902</u>	<u>714</u>	901	715	902	714	80	<u>902</u>	<u>714</u>	901	715	902	714
429.mcf	80	<u>813</u>	<u>897</u>	810	900	815	895	80	755	966	757	964	<u>756</u>	<u>965</u>
445.gobmk	80	1047	801	1042	806	<u>1047</u>	<u>801</u>	80	<u>991</u>	<u>847</u>	993	845	985	852
456.hammer	80	677	1100	673	1110	<u>673</u>	<u>1110</u>	80	524	1420	<u>524</u>	<u>1420</u>	523	1430
458.sjeng	80	1274	760	1275	759	<u>1275</u>	<u>759</u>	80	<u>1167</u>	<u>830</u>	1166	830	1168	829
462.libquantum	80	406	4090	<u>403</u>	<u>4110</u>	401	4130	80	406	4090	<u>403</u>	<u>4110</u>	401	4130
464.h264ref	80	1610	1100	1640	1080	<u>1637</u>	<u>1080</u>	80	1637	1080	<u>1637</u>	<u>1080</u>	1635	1080
471.omnetpp	80	961	520	959	521	<u>959</u>	<u>521</u>	80	<u>897</u>	<u>557</u>	896	558	898	557
473.astar	80	<u>1094</u>	<u>513</u>	1094	514	1095	513	80	<u>1094</u>	<u>513</u>	1094	514	1095	513
483.xalancbmk	80	670	824	<u>671</u>	<u>823</u>	672	822	80	670	824	<u>671</u>	<u>823</u>	672	822

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 72000 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

Platform Notes

BIOS Settings:
Power Management = Maximum Performance (Default = Active Power Controller)

General Notes

The Dell PowerEdge R910 and the Bull NovaScale R480 F2 models are electronically equivalent. The results have been measured on a Dell PowerEdge R910 model. Binaries were compiled on RHEL5.5



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Base Compiler Invocation

C benchmarks:

`icc -m32`

C++ benchmarks:

`icpc -m32`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch`
`-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

C++ benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs`
`-L/smartheap -lsmartheap`
`-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m32`

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

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Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403.gcc: basepeak = yes

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

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Peak Optimization Flags (Continued)

C++ benchmarks:

```
471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
             -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
             -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
             -L/smartheap -lsmartheap
```

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>
<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110308.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>
<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110308.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 19:15:29 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 12 April 2011.