



SPEC® CFP2006 Result

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Fujitsu

SPECfp®_rate2006 = 117

PRIMERGY BX924 S2, Intel Xeon E5645, 2.40 GHz

SPECfp_rate_base2006 = 113

CPU2006 license: 19

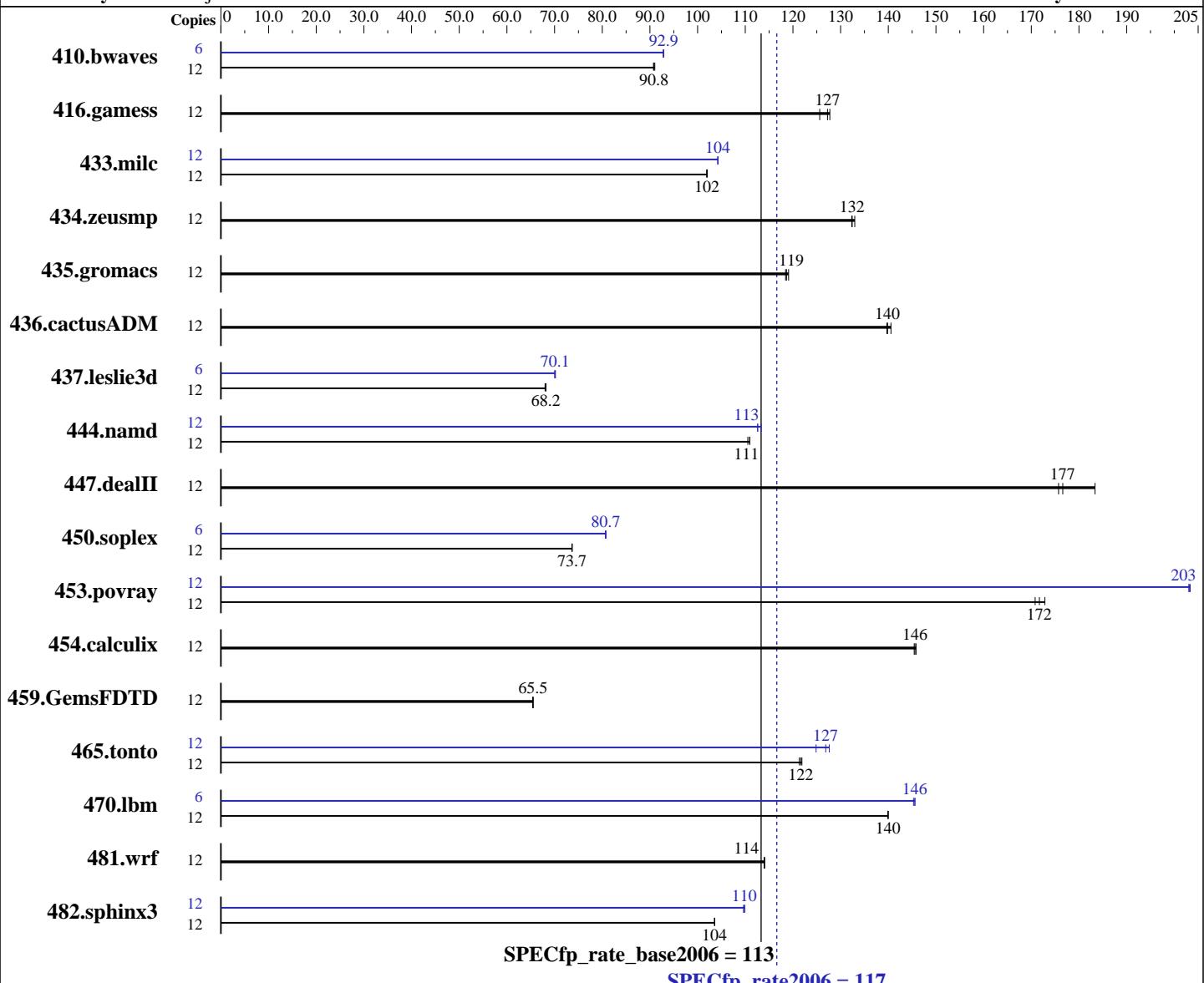
Test date: Feb-2011

Test sponsor: Fujitsu

Hardware Availability: Feb-2011

Tested by: Fujitsu

Software Availability: Jan-2011



Hardware

CPU Name: Intel Xeon E5645
CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz: 2400
FPU: Integrated
CPU(s) enabled: 6 cores, 1 chip, 6 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default
Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116
Auto Parallel: No
File System: ext3
System State: Run level 3 (multi-user)
Base Pointers: 64-bit

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L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (6 x 8 GB 2Rx4 PC3-10600R-9, ECC)
 Disk Subsystem: 1 x SAS, 300 GB, 10000 RPM
 Other Hardware: --

Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	12	1792	91.0	1795	90.8	1797	90.7	6	878	92.9	878	92.9	879	92.8
416.gamess	12	1839	128	1846	127	1870	126	12	1839	128	1846	127	1870	126
433.milc	12	1080	102	1080	102	1081	102	12	1057	104	1057	104	1056	104
434.zeusmp	12	825	132	821	133	825	132	12	825	132	821	133	825	132
435.gromacs	12	723	118	719	119	722	119	12	723	118	719	119	722	119
436.cactusADM	12	1027	140	1020	141	1025	140	12	1027	140	1020	141	1025	140
437.leslie3d	12	1655	68.2	1659	68.0	1654	68.2	6	805	70.1	805	70.0	804	70.2
444.namd	12	871	111	867	111	868	111	12	855	113	855	113	849	113
447.dealII	12	749	183	777	177	781	176	12	749	183	777	177	781	176
450.soplex	12	1358	73.7	1358	73.7	1359	73.6	6	620	80.6	620	80.8	620	80.7
453.povray	12	372	172	369	173	374	171	12	314	203	314	203	314	203
454.calculix	12	679	146	681	145	680	146	12	679	146	681	145	680	146
459.GemsFDTD	12	1945	65.5	1946	65.4	1943	65.5	12	1945	65.5	1946	65.4	1943	65.5
465.tonto	12	973	121	969	122	971	122	12	931	127	925	128	946	125
470.lbm	12	1178	140	1178	140	1178	140	6	566	146	566	146	567	145
481.wrf	12	1175	114	1176	114	1176	114	12	1175	114	1176	114	1176	114
482.sphinx3	12	2258	104	2258	104	2258	104	12	2128	110	2130	110	2134	110

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
 numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
 Hugepages were not configured on the system

Platform Notes

BIOS configuration:
 Data Reuse Optimization = Disable
 Performance/Power Setting = Traditional



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General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>
Binaries were compiled on RHEL5.5 with binutils-2.17.50.0.6-14.el5

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

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Base Optimization Flags (Continued)

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

```
433.milc: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
```

```
470.lbm: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
          -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
          -ansi-alias -opt-prefetch -static -auto-ilp32
```

```
482.sphinx3: -xsse4.2 -ipo -O3 -no-prec-div -unroll12
```

C++ benchmarks:

```
444.namd: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
           -auto-ilp32
```

```
447.dealII: basepeak = yes
```

```
450.soplex: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
             -B /usr/share/libhugetlbfss/ -Wl,-hugetlbfss-link=BDT
```

```
453.povray: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -unroll14 -ansi-alias
             -B /usr/share/libhugetlbfss/ -Wl,-melf_x86_64 -Wl,-hugetlbfss-link=BDT
```

Fortran benchmarks:

```
410.bwaves: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -static
```

```
416.gamess: basepeak = yes
```

```
434.zeusmp: basepeak = yes
```

```
437.leslie3d: -xsse4.2 -ipo -O3 -no-prec-div
               -B /usr/share/libhugetlbfss/ -Wl,-melf_x86_64 -Wl,-hugetlbfss-link=BDT
```

```
459.GemsFDTD: basepeak = yes
```

```
465.tonto: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -unroll14 -auto
             -inline-alloc -opt-malloc-options=3
             -B /usr/share/libhugetlbfss/ -Wl,-melf_x86_64 -Wl,-hugetlbfss-link=BDT
```

Benchmarks using both Fortran and C:

```
435.gromacs: basepeak = yes
```

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Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.html>

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110316.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110316.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

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