SPEC® CINT2006 Result
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Fujitsu

PRIMERGY BX920 S2, Intel Xeon E5606, 2.13 GHz

SPECint®_rate2006 =   163

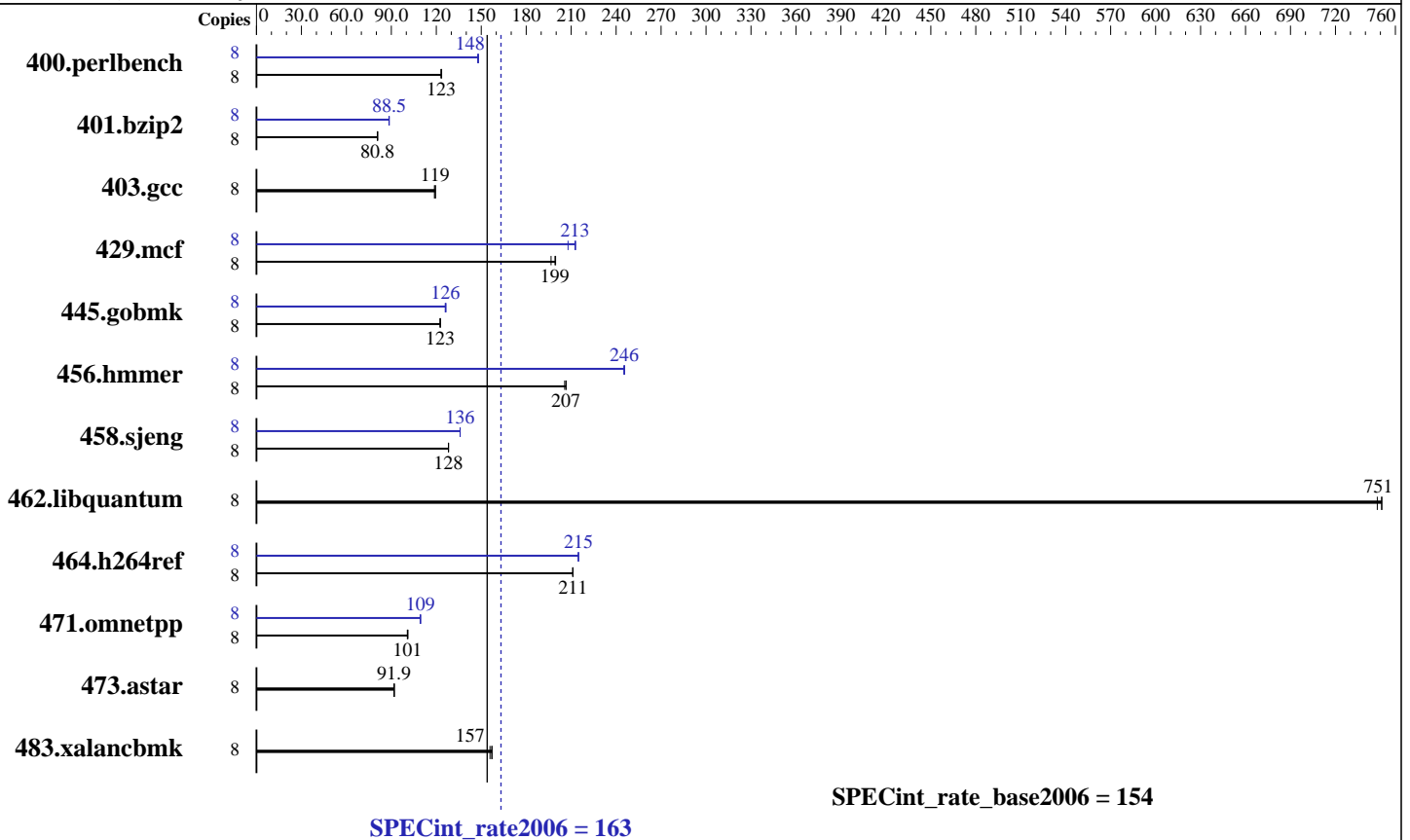SPECint_rate_base2006 = 154

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Feb-2011

Hardware Availability: Feb-2011

Software Availability: Jan-2011

Copies 0   30.0 60.0 90.0 120 150 180 210 240 270 300 330 360 390 420 450 480 510 540 570 600 630 660 690 720 760

Benchmark		
400.perlbench	8	148
	8	123
401.bzip2	8	88.5
	8	80.8
403.gcc	8	119
429.mcf	8	213
	8	199
445.gobmk	8	126
	8	123
456.hmmer	8	246
	8	207
458.sjeng	8	136
	8	128
462.libquantum	8	751
464.h264ref	8	215
	8	211
471.omnetpp	8	109
	8	101
473.astar	8	91.9
483.xalancbmk	8	157

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Hardware

CPU Name: Intel Xeon E5606

CPU Characteristics:

CPU MHz: 2133

FPU: Integrated

CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip

CPU(s) orderable: 1,2 chips

Primary Cache: 32 KB I + 32 KB D on chip per core

Secondary Cache: 256 KB I+D on chip per core

L3 Cache: 8 MB I+D on chip per chip

Other Cache: None

Memory: 48 GB (6 x 8 GB 2Rx4 PC3-10600R-9, ECC, running at 1067 MHz and CL7)

Disk Subsystem: 1 x SAS, 300 GB, 10000 RPM

Other Hardware: --

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default

Compiler: Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116

Auto Parallel: No

File System: ext3

System State: Run level 3 (multi-user)

Base Pointers: 32-bit

Peak Pointers: 32/64-bit

Other Software: Microquill SmartHeap V9.01

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Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	632	124	**634**	**123**	635	123	8	529	148	**528**	**148**	528	148
401.bzip2	8	957	80.6	954	80.9	**956**	**80.8**	8	871	88.6	873	88.4	**873**	**88.5**
403.gcc	8	542	119	**540**	**119**	539	119	8	542	119	**540**	**119**	539	119
429.mcf	8	371	197	366	200	**366**	**199**	8	351	208	343	213	**343**	**213**
445.gobmk	8	685	123	**684**	**123**	684	123	8	666	126	**665**	**126**	664	126
456.hmmer	8	**361**	**207**	363	206	361	207	8	**304**	**246**	304	245	304	246
458.sjeng	8	755	128	756	128	**755**	**128**	8	**712**	**136**	712	136	712	136
462.libquantum	8	221	751	222	748	**221**	**751**	8	221	751	222	748	**221**	**751**
464.h264ref	8	839	211	838	211	**838**	**211**	8	**824**	**215**	824	215	824	215
471.omnetpp	8	494	101	497	101	**496**	**101**	8	**457**	**109**	456	110	458	109
473.astar	8	**611**	**91.9**	611	91.9	611	91.9	8	**611**	**91.9**	611	91.9	611	91.9
483.xalancbmk	8	354	156	**352**	**157**	351	157	8	354	156	**352**	**157**	351	157

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
 'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
 Hugepages were not configured on the system
```

Platform Notes

```
 BIOS configuration:
 Data Reuse Optimization = Disable
 Performance/Power Setting = Traditional
```

General Notes

```
 For information about Fujitsu please visit: http://www.fujitsu.com
 Binaries were compiled on RHEL5.5 with binutils-2.17.50.0.6-14.el5
```

Base Compiler Invocation

C benchmarks:
```
     icc  -m32
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
    icpc -m32

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
    -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
    -L/smartheap -lsmartheap
    -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:

    403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

    400.perlbench: icc -m64

    401.bzip2: icc -m64

    456.hmmer: icc -m64

    458.sjeng: icc -m64

C++ benchmarks:
    icpc -m32

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Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
    401.bzip2: -DSPEC_CPU_LP64
   456.hmmer: -DSPEC_CPU_LP64
    458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

    401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -opt-prefetch -auto-ilp32 -ansi-alias
               -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

      403.gcc: basepeak = yes

      429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -ansi-alias -auto-ilp32

    445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
               -ansi-alias -auto-ilp32

   456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
               -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

    458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -unroll4 -auto-ilp32
               -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

   464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -unroll2 -ansi-alias
```

C++ benchmarks:

```
   471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
               -L/smartheap -lsmartheap
```

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Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.html
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110316.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.xml
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110316.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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