



# SPEC® CFP2006 Result

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## Oracle Corporation

Sun Blade X6275 M2 Server Module (Intel Xeon X5675 3.06 GHz)

**SPECfp®2006 = 60.4**

**SPECfp\_base2006 = 57.3**

CPU2006 license: 6

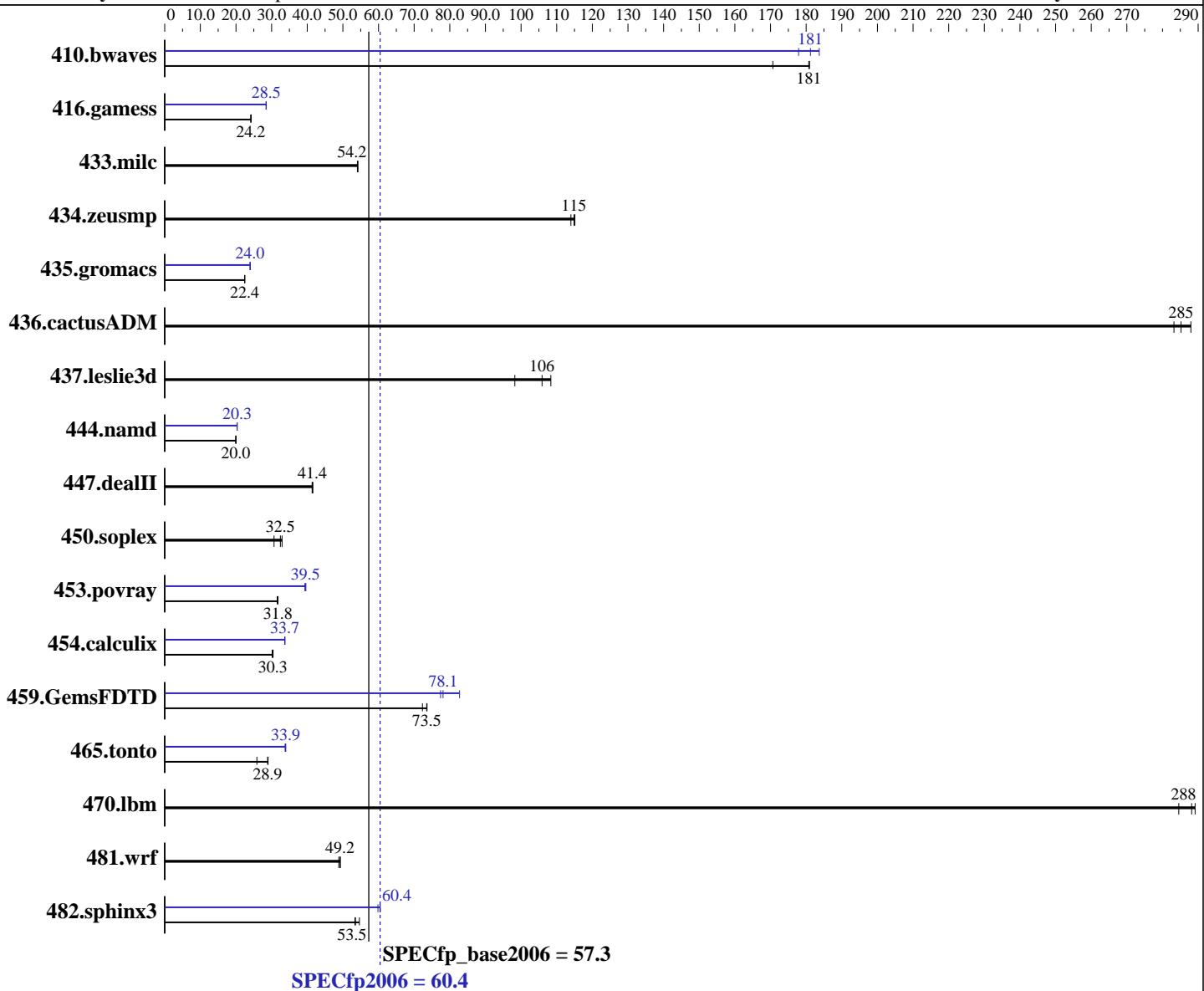
Test sponsor: Oracle Corporation

Tested by: Oracle Corporation

Test date: Jan-2011

Hardware Availability: Mar-2011

Software Availability: Nov-2010



### Hardware

CPU Name: Intel Xeon X5675  
CPU Characteristics: Intel Turbo Boost Technology up to 3.46 GHz  
CPU MHz: 3067  
FPU: Integrated  
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
CPU(s) orderable: 1 or 2 chips per Sun Blade X6275 M2 node  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core

### Software

Operating System: Oracle Linux 5.5  
Compiler: kernel 2.6.18-194.el5  
Auto Parallel: Intel C++ and Fortran Intel 64 Compiler XE  
File System: for applications running on Intel 64  
System State: Version 12 Alpha Build 20110105  
Base Pointers: Yes  
ext3  
Run level 3 (multi-user)  
64-bit

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## General Notes

OMP\_NUM\_THREADS set to number of cores

KMP\_AFFINITY set to granularity=fine,scatter

KMP\_STACKSIZE set to 200M

Though binaries were linked with hugepages, this result did not use hugepages.

Binaries were compiled on RHEL5.5 with Binutils binutils-2.17.50.0.6-14.el5

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
  433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
  437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
  447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
  465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
      481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

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## Base Optimization Flags (Continued)

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
```

Fortran benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias
```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
433.milc: basepeak = yes
```

```
470.lbm: basepeak = yes
```

```
482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel
```

C++ benchmarks:

```
444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32
```

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## Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll14 -ansi-alias  
 -B /usr/share/libhugetlbfsl -Wl,-melf\_x86\_64 -Wl,-hugetlbfsl-link=BDT

Fortran benchmarks:

410.bwaves: -xsSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -parallel  
 -static

416.gamess: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll12  
 -inline-level=0 -scalar-rep -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll12  
 -inline-level=0 -opt-prefetch -parallel  
 -B /usr/share/libhugetlbfsl -Wl,-melf\_x86\_64 -Wl,-hugetlbfsl-link=BDT

465.tonto: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
 -opt-malloc-options=3 -auto -unroll14  
 -B /usr/share/libhugetlbfsl -Wl,-melf\_x86\_64 -Wl,-hugetlbfsl-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
 -ansi-alias

436.cactusADM: basepeak = yes

454.calculix: -xsSE4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.html>  
[http://www.spec.org/cpu2006/flags/Oracle-platform-x86\\_64.20101027.html](http://www.spec.org/cpu2006/flags/Oracle-platform-x86_64.20101027.html)

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.xml>  
[http://www.spec.org/cpu2006/flags/Oracle-platform-x86\\_64.20101027.xml](http://www.spec.org/cpu2006/flags/Oracle-platform-x86_64.20101027.xml)



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 16:22:13 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 3 March 2011.