SPEC® CFP2006 Result

Huawei

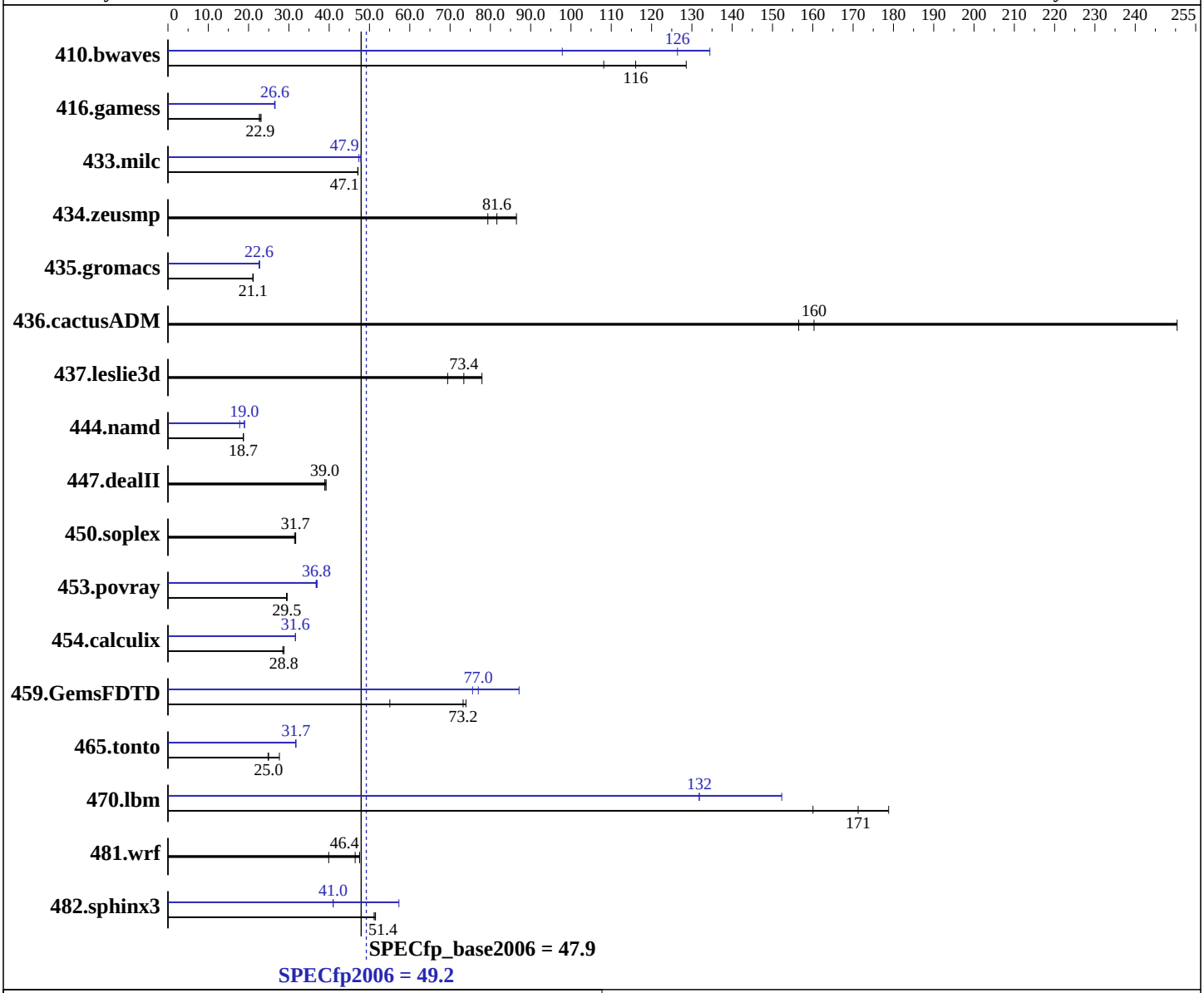
Huawei XH620, Intel Xeon X5670

SPECfp®2006 = 49.2

SPECfp_base2006 = 47.9

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Jan-2011
Hardware Availability: Apr-2010
Software Availability: Jan-2011



Hardware

CPU Name:	Intel Xeon X5670
CPU Characteristics:	Intel Turbo Boost Technology up to 3.33 GHz
CPU MHz:	2933
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-smp
Compiler:	Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit

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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem: 1 x 300 GB SAS, 15K RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	106	129	126	108	**117**	**116**	101	134	139	97.8	**107**	**126**
416.gamess	**855**	**22.9**	864	22.7	847	23.1	**737**	**26.6**	737	26.6	740	26.5
433.milc	**195**	**47.1**	195	47.2	195	47.1	194	47.4	192	47.9	**192**	**47.9**
434.zeusmp	115	79.4	**112**	**81.6**	105	86.5	115	79.4	**112**	**81.6**	105	86.5
435.gromacs	**338**	**21.1**	339	21.1	337	21.2	315	22.6	314	22.8	**315**	**22.6**
436.cactusADM	76.4	157	47.7	250	**74.5**	**160**	76.4	157	47.7	250	**74.5**	**160**
437.leslie3d	121	77.9	135	69.4	**128**	**73.4**	121	77.9	135	69.4	**128**	**73.4**
444.namd	**429**	**18.7**	429	18.7	429	18.7	**423**	**19.0**	422	19.0	451	17.8
447.dealII	294	38.9	**294**	**39.0**	291	39.2	294	38.9	**294**	**39.0**	291	39.2
450.soplex	**263**	**31.7**	263	31.7	265	31.4	**263**	**31.7**	263	31.7	265	31.4
453.povray	**180**	**29.5**	180	29.6	181	29.4	144	37.0	145	36.8	**144**	**36.8**
454.calculix	**287**	**28.8**	286	28.8	290	28.5	**261**	**31.6**	261	31.6	261	31.6
459.GemsFDTD	143	73.9	193	55.1	**145**	**73.2**	122	87.1	140	75.6	**138**	**77.0**
465.tonto	**394**	**25.0**	356	27.6	396	24.8	**310**	**31.7**	310	31.8	311	31.7
470.lbm	76.8	179	85.8	160	**80.2**	**171**	104	132	**104**	**132**	90.2	152
481.wrf	280	39.9	**240**	**46.4**	235	47.6	280	39.9	**240**	**46.4**	235	47.6
482.sphinx3	**379**	**51.4**	378	51.5	381	51.1	476	41.0	340	57.3	**475**	**41.0**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
Hugepages were not configured on the system.
```

Platform Notes

```
Data Reuse Optimization disabled in BIOS Setup.
```

General Notes

```
Binaries compiled on RHEL 5.5
 OMP_NUM_THREADS set to number of cores
 KMP_AFFINITY set to granularity=fine,scatter
 KMP_STACKSIZE set to 200M
```

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32 -ansi-alias

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -parallel -ansi-alias -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias -parallel

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

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Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -parallel -static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32 -ansi-alias

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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