



SPEC® CFP2006 Result

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Fujitsu

SPECfp®_rate2006 = 216

PRIMERGY TX300 S6, Intel Xeon E5645, 2.40 GHz

SPECfp_rate_base2006 = 211

CPU2006 license: 19

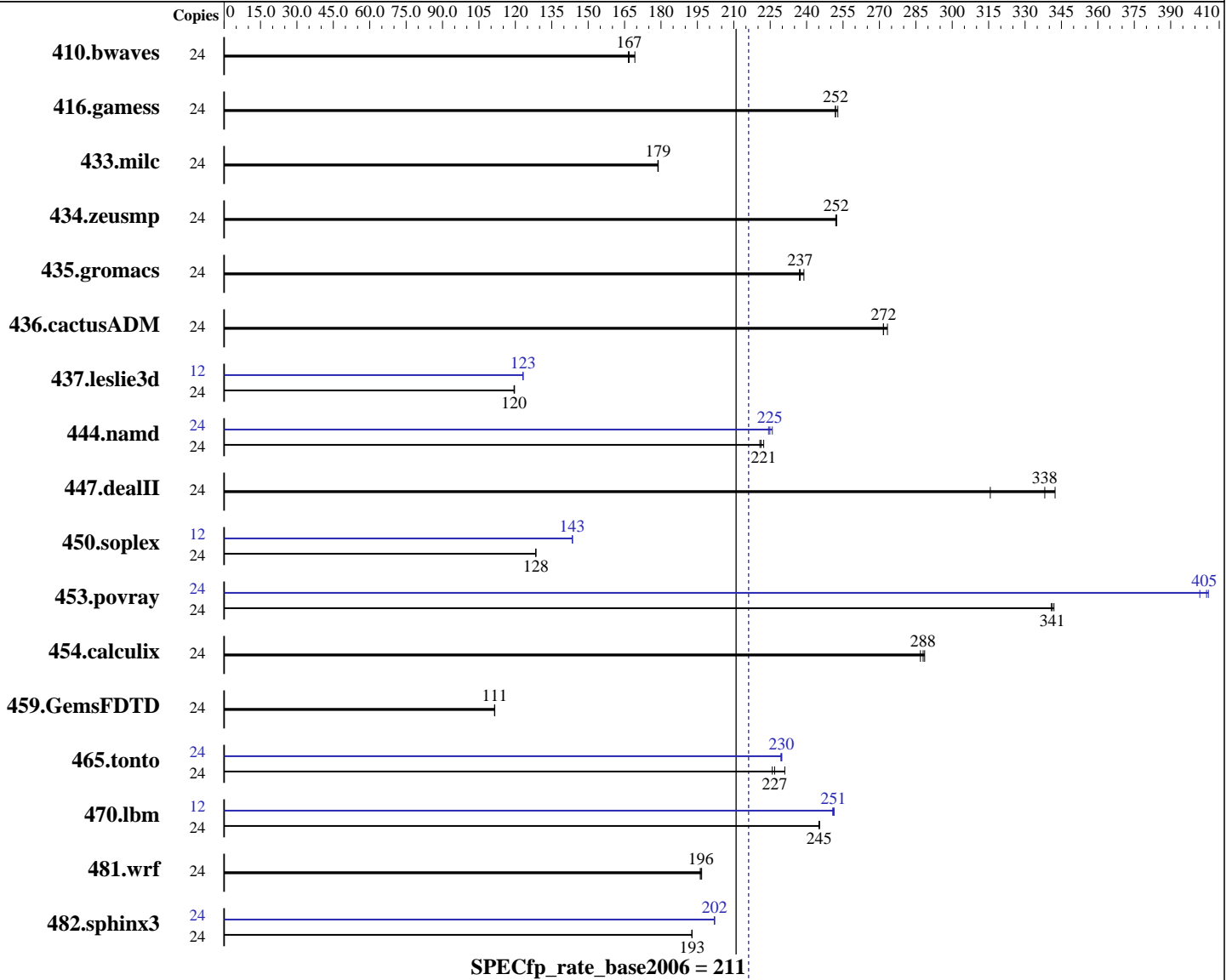
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jan-2011

Hardware Availability: Feb-2011

Software Availability: Nov-2010



Hardware

CPU Name: Intel Xeon E5645
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64, Version 12.0.0.082 Build 20101006
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem: 1 x SAS, 300 GB, 10000 RPM
Other Hardware: --

Peak Pointers: 32/64-bit
Other Software: none

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	1927	169	<u>1954</u>	<u>167</u>	1958	167	24	1927	169	<u>1954</u>	<u>167</u>	1958	167
416.gamess	24	1858	253	<u>1865</u>	<u>252</u>	1866	252	24	1858	253	<u>1865</u>	<u>252</u>	1866	252
433.milc	24	1233	179	1232	179	<u>1232</u>	<u>179</u>	24	1233	179	1232	179	<u>1232</u>	<u>179</u>
434.zeusmp	24	<u>866</u>	<u>252</u>	865	252	867	252	24	<u>866</u>	<u>252</u>	865	252	867	252
435.gromacs	24	<u>722</u>	<u>237</u>	717	239	723	237	24	<u>722</u>	<u>237</u>	717	239	723	237
436.cactusADM	24	<u>1056</u>	<u>272</u>	1049	273	1056	272	24	<u>1056</u>	<u>272</u>	1049	273	1056	272
437.leslie3d	24	1888	119	<u>1888</u>	<u>120</u>	1885	120	12	916	123	<u>916</u>	<u>123</u>	916	123
444.namd	24	872	221	866	222	<u>870</u>	<u>221</u>	24	<u>856</u>	<u>225</u>	852	226	858	224
447.dealII	24	870	316	802	342	<u>812</u>	<u>338</u>	24	870	316	802	342	<u>812</u>	<u>338</u>
450.soplex	24	1558	128	<u>1558</u>	<u>128</u>	1560	128	12	<u>698</u>	<u>143</u>	698	143	697	144
453.povray	24	373	342	375	341	<u>374</u>	<u>341</u>	24	318	402	<u>315</u>	<u>405</u>	315	406
454.calculix	24	690	287	<u>687</u>	<u>288</u>	686	289	24	690	287	<u>687</u>	<u>288</u>	686	289
459.GemsFDTD	24	<u>2284</u>	<u>111</u>	2283	112	2285	111	24	<u>2284</u>	<u>111</u>	2283	112	2285	111
465.tonto	24	1022	231	<u>1041</u>	<u>227</u>	1046	226	24	1028	230	<u>1028</u>	<u>230</u>	1029	229
470.lbm	24	1344	245	1346	245	<u>1345</u>	<u>245</u>	12	658	251	656	251	<u>657</u>	<u>251</u>
481.wrf	24	<u>1364</u>	<u>196</u>	1367	196	1363	197	24	<u>1364</u>	<u>196</u>	1367	196	1363	197
482.sphinx3	24	2426	193	<u>2426</u>	<u>193</u>	2427	193	24	<u>2315</u>	<u>202</u>	2316	202	2315	202

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
Large pages were not enabled for this run

Platform Notes

BIOS configuration:
Data Reuse Optimization = Disable



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General Notes

This result was measured on the PRIMERGY TX300 S6. The PRIMERGY TX300 S6 and the PRIMERGY TX300 S6 are electronically equivalent.

For information about Fujitsu please visit: <http://www.fujitsu.com>
Binaries were compiled on SLES 10 SP1 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -ansi-alias

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Base Optimization Flags (Continued)

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -ansi-alias

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -ansi-alias

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):
icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-ansi-alias -opt-prefetch -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

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Peak Optimization Flags (Continued)

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.00.xml>

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For other inquiries, please contact webmaster@spec.org.

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