SPEC® CINT2006 Result

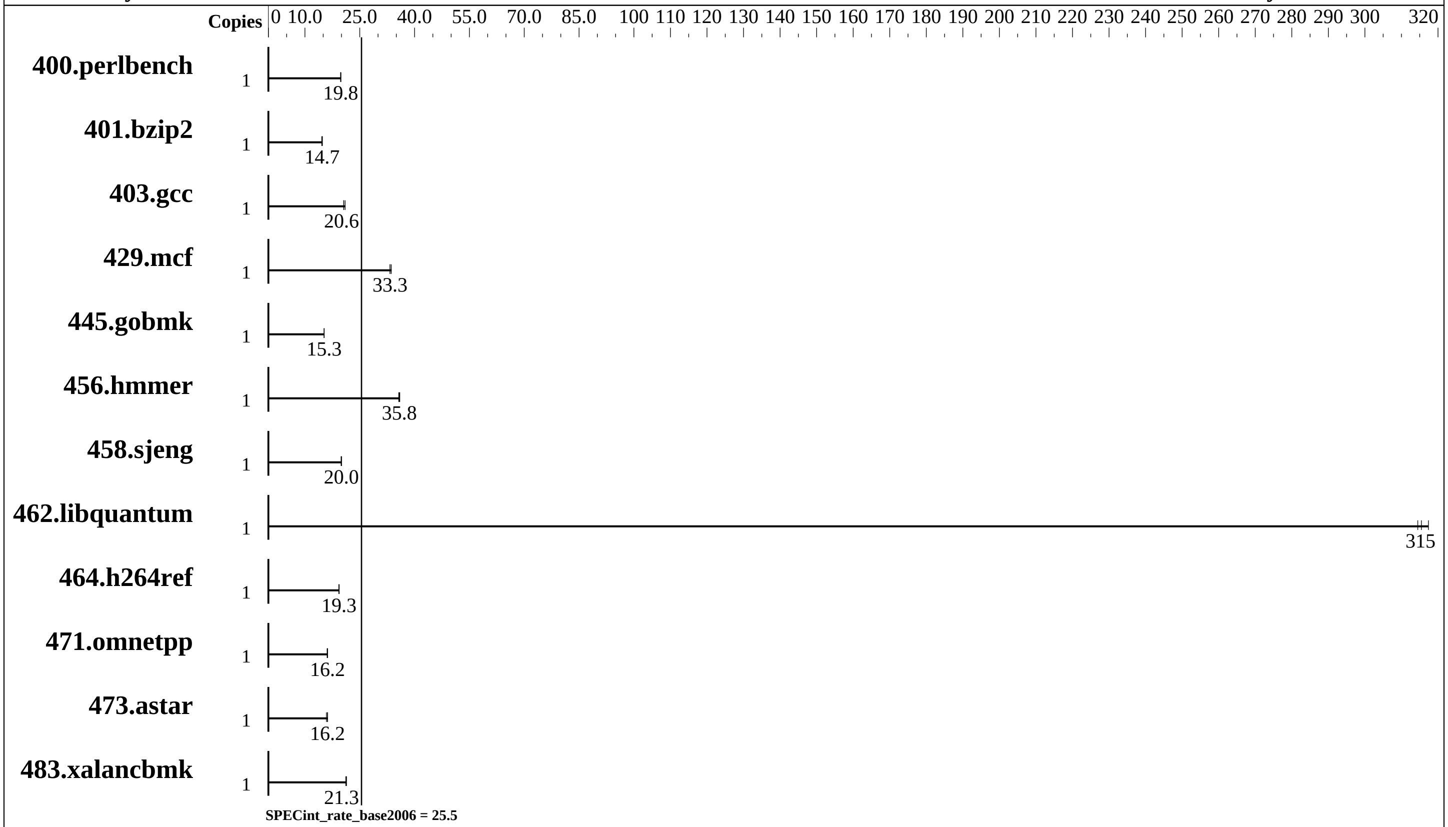
J.P Sa Couto S.A.

TSUNAMI Tidal S6016BUB server system (Intel Xeon X5520)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 25.5

CPU2006 license:	4105	**Test date:**	Dec-2010
Test sponsor:	J.P Sa Couto S.A.	**Hardware Availability:**	Nov-2010
Tested by:	J.P Sa Couto S.A.	**Software Availability:**	Nov-2010



Hardware

CPU Name:	Intel Xeon E5520
CPU Characteristics:	Intel Turbo Boost Technology up to 2.53 GHz
CPU MHz:	2266
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	8 GB (4 x 2 GB PC3-10600R-9, ECC)
Disk Subsystem:	INTEL X25-M SATA SSD
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ CompilerXE for Intel 64, Version 12.0 Package ID: l_ccompxe_intel64_2011.0.084
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	Not Applicable
Other Software:	None

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Results Table

	Base							Peak						
Benchmark	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**
400.perlbench	1	**494**	**19.8**	492	19.9	495	19.8							
401.bzip2	1	654	14.8	**655**	**14.7**	661	14.6							
403.gcc	1	391	20.6	383	21.0	**390**	**20.6**							
429.mcf	1	271	33.6	274	33.3	**274**	**33.3**							
445.gobmk	1	**687**	**15.3**	687	15.3	687	15.3							
456.hmmer	1	**260**	**35.8**	260	35.9	262	35.7							
458.sjeng	1	605	20.0	**605**	**20.0**	608	19.9							
462.libquantum	1	65.9	314	65.3	317	**65.7**	**315**							
464.h264ref	1	1142	19.4	**1145**	**19.3**	1148	19.3							
471.omnetpp	1	**386**	**16.2**	386	16.2	389	16.1							
473.astar	1	**434**	**16.2**	433	16.2	442	15.9							
483.xalancbmk	1	326	21.2	323	21.4	**324**	**21.3**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
`icc -m64`

C++ benchmarks:
`icpc -m64`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
403.gcc: `-DSPEC_CPU_LP64`
429.mcf: `-DSPEC_CPU_LP64`
445.gobmk: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
464.h264ref: `-DSPEC_CPU_LP64`
471.omnetpp: `-DSPEC_CPU_LP64`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revF.20100609.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revF.20100609.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
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