



SPEC® CINT2006 Result

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Hewlett-Packard Company

SPECint®2006 = **Not Run**

ProLiant DL180 G6 (2.67 GHz, Intel Xeon E5640)

SPECint_base2006 = **27.9**

CPU2006 license: 3

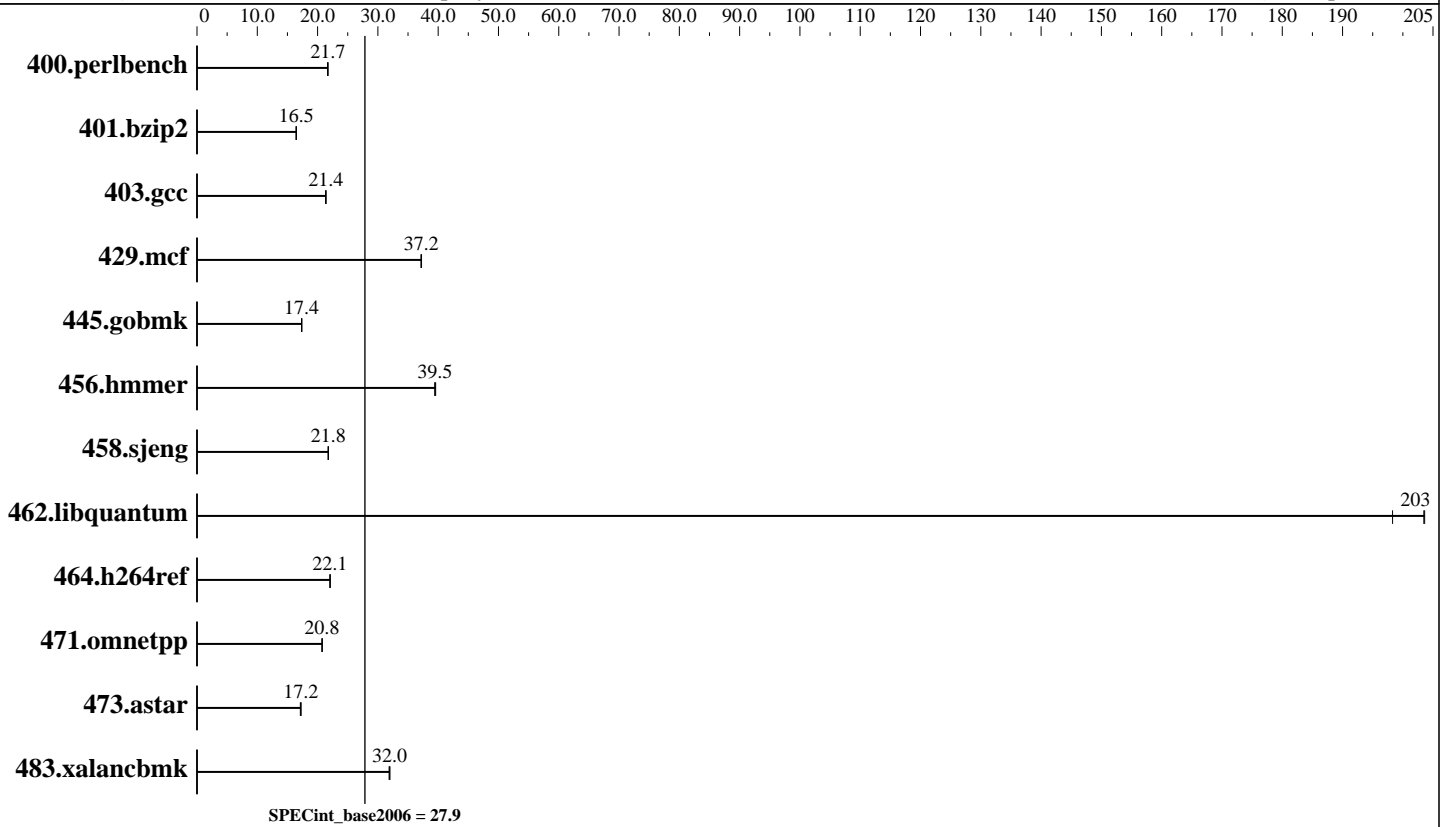
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Nov-2010

Hardware Availability: Mar-2010

Software Availability: Apr-2010



Hardware

CPU Name: Intel Xeon E5640
 CPU Characteristics: Intel Turbo Boost Technology up to 2.93 GHz
 CPU MHz: 2667
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (4 x 4 GB 2Rx4 PC3-10600R-9, ECC)
 Disk Subsystem: 4 x 146 GB 10 K SAS
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 5.5
 Kernel 2.6.18-194.el5
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1
 Build 20091130 Package ID: 1_cproc_p_11.1.064
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: Not Applicable
 Other Software: Microquill SmartHeap V8.1



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	451	21.7	450	21.7	450	21.7						
401.bzip2	587	16.4	586	16.5	586	16.5						
403.gcc	376	21.4	377	21.4	377	21.3						
429.mcf	245	37.2	246	37.1	245	37.2						
445.gobmk	604	17.4	603	17.4	605	17.4						
456.hammer	236	39.5	236	39.6	236	39.5						
458.sjeng	556	21.8	556	21.8	556	21.8						
462.libquantum	102	204	104	198	102	203						
464.h264ref	1001	22.1	1003	22.1	1004	22.0						
471.omnetpp	301	20.8	301	20.8	301	20.7						
473.astar	410	17.1	407	17.2	407	17.2						
483.xalancbmk	216	32.0	217	31.9	216	32.0						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
Power Efficiency set to Performance

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64



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Base Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revF.20101118.html>

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20101221.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revF.20101118.xml>

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20101221.xml>



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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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