



# SPEC® CINT2006 Result

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## Acer Incorporated

SPECint®\_rate2006 = 71.0

Gateway GT115 F1 (AMD Opteron 4122, 2.20 GHz)

SPECint\_rate\_base2006 = 61.9

CPU2006 license: 97

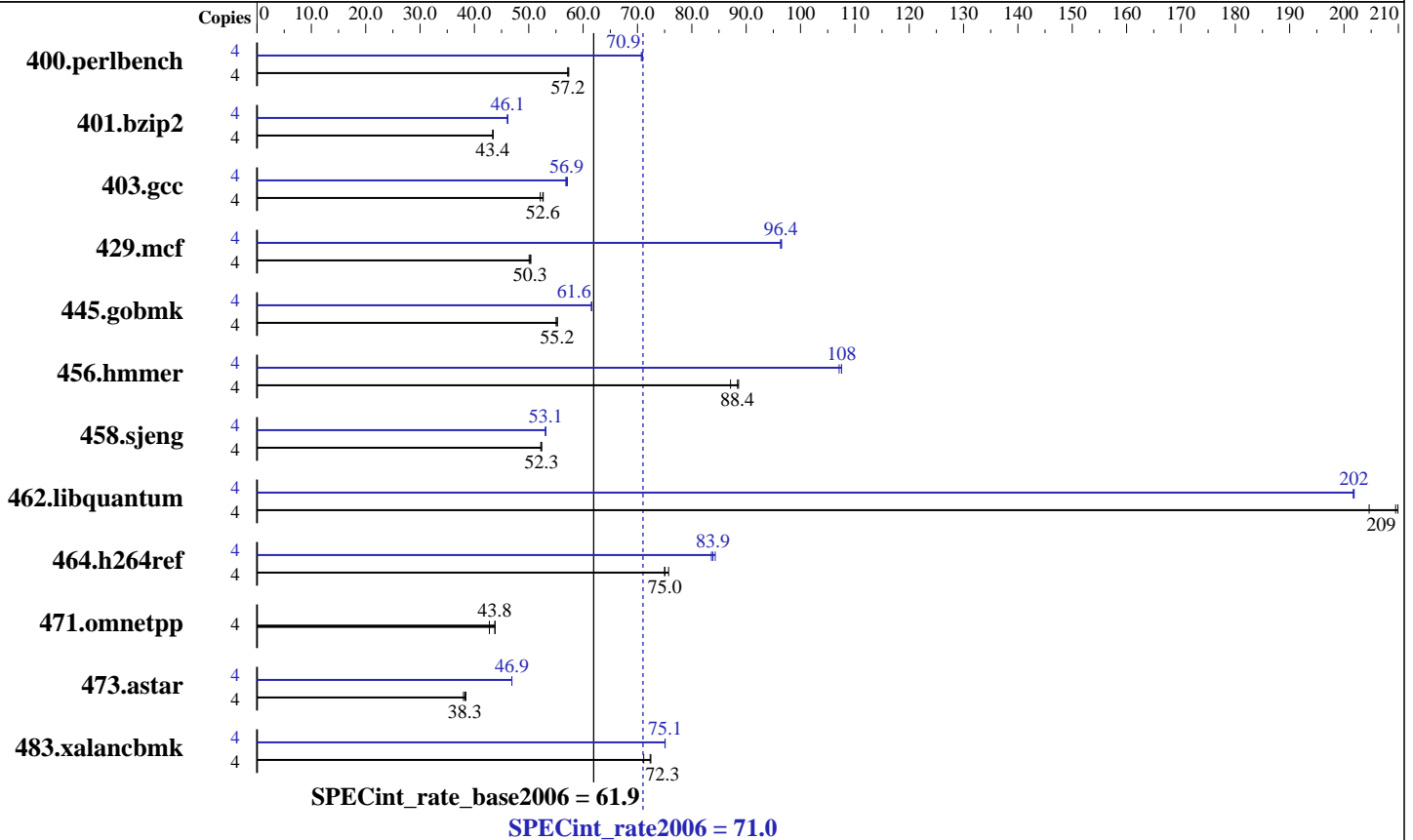
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Oct-2010

Hardware Availability: Oct-2010

Software Availability: Aug-2010



### Hardware

CPU Name: AMD Opteron 4122  
 CPU Characteristics:  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 64 KB I + 64 KB D on chip per core  
 Secondary Cache: 512 KB I+D on chip per core  
 L3 Cache: 6 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 16 GB (2 x 8 GB 2Rx4 PC3-10600R-9, ECC)  
 Disk Subsystem: 1 x 640 GB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64)  
 Kernel 2.6.27.19-5-default  
 Compiler: x86 Open64 4.2.4 Compiler Suite (from AMD)  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: binutils 2.18  
 SmartHeap 8.1 32-bit Library for Linux



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	<b>683</b>	<b>57.2</b>	684	57.2	681	57.4	4	551	70.9	553	70.7	<b>552</b>	<b>70.9</b>
401.bzip2	4	<b>889</b>	<b>43.4</b>	889	43.4	889	43.4	4	<b>837</b>	<b>46.1</b>	836	46.1	838	46.1
403.gcc	4	611	52.7	<b>612</b>	<b>52.6</b>	618	52.1	4	<b>565</b>	<b>56.9</b>	564	57.1	567	56.8
429.mcf	4	724	50.4	<b>726</b>	<b>50.3</b>	729	50.1	4	<b>378</b>	<b>96.4</b>	379	96.3	378	96.5
445.gobmk	4	759	55.3	<b>760</b>	<b>55.2</b>	763	55.0	4	682	61.5	<b>682</b>	<b>61.6</b>	682	61.6
456.hammer	4	<b>422</b>	<b>88.4</b>	421	88.6	428	87.1	4	<b>347</b>	<b>108</b>	347	108	348	107
458.sjeng	4	<b>925</b>	<b>52.3</b>	924	52.4	927	52.2	4	<b>911</b>	<b>53.1</b>	912	53.1	911	53.1
462.libquantum	4	395	210	<b>396</b>	<b>209</b>	405	205	4	411	202	411	202	<b>411</b>	<b>202</b>
464.h264ref	4	1169	75.7	1180	75.0	<b>1180</b>	<b>75.0</b>	4	1050	84.3	<b>1055</b>	<b>83.9</b>	1059	83.6
471.omnetpp	4	571	43.8	<b>571</b>	<b>43.8</b>	585	42.8	4	571	43.8	<b>571</b>	<b>43.8</b>	585	42.8
473.astar	4	731	38.4	<b>734</b>	<b>38.3</b>	739	38.0	4	<b>599</b>	<b>46.9</b>	599	46.9	599	46.9
483.xalancbmk	4	<b>382</b>	<b>72.3</b>	381	72.5	388	71.2	4	<b>368</b>	<b>75.1</b>	368	75.1	368	75.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

Set vm/nr\_hugepages=3584 in /etc/sysctl.conf  
mount -t hugetlbfs nodev /mnt/hugepages

## General Notes

Environment variables set by runspec before the start of the run:  
HUGETLB\_LIMIT = "896"  
LD\_LIBRARY\_PATH = "/usr/cpu2006/amd1002-rate-libs-revC/64:/usr/cpu2006/amd1002-rate-libs-revC/32"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at <http://developer.amd.com/cpu/open64>  
Binaries were compiled on SLES10 SP2 with binutils 2.18  
The Acer AT115 F1, and Gateway GT115 F1 are electronically equivalent.  
This result was measured on Gateway GT115 F1.



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## Base Compiler Invocation

C benchmarks:  
opencc

C++ benchmarks:  
openCC

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-march=barcelona -mso -Ofast -CG:local\_sched\_alg=1  
-INLINE:aggressive=on -IPA:plimit=8000 -IPA:small\_pu=100  
-HP:bdt=2m:heap=2m

C++ benchmarks:  
-march=barcelona -mso -Ofast -m32 -INLINE:aggressive=on  
-CG:cmp\_peep=on -L/root/work/libraries/SmartHeap-8.1/lib -lsmartheap

## Peak Compiler Invocation

C benchmarks:  
opencc

C++ benchmarks:  
openCC

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

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## Peak Portability Flags (Continued)

401.bzip2: -DSPEC\_CPU\_LP64  
 445.gobmk: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
 464.h264ref: -DSPEC\_CPU\_LP64  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -march=barcelona -mso -fb\_create fbdata(pass 1)  
 -fb\_opt fbdata(pass 2) -Ofast -IPA:plimit=20000 -LNO:opt=0  
 -OPT:unroll\_times\_max=8 -OPT:unroll\_size=256  
 -OPT:unroll\_level=2 -OPT:keep\_ext=on -WOPT:if\_conv=0  
 -CG:local\_sched\_alg=1 -CG:unroll\_fb\_req=on  
 -HP:bdt=2m:heap=2m

401.bzip2: -march=barcelona -mso -fb\_create fbdata(pass 1)  
 -fb\_opt fbdata(pass 2) -O3 -OPT:alias=disjoint  
 -OPT:goto=off -CG:local\_sched\_alg=1 -HP:bdt=2m:heap=2m

403.gcc: -march=barcelona -mso -fb\_create fbdata(pass 1)  
 -fb\_opt fbdata(pass 2) -Ofast -LNO:trip\_count=256  
 -LNO:prefetch Ahead=10 -CG:cmp\_peep=on -m32  
 -HP:bdt=2m:heap=2m -GRA:unspill=on -IPA:small\_pu=200

429.mcf: -march=barcelona -mso -O3 -ipa -INLINE:aggressive=on  
 -CG:gcm=off -GRA:prioritize\_by\_density=on -m32  
 -HP:bdt=2m:heap=2m

445.gobmk: -march=barcelona -mso -fb\_create fbdata(pass 1)  
 -fb\_opt fbdata(pass 2) -O3 -OPT:alias=restrict  
 -OPT:unroll\_times\_max=8 -OPT:unroll\_size=256  
 -OPT:unroll\_level=2 -OPT:keep\_ext=on -ipa -IPA:plimit=750  
 -IPA:min\_hotness=300 -IPA:pu\_reorder=1 -LNO:prefetch=1  
 -LNO:ignore\_feedback=off -CG:p2align=on  
 -CG:unroll\_fb\_req=on -HP:bdt=2m:heap=2m

456.hmmer: -march=barcelona -mso -fb\_create fbdata(pass 1)  
 -fb\_opt fbdata(pass 2) -Ofast -LNO:prefetch=0  
 -OPT:alias=disjoint -OPT:unroll\_times\_max=8  
 -OPT:unroll\_size=256 -OPT:unroll\_level=2 -OPT:keep\_ext=on  
 -CG:local\_sched\_alg=1 -CG:cflow=0  
 -CG:push\_pop\_int\_saved\_regs=off -CG:cmp\_peep=on  
 -HP:bdt=2m:heap=2m

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## Peak Optimization Flags (Continued)

```
458.sjeng: -march=barcelona -mso -fb_create fbdata(pass 1)
          -fb_opt fbdata(pass 2) -O3 -ipa -LNO:ignore_feedback=off
          -LNO:full_unroll=10 -LNO:fusion=0 -LNO:fission=2
          -IPA:pu_reorder=2 -CG:ptr_load_use=0
          -OPT:unroll_times_max=8 -INLINE:aggressive=on
```

```
462.libquantum: -march=barcelona -mso -Ofast -LNO:pf2=0 -CG:gcm=off
               -CG:use_prefetchnta=on -CG:cmp_peep=on -WOPT:aggstr=0
               -HP:bdt=2m:heap=2m -OPT:alias=disjoint
               -INLINE:aggressive=on -IPA:space=1000 -IPA:plimit=20000
```

```
464.h264ref: -march=barcelona -mso -fb_create fbdata(pass 1)
             -fb_opt fbdata(pass 2) -O3 -IPA:plimit=20000
             -OPT:alias=disjoint -LNO:prefetch=0 -CG:ptr_load_use=0
             -CG:push_pop_int_saved_regs=off
```

C++ benchmarks:

```
471.omnetpp: basepeak = yes
```

```
473.astar: -march=barcelona -mso -fb_create fbdata(pass 1)
           -fb_opt fbdata(pass 2) -Ofast -TENV:frame_pointer=off
           -WOPT:if_conv=0 -GRA:optimize_boundary=on
           -OPT:alias=disjoint -INLINE:aggressive=on
           -IPA:small_pu=3000 -IPA:plimit=3000 -m32
           -HP:bdt=2m:heap=2m
```

```
483.xalancbmk: -march=barcelona -mso -Ofast -INLINE:aggressive=on -m32
              -CG:cmp_peep=on -GRA:unspill=on -TENV:frame_pointer=off
              -fno-emit-exceptions
              -L/root/work/libraries/SmartHeap-8.1/lib -lsmarheap
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.20100901.html>

<http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.20100901.xml>

<http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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