



# SPEC® CINT2006 Result

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## Oracle Corporation

**SPECint®2006 = 39.0**

Sun Fire X4170 M2 (Intel Xeon X5670 2.93 GHz)

**SPECint\_base2006 = 36.2**

CPU2006 license: 6

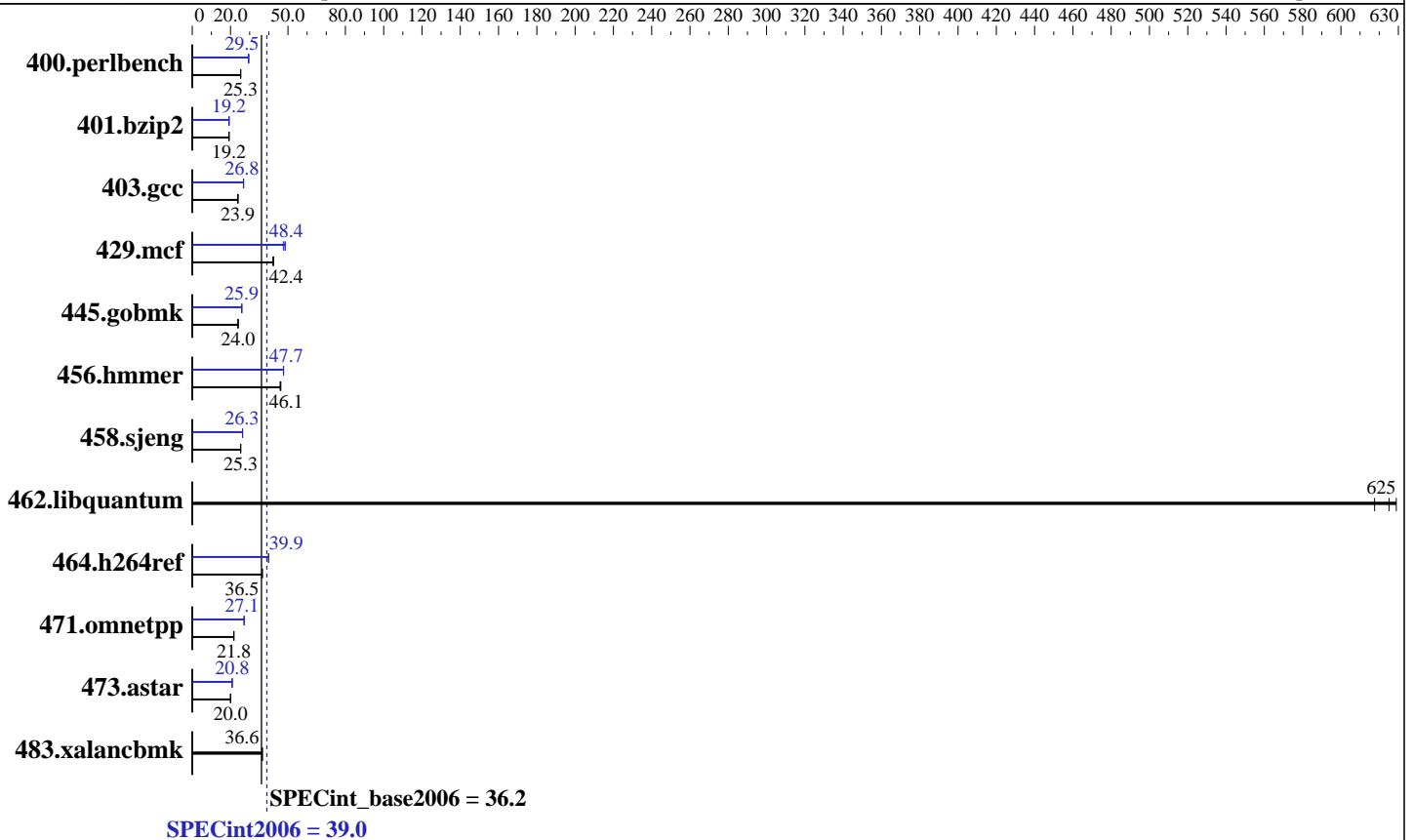
Test date: Sep-2010

Test sponsor: Oracle Corporation

Hardware Availability: Jun-2010

Tested by: Oracle Corporation

Software Availability: Apr-2010



### Hardware

CPU Name: Intel Xeon X5670  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz  
 CPU MHz: 2933  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 or 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)  
 Disk Subsystem: 1 x 146 GB SAS, 10000 RPM  
 Other Hardware: None

### Software

Operating System: Oracle Enterprise Linux Server release 5.5 kernel 2.6.18-194.el5  
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l\_cproc\_p\_11.1.064  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	386	25.3	<b><u>386</u></b>	<b><u>25.3</u></b>	386	25.3	332	29.4	<b><u>332</u></b>	<b><u>29.5</u></b>	331	29.5
401.bzip2	<b><u>502</u></b>	<b><u>19.2</u></b>	502	19.2	502	19.2	502	19.2	502	19.2	<b><u>502</u></b>	<b><u>19.2</u></b>
403.gcc	<b><u>337</u></b>	<b><u>23.9</u></b>	337	23.9	337	23.9	300	26.9	<b><u>300</u></b>	<b><u>26.8</u></b>	301	26.7
429.mcf	216	42.2	<b><u>215</u></b>	<b><u>42.4</u></b>	215	42.5	192	47.6	188	48.6	<b><u>188</u></b>	<b><u>48.4</u></b>
445.gobmk	437	24.0	<b><u>437</u></b>	<b><u>24.0</u></b>	439	23.9	405	25.9	407	25.8	<b><u>405</u></b>	<b><u>25.9</u></b>
456.hammer	<b><u>202</u></b>	<b><u>46.1</u></b>	202	46.3	203	45.9	196	47.7	196	47.7	<b><u>196</u></b>	<b><u>47.7</u></b>
458.sjeng	478	25.3	479	25.3	<b><u>478</u></b>	<b><u>25.3</u></b>	460	26.3	<b><u>460</u></b>	<b><u>26.3</u></b>	460	26.3
462.libquantum	32.9	629	<b><u>33.1</u></b>	<b><u>625</u></b>	33.5	618	32.9	629	<b><u>33.1</u></b>	<b><u>625</u></b>	33.5	618
464.h264ref	603	36.7	<b><u>606</u></b>	<b><u>36.5</u></b>	607	36.5	<b><u>554</u></b>	<b><u>39.9</u></b>	554	39.9	555	39.9
471.omnetpp	287	21.8	288	21.7	<b><u>287</u></b>	<b><u>21.8</u></b>	230	27.1	<b><u>230</u></b>	<b><u>27.1</u></b>	231	27.1
473.astar	<b><u>350</u></b>	<b><u>20.0</u></b>	350	20.0	351	20.0	<b><u>337</u></b>	<b><u>20.8</u></b>	337	20.8	336	20.9
483.xalancbmk	<b><u>189</u></b>	<b><u>36.6</u></b>	188	36.7	189	36.5	<b><u>189</u></b>	<b><u>36.6</u></b>	188	36.7	189	36.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to granularity=fine,scatter

## Platform Notes

Load Default BIOS Settings and then change the following  
Data Reuse Optimization Disabled  
Hardware Prefetch Enabled  
Adjacent Cache Line Prefetch Enabled  
L1 Data Prefetch Enabled

## General Notes

This result is measured on a Sun Fire X4170 M2 server. The Sun Fire X4170 M2 and the Sun Fire X4270 M2 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc -m64

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L(path to library) -lsmartheap64

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m64  
400.perlbench: icc -m32  
429.mcf: icc -m32

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## Peak Compiler Invocation (Continued)

445.gobmk: `icc -m32`

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m64`

471.omnetpp: `icpc -m32`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

401.bzip2: `-DSPEC_CPU_LP64`

403.gcc: `-DSPEC_CPU_LP64`

456.hmmer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

473.astar: `-DSPEC_CPU_LP64`

483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -ansi-alias -opt-prefetch`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div -static(pass 2) -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias`

403.gcc: `-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc -opt-malloc-options=3 -auto-ilp32`

429.mcf: `-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2 -ipo -no-prec-div -ansi-alias`

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -auto-ilp32`

458.sjeng: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4`

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## Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L(path to library) -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs  
-L(path to library) -lsmartheap64

483.xalanbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE-pathfix-smartheap.20101027.html>  
[http://www.spec.org/cpu2006/flags/Oracle-platform-x86\\_64.20101027.html](http://www.spec.org/cpu2006/flags/Oracle-platform-x86_64.20101027.html)

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE-pathfix-smartheap.20101027.xml>  
[http://www.spec.org/cpu2006/flags/Oracle-platform-x86\\_64.20101027.xml](http://www.spec.org/cpu2006/flags/Oracle-platform-x86_64.20101027.xml)

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