



SPEC® CFP2006 Result

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IBM Corporation

SPECfp®2006 = 30.3

IBM System x3690 X5 (Intel Xeon E7530)

SPECfp_base2006 = 28.4

CPU2006 license: 11

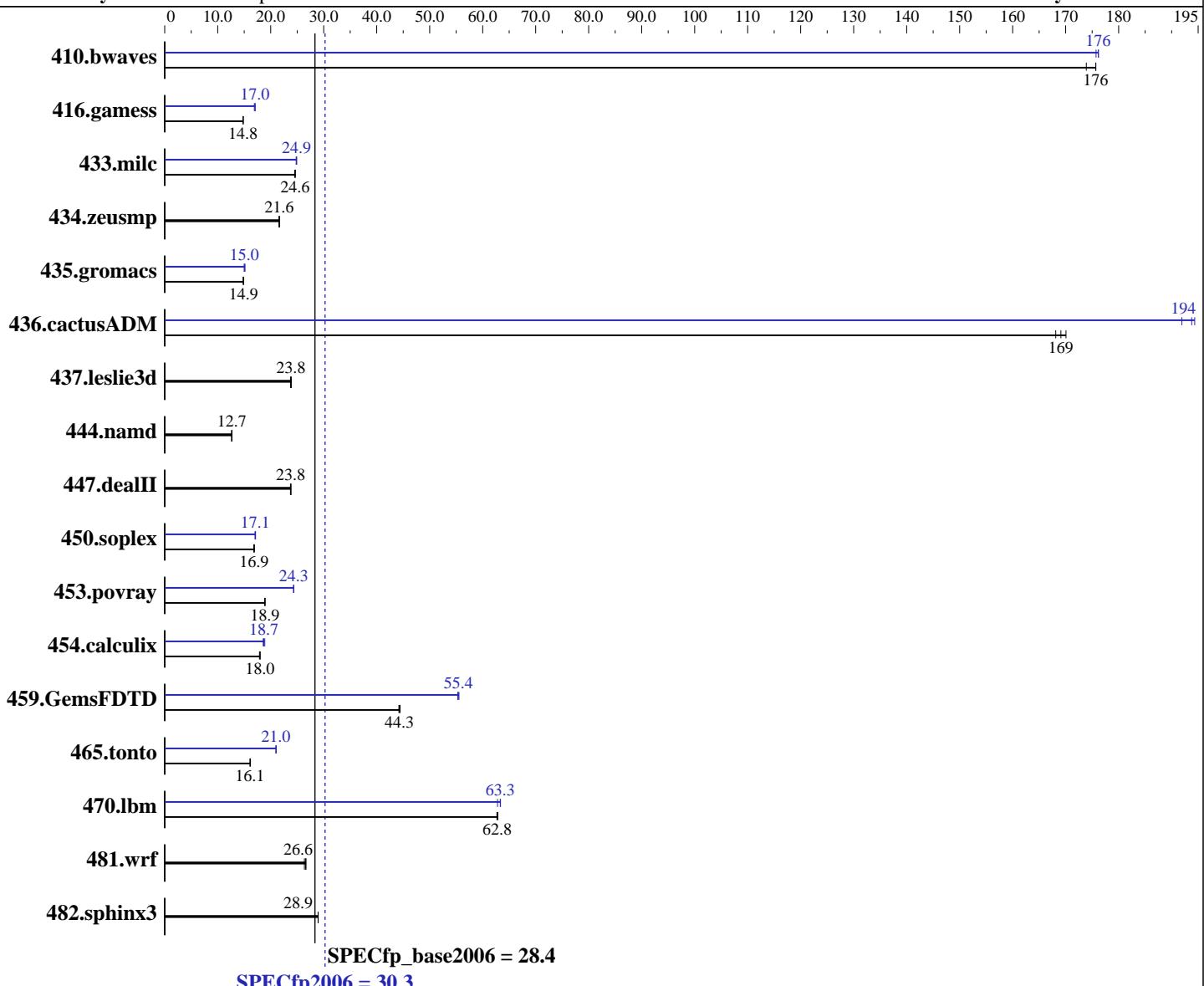
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jul-2010

Hardware Availability: Aug-2010

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon E7530
CPU Characteristics: Intel Turbo Boost Technology up to 2.13 GHz
CPU MHz: 1867
FPU: Integrated
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SuSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
Auto Parallel: Yes
File System: ext3
System State: Run level 3 (multi-user)

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L3 Cache: 12 MB I+D on chip per chip Other Cache: None Memory: 128 GB (32 x 4 GB PC3-8500R CL7, Quad Rank, running at 978 MHz) Disk Subsystem: 1 x 146 GB SAS, 15000 RPM Other Hardware: None	Base Pointers: 64-bit Peak Pointers: 32/64-bit Other Software: None	

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	78.1	174	77.3	176	77.4	176	77.1	176	77.3	176	77.1	176
416.gamess	1321	14.8	1326	14.8	1323	14.8	1147	17.1	1158	16.9	1149	17.0
433.milc	373	24.6	374	24.5	372	24.7	369	24.9	369	24.9	370	24.8
434.zeusmp	422	21.6	420	21.7	421	21.6	422	21.6	420	21.7	421	21.6
435.gromacs	480	14.9	479	14.9	483	14.8	475	15.0	476	15.0	471	15.2
436.cactusADM	71.1	168	70.7	169	70.3	170	62.3	192	61.5	194	61.7	194
437.leslie3d	394	23.8	396	23.7	394	23.9	394	23.8	396	23.7	394	23.9
444.namd	633	12.7	635	12.6	634	12.7	633	12.7	635	12.6	634	12.7
447.dealII	481	23.8	482	23.7	480	23.8	481	23.8	482	23.7	480	23.8
450.soplex	495	16.9	496	16.8	493	16.9	489	17.0	486	17.1	488	17.1
453.povray	281	19.0	281	18.9	282	18.9	219	24.3	218	24.4	219	24.3
454.calculix	459	18.0	461	17.9	458	18.0	444	18.6	441	18.7	437	18.9
459.GemsFDTD	240	44.3	240	44.2	239	44.4	191	55.5	191	55.4	192	55.3
465.tonto	611	16.1	611	16.1	608	16.2	468	21.0	469	21.0	469	21.0
470.lbm	219	62.9	219	62.7	219	62.8	219	62.8	217	63.4	217	63.3
481.wrf	424	26.4	419	26.7	420	26.6	424	26.4	419	26.7	420	26.6
482.sphinx3	688	28.3	673	29.0	674	28.9	688	28.3	673	29.0	674	28.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
echo 1 > /proc/sys/vm/zone_reclaim_mode
```

Platform Notes

Turbo Boost set to Traditional

General Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter

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General Notes (Continued)

KMP_STACKSIZE set to 200M

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-ansi-alias

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-parallel -ansi-alias -auto-ilp32

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: basepeak = yes

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -auto-ilp32

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Peak Optimization Flags (Continued)

453.povray: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xsSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
 -parallel

416.gamess: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -opt-prefetch -auto-ilp32

436.cactusADM: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xsSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100603.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100603.xml>



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