SPEC® CINT2006 Result

Fujitsu

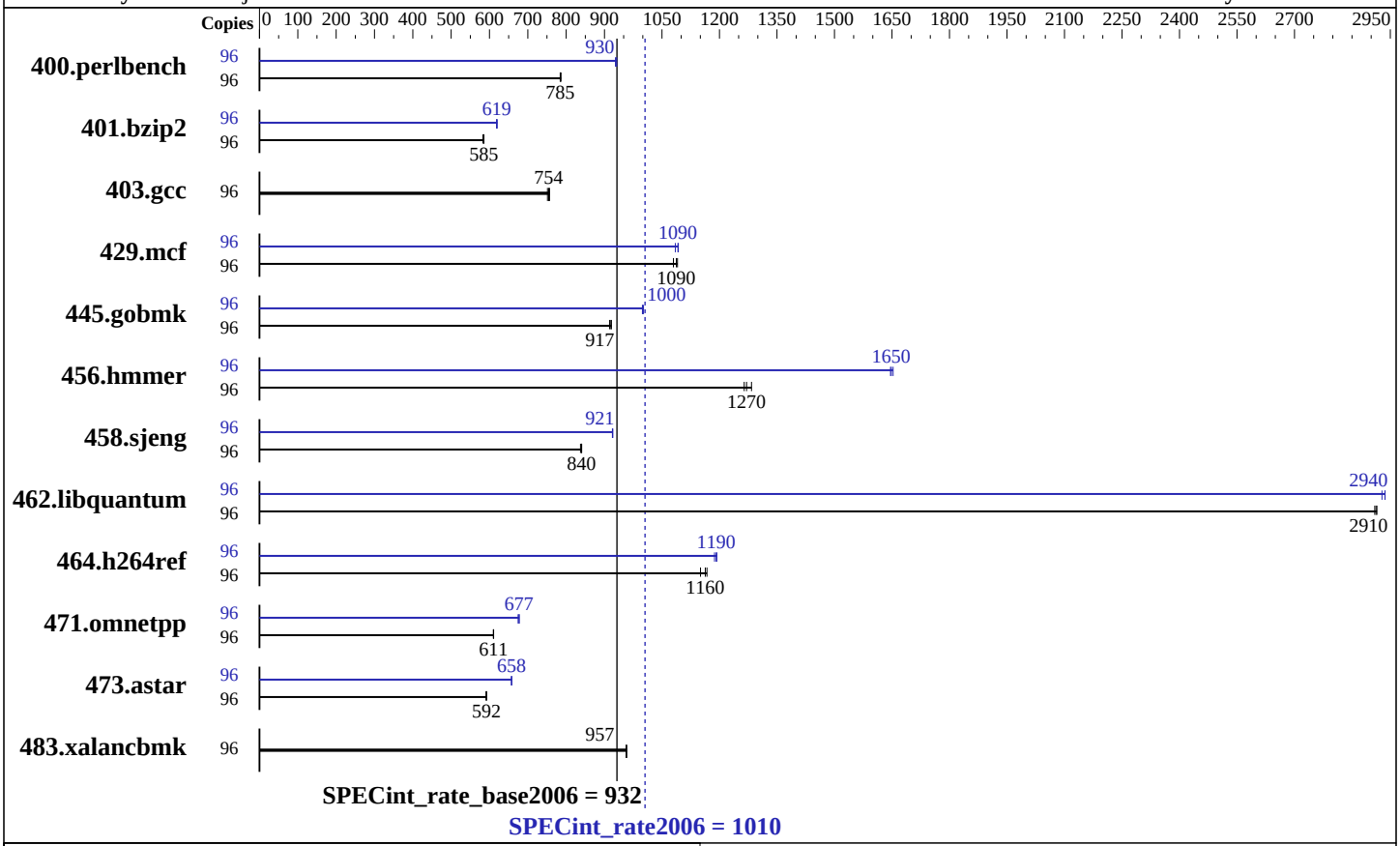
PRIMERGY RX900 S1, Intel Xeon E7540, 2.00 GHz

SPECint®_rate2006 = 1010

SPECint_rate_base2006 = 932

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Aug-2010
Hardware Availability: Aug-2010
Software Availability: Feb-2010



Hardware

CPU Name:	Intel Xeon E7540
CPU Characteristics:	Intel Turbo Boost Technology up to 2.26 GHz
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	48 cores, 8 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	4,6,8 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	18 MB I+D on chip per chip
Other Cache:	None
Memory:	1 TB (128 x 8 GB DDR3-1066 DIMMs)
Disk Subsystem:	2 x 147 GB (SAS, 15000 RPM, RAID0)
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20100203 Package ID: l_cproc_p_11.1.069
Auto Parallel:	No
File System:	ext2
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.0

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	96	1196	784	**1195**	**785**	1192	787	96	1010	929	1006	932	**1009**	**930**
401.bzip2	96	1589	583	1582	586	**1582**	**585**	96	1498	619	**1497**	**619**	1494	620
403.gcc	96	**1024**	**754**	1021	757	1029	751	96	**1024**	**754**	1021	757	1029	751
429.mcf	96	803	1090	**805**	**1090**	811	1080	96	**802**	**1090**	801	1090	807	1080
445.gobmk	96	**1099**	**917**	1103	913	1097	918	96	1005	1000	1008	999	**1007**	**1000**
456.hmmer	96	**705**	**1270**	708	1260	698	1280	96	**543**	**1650**	542	1650	544	1650
458.sjeng	96	1387	837	1382	841	**1383**	**840**	96	1262	921	**1261**	**921**	1260	922
462.libquantum	96	684	2910	**683**	**2910**	682	2920	96	679	2930	**678**	**2940**	677	2940
464.h264ref	96	1820	1170	1847	1150	**1827**	**1160**	96	1781	1190	1790	1190	**1783**	**1190**
471.omnetpp	96	982	611	**983**	**611**	984	610	96	885	678	**886**	**677**	889	675
473.astar	96	**1139**	**592**	1140	591	1138	592	96	**1025**	**658**	1025	657	1024	658
483.xalancbmk	96	691	958	692	957	**692**	**957**	96	691	958	692	957	**692**	**957**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
The following command was used prior to run

   ulimit -s unlimited
   echo 1 > /proc/sys/vm/zone_reclaim_mode
```

Base Compiler Invocation

C benchmarks:
```
icc  -m32
```

C++ benchmarks:
```
icpc -m32
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`

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Base Portability Flags (Continued)

483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs`
`-L/opt/SmartHeap_9/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m32`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

462.libquantum: `icc -m64`

C++ benchmarks (except as noted below):
`icpc -m32`

473.astar: `icpc -m64`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`

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Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: basepeak = yes

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/opt/SmartHeap_9/lib -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/opt/SmartHeap_9_64/lib -lsmartheap64

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Peak Optimization Flags (Continued)

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Fujitsu.RX900.ic11.1-linux64.20100901.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Fujitsu.RX900.ic11.1-linux64.20100901.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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