SPEC® CFP2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

Gateway GW2000ht-GW170ht F1 (Intel Xeon L5609, 1.86 GHz)

$SPECfp®\_rate2006 =$ 120

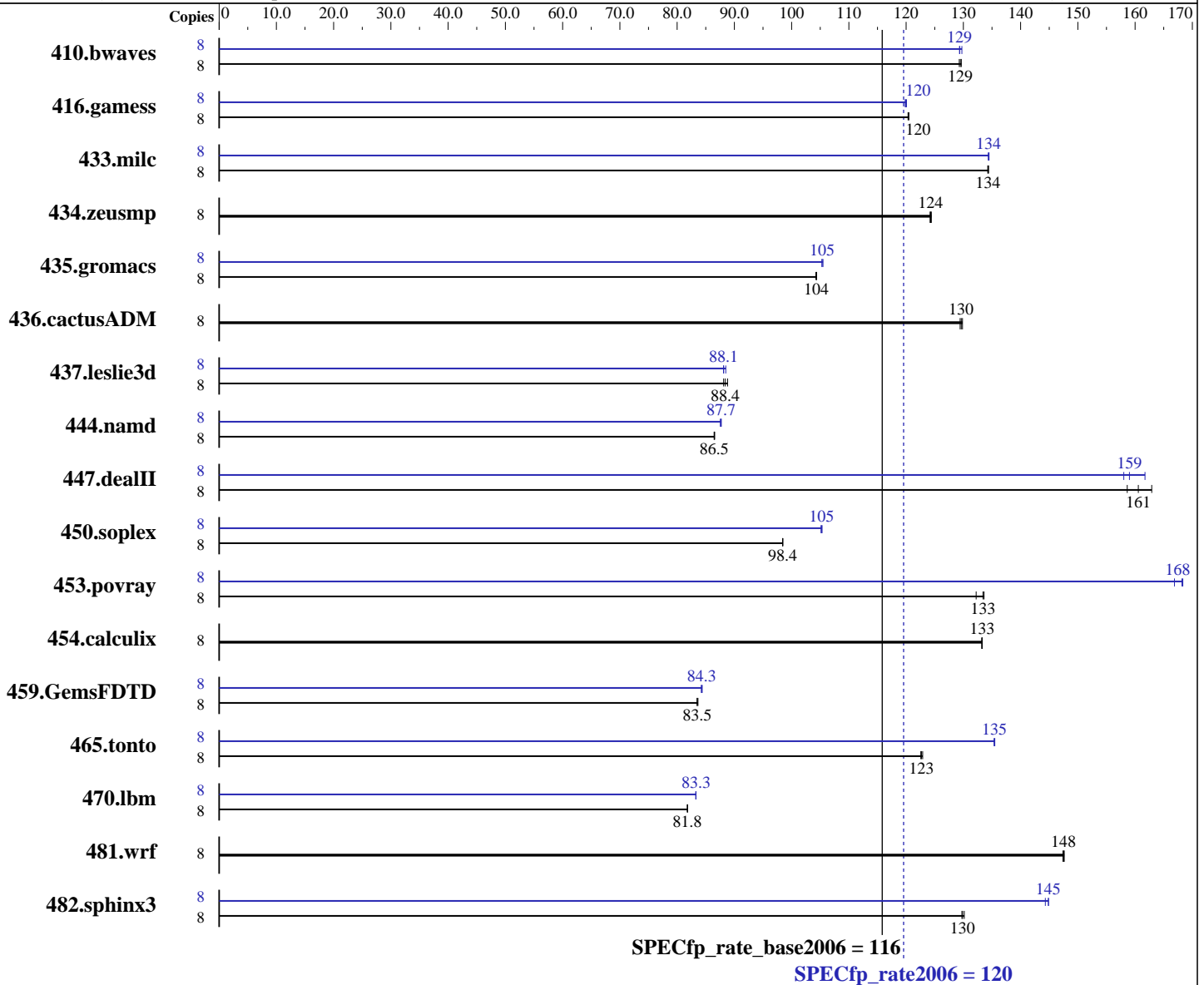$SPECfp\_rate\_base2006 =$ 116

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Jul-2010
Hardware Availability: Aug-2010
Software Availability: Jan-2010

SPECfp_rate_base2006 = 116

SPECfp_rate2006 = 120

Hardware

CPU Name:	Intel Xeon L5609
CPU Characteristics:	
CPU MHz:	1866
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) Kernel 2.6.27.19-5-default
Compiler:	Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)

Continued on next page

SPEC CFP2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

Gateway GW2000ht-GW170ht F1 (Intel Xeon L5609, 1.86 GHz)

SPECfp_rate2006 = 120

SPECfp_rate_base2006 = 116

CPU2006 license: 97	**Test date:** Jul-2010
Test sponsor: Acer Incorporated	**Hardware Availability:** Aug-2010
Tested by: Acer Incorporated	**Software Availability:** Jan-2010

L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (12 x 4 GB DDR3-1333 RDIMM, ECC, memory runs at 1066 MHz)
Disk Subsystem:	1 x 1000 GB SATA II, 7200 RPM
Other Hardware:	None

Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other Software:	None

Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	839	130	841	129	**840**	**129**	8	838	130	841	129	**841**	**129**
416.gamess	8	1300	120	1302	120	**1300**	**120**	8	**1306**	**120**	1307	120	1305	120
433.milc	8	**547**	**134**	547	134	546	134	8	547	134	546	134	**546**	**134**
434.zeusmp	8	586	124	585	124	**586**	**124**	8	586	124	585	124	**586**	**124**
435.gromacs	8	547	104	**547**	**104**	548	104	8	542	105	542	105	**542**	**105**
436.cactusADM	8	736	130	739	129	**737**	**130**	8	736	130	739	129	**737**	**130**
437.leslie3d	8	847	88.8	**851**	**88.4**	853	88.1	8	854	88.1	**853**	**88.1**	850	88.5
444.namd	8	742	86.5	741	86.6	**742**	**86.5**	8	**732**	**87.7**	733	87.5	732	87.7
447.dealII	8	**570**	**161**	577	159	562	163	8	566	162	579	158	**576**	**159**
450.soplex	8	678	98.4	**678**	**98.4**	677	98.5	8	635	105	**634**	**105**	634	105
453.povray	8	**319**	**133**	319	134	322	132	8	253	168	**253**	**168**	255	167
454.calculix	8	496	133	495	133	**495**	**133**	8	496	133	495	133	**495**	**133**
459.GemsFDTD	8	**1016**	**83.5**	1017	83.5	1015	83.6	8	1006	84.4	**1007**	**84.3**	1008	84.2
465.tonto	8	**642**	**123**	642	123	641	123	8	581	135	582	135	**581**	**135**
470.lbm	8	**1344**	**81.8**	1344	81.8	1343	81.8	8	1320	83.2	1319	83.3	**1320**	**83.3**
481.wrf	8	605	148	606	147	**606**	**148**	8	605	148	606	147	**606**	**148**
482.sphinx3	8	**1201**	**130**	1202	130	1198	130	8	1080	144	1076	145	**1076**	**145**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
```

General Notes

```
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

The Acer AW2000h-AW170h F1, Gateway GW2000h-GW170h F1, Acer AW2000ht-AW170ht F1
```

SPEC CFP2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

Gateway GW2000ht-GW170ht F1 (Intel Xeon L5609, 1.86 GHz)

SPECfp_rate2006 =   120

SPECfp_rate_base2006 =   116

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Jul-2010

Hardware Availability: Aug-2010

Software Availability: Jan-2010

General Notes (Continued)

and Gateway GW2000ht-GW170ht F1 are electronically equivalent.
This result was measured on Gateway GW2000ht-GW170ht F1.

Base Compiler Invocation

C benchmarks:
    icc   -m64

C++ benchmarks:
    icpc   -m64

Fortran benchmarks:
    ifort -m64

Benchmarks using both Fortran and C:
    icc   -m64 ifort -m64

Base Portability Flags

    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
  436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
     444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
  459.GemsFDTD: -DSPEC_CPU_LP64
     465.tonto: -DSPEC_CPU_LP64
      470.lbm: -DSPEC_CPU_LP64
      481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
    482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -static

Continued on next page

SPEC CFP2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

Gateway GW2000ht-GW170ht F1 (Intel Xeon L5609, 1.86 GHz)

SPECfp_rate2006 = 120

SPECfp_rate_base2006 = 116

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Jul-2010
Hardware Availability: Aug-2010
Software Availability: Jan-2010

Base Optimization Flags (Continued)

Fortran benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static
```

Benchmarks using both Fortran and C:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc    -m64
```

```
482.sphinx3: icc -m32
```

C++ benchmarks (except as noted below):
```
icpc   -m64
```

```
450.soplex: icpc -m32
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc    -m64 ifort -m64
```

Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

SPEC CFP2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

Gateway GW2000ht-GW170ht F1 (Intel Xeon L5609, 1.86 GHz)

$SPECfp\_rate2006 =$ 120

$SPECfp\_rate\_base2006 =$ 116

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Jul-2010
Hardware Availability: Aug-2010
Software Availability: Jan-2010

Peak Optimization Flags

C benchmarks:

> 433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
> -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
> -fno-alias -opt-prefetch

> 470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
> -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
> -opt-malloc-options=3 -ansi-alias -auto-ilp32

> 482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

> 444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
> -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
> -fno-alias -auto-ilp32

> 447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
> -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
> -unroll2 -ansi-alias -scalar-rep-

> 450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
> -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
> -opt-malloc-options=3

> 453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
> -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
> -unroll4 -ansi-alias

Fortran benchmarks:

> 410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

> 416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
> -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
> -unroll2 -Ob0 -ansi-alias -scalar-rep-

> 434.zeusmp: basepeak = yes

> 437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static

> 459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
> -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
> -unroll2 -Ob0

> 465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
> -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
> -unroll4 -auto -inline-calloc -opt-malloc-options=3

Acer Incorporated

Gateway GW2000ht-GW170ht F1 (Intel Xeon L5609, 1.86 GHz)

$SPECfp_rate2006 = $ 120

$SPECfp_rate_base2006 = $ 116

CPU2006 license: 97		**Test date:**	Jul-2010
Test sponsor:	Acer Incorporated	**Hardware Availability:**	Aug-2010
Tested by:	Acer Incorporated	**Software Availability:**	Jan-2010

Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

```
435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
             -opt-prefetch -auto-ilp32
```

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 11:55:14 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 17 August 2010.