



SPEC® CINT2006 Result

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Fujitsu

SPECint_rate2006 = 1390

PRIMERGY RX900 S1, Intel Xeon X7560, 2.26 GHz

SPECint_rate_base2006 = 1300

CPU2006 license: 19

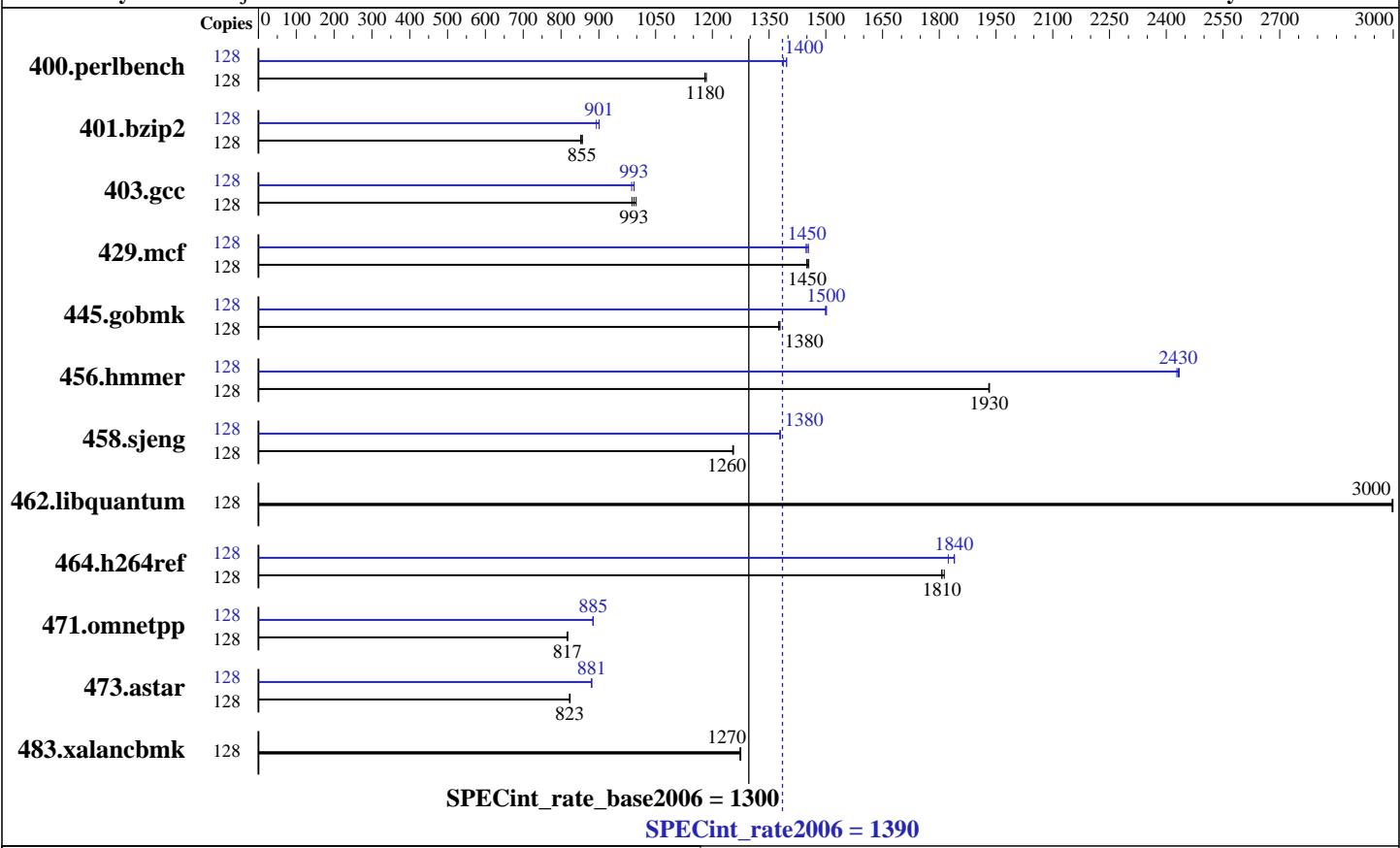
Test date: Jul-2010

Test sponsor: Fujitsu

Hardware Availability: Aug-2010

Tested by: Fujitsu

Software Availability: Feb-2010



Hardware

CPU Name: Intel Xeon X7560
CPU Characteristics: Intel Turbo Boost Technology up to 2.67 GHz
CPU MHz: 2267
FPU: Integrated
CPU(s) enabled: 64 cores, 8 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable: 4,6,8 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 24 MB I+D on chip per chip
Other Cache: None
Memory: 1 TB (128x 8GB DDR3-1066 DIMMs)
Disk Subsystem: 2 x 147 GB (SAS, 15000 RPM, RAID0)
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20100203 Package ID: l_cproc_p_11.1.069
Auto Parallel: No
File System: ext2
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V9.0



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	128	1059	1180	1059	1180	1056	1180	128	896	1400	896	1400	901	1390
401.bzip2	128	1450	852	1444	855	1443	856	128	1383	893	1371	901	1371	901
403.gcc	128	1038	993	1043	988	1032	999	128	1044	987	1037	993	1038	993
429.mcf	128	804	1450	802	1450	805	1450	128	805	1450	806	1450	803	1450
445.gobmk	128	976	1380	976	1380	974	1380	128	894	1500	894	1500	896	1500
456.hammer	128	618	1930	618	1930	618	1930	128	492	2430	491	2430	491	2430
458.sjeng	128	1233	1260	1234	1260	1235	1250	128	1123	1380	1123	1380	1123	1380
462.libquantum	128	885	3000	884	3000	885	3000	128	885	3000	884	3000	885	3000
464.h264ref	128	1562	1810	1568	1810	1568	1810	128	1540	1840	1553	1820	1539	1840
471.omnetpp	128	979	817	979	817	980	816	128	903	885	904	885	905	884
473.astar	128	1091	824	1092	823	1092	823	128	1021	880	1020	881	1020	881
483.xalancbmk	128	693	1280	693	1270	694	1270	128	693	1280	693	1270	694	1270

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

The following command was used prior to run

```
ulimit -s unlimited
echo 1 > /proc/sys/vm/zone_reclaim_mode
```

General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:

```
icc -m32
```

C++ benchmarks:

```
icpc -m32
```



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/opt/SmartHeap_9/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

401.bzip2: icc -m64

456.hmmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

456.hmmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

456.hmmr: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/opt/SmartHeap_9/lib -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
-L/opt/SmartHeap_9_64/lib -lsmartheap64

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=__alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Fujitsu.RX900.ic11.1-linux64.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Fujitsu.RX900.ic11.1-linux64.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

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