



# SPEC® CINT2006 Result

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## Fujitsu

SPECint®\_rate2006 = 145

PRIMERGY RX200 S6, Intel Xeon X5667, 3.06 GHz

SPECint\_rate\_base2006 = 137

CPU2006 license: 19

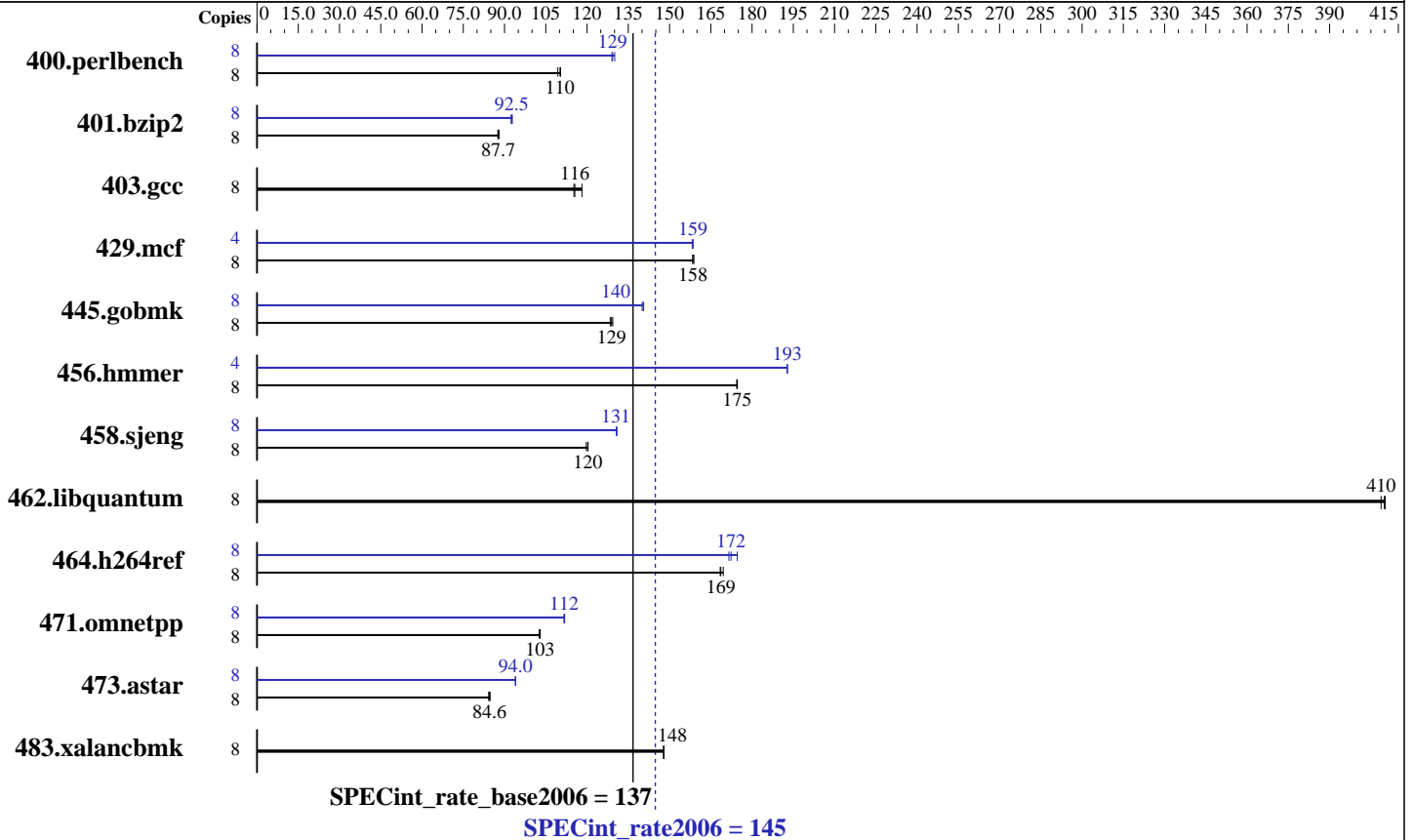
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: May-2010

Hardware Availability: Oct-2010

Software Availability: Jan-2010



### Hardware

CPU Name: Intel Xeon X5667  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.46 GHz  
 CPU MHz: 3067  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 24 GB (6x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)  
 Disk Subsystem: 1 x SATA, 160 GB, 5400 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-default  
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l\_cproc\_p\_11.1.064  
 Auto Parallel: No  
 File System: ext3  
 System State: Multi-User Run Level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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### Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	708	110	<b><u>709</u></b>	<b><u>110</u></b>	715	109	8	<b><u>604</u></b>	<b><u>129</u></b>	606	129	601	130
401.bzip2	8	877	88.0	882	87.6	<b><u>880</u></b>	<b><u>87.7</u></b>	8	836	92.4	832	92.8	<b><u>834</u></b>	<b><u>92.5</u></b>
403.gcc	8	545	118	559	115	<b><u>557</u></b>	<b><u>116</u></b>	8	545	118	559	115	<b><u>557</u></b>	<b><u>116</u></b>
429.mcf	8	459	159	<b><u>460</u></b>	<b><u>158</u></b>	461	158	4	230	159	<b><u>230</u></b>	<b><u>159</u></b>	231	158
445.gobmk	8	653	128	<b><u>651</u></b>	<b><u>129</u></b>	649	129	8	<b><u>597</u></b>	<b><u>140</u></b>	597	141	599	140
456.hammer	8	428	174	427	175	<b><u>427</u></b>	<b><u>175</u></b>	4	194	193	194	193	<b><u>194</u></b>	<b><u>193</u></b>
458.sjeng	8	808	120	<b><u>805</u></b>	<b><u>120</u></b>	804	120	8	740	131	741	131	<b><u>740</u></b>	<b><u>131</u></b>
462.libquantum	8	406	409	<b><u>404</u></b>	<b><u>410</u></b>	404	410	8	406	409	<b><u>404</u></b>	<b><u>410</u></b>	404	410
464.h264ref	8	<b><u>1050</u></b>	<b><u>169</u></b>	1051	168	1044	170	8	1032	172	1014	175	<b><u>1027</u></b>	<b><u>172</u></b>
471.omnetpp	8	<b><u>486</u></b>	<b><u>103</u></b>	487	103	486	103	8	448	112	<b><u>448</u></b>	<b><u>112</u></b>	447	112
473.astar	8	663	84.7	<b><u>664</u></b>	<b><u>84.6</u></b>	667	84.2	8	597	94.0	598	94.0	<b><u>598</u></b>	<b><u>94.0</u></b>
483.xalancbmk	8	<b><u>373</u></b>	<b><u>148</u></b>	373	148	373	148	8	<b><u>373</u></b>	<b><u>148</u></b>	373	148	373	148

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The config file option 'submit' was used.

### Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

### Platform Notes

BIOS configuration:  
Data Reuse Optimization = Disable

### General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502  
For information about Fujitsu please visit: <http://www.fujitsu.com>

### Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.icl11.1/libic11.1-32bit -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

462.libquantum: -DSPEC\_CPU\_LINUX  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: basepeak = yes

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

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## Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.01.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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