



SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECfp®_rate2006 = 141

PRIMERGY BX920 S2, Intel Xeon L5630, 2.13 GHz

SPECfp_rate_base2006 = 136

CPU2006 license: 19

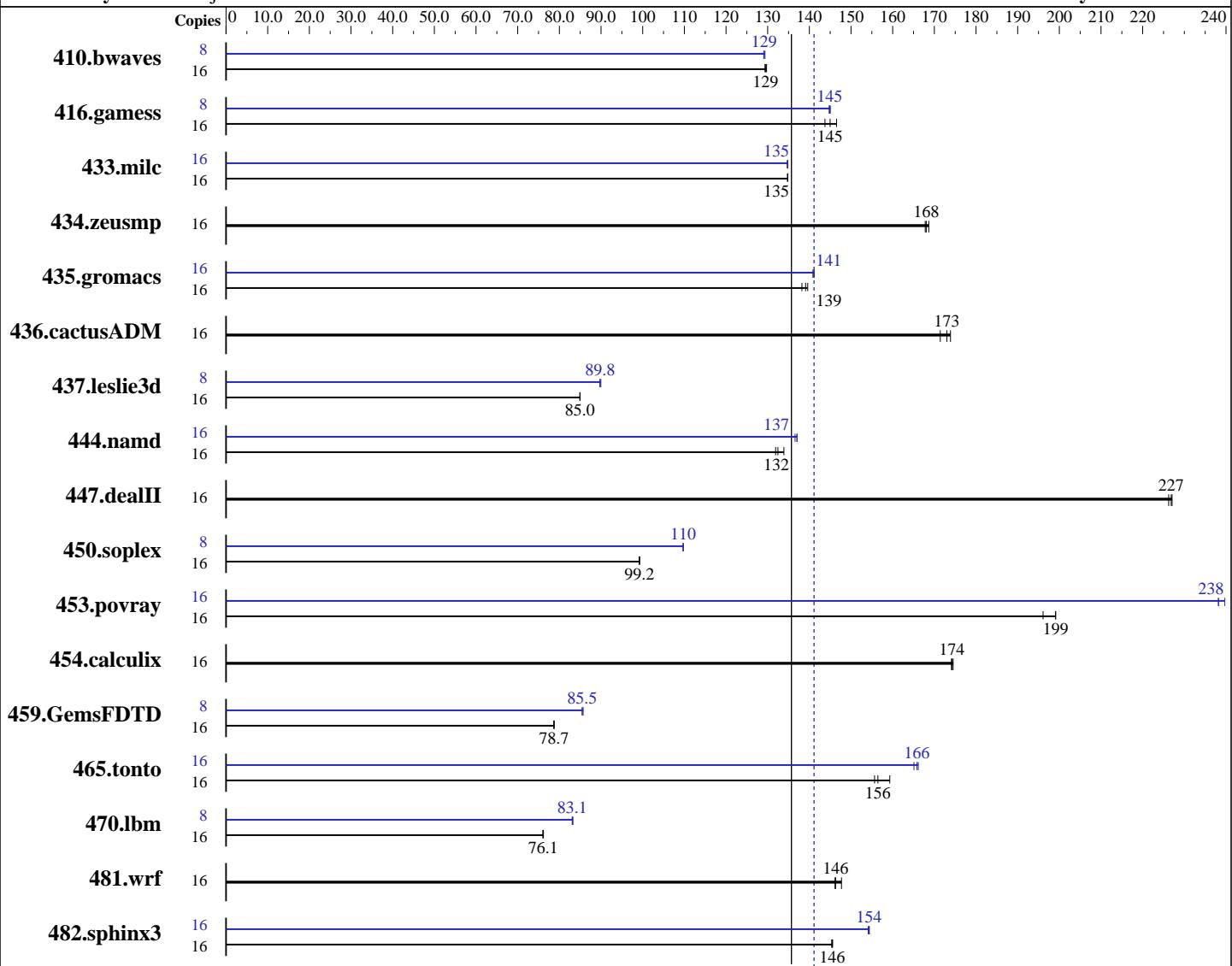
Test date: Jun-2010

Test sponsor: Fujitsu

Hardware Availability: Aug-2010

Tested by: Fujitsu

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon L5630
CPU Characteristics: Intel Turbo Boost Technology up to 2.40 GHz
CPU MHz: 2133
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
Auto Parallel: No
File System: ext3
System State: Multi-User Run Level 3

Continued on next page

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY BX920 S2, Intel Xeon L5630, 2.13 GHz

SPECfp_rate2006 = 141

CPU2006 license: 19

Test date: Jun-2010

Test sponsor: Fujitsu

Hardware Availability: Aug-2010

Tested by: Fujitsu

Software Availability: Jan-2010

L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (6x8 GB PC3-10600R, 2 rank, CL9-9-9, ECC,
 see add'l detail in notes)
 Disk Subsystem: 1 x SAS, 300 GB, 10000 RPM
 Other Hardware: None

Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	<u>1679</u>	<u>129</u>	1676	130	1681	129	8	<u>842</u>	<u>129</u>	842	129	840	129
416.gamess	16	<u>2161</u>	<u>145</u>	2138	147	2179	144	8	<u>1081</u>	<u>145</u>	1082	145	1080	145
433.milc	16	<u>1090</u>	<u>135</u>	1090	135	1090	135	16	<u>1089</u>	<u>135</u>	1090	135	<u>1090</u>	<u>135</u>
434.zeusmp	16	863	169	868	168	<u>866</u>	<u>168</u>	16	863	169	868	168	<u>866</u>	<u>168</u>
435.gromacs	16	<u>821</u>	<u>139</u>	818	140	827	138	16	<u>811</u>	<u>141</u>	809	141	811	141
436.cactusADM	16	1100	174	<u>1105</u>	<u>173</u>	1115	171	16	1100	174	<u>1105</u>	<u>173</u>	1115	171
437.leslie3d	16	1770	85.0	1771	84.9	<u>1770</u>	<u>85.0</u>	8	<u>838</u>	<u>89.8</u>	838	89.7	836	90.0
444.namd	16	958	134	<u>969</u>	<u>132</u>	973	132	16	936	137	940	137	<u>937</u>	<u>137</u>
447.dealII	16	<u>807</u>	<u>227</u>	809	226	806	227	16	<u>807</u>	<u>227</u>	809	226	806	227
450.soplex	16	1344	99.3	1345	99.2	<u>1345</u>	<u>99.2</u>	8	608	110	<u>608</u>	<u>110</u>	608	110
453.povray	16	<u>428</u>	<u>199</u>	434	196	428	199	16	355	240	<u>357</u>	<u>238</u>	357	238
454.calculix	16	758	174	<u>758</u>	<u>174</u>	756	175	16	758	174	<u>758</u>	<u>174</u>	756	175
459.GemsFDTD	16	<u>2157</u>	<u>78.7</u>	2158	78.7	2156	78.7	8	<u>993</u>	<u>85.5</u>	993	85.5	990	85.7
465.tonto	16	<u>1006</u>	<u>156</u>	1012	156	988	159	16	<u>949</u>	<u>166</u>	948	166	954	165
470.lbm	16	2886	76.2	<u>2888</u>	<u>76.1</u>	2891	76.0	8	1320	83.3	<u>1322</u>	<u>83.1</u>	1323	83.1
481.wrf	16	<u>1222</u>	<u>146</u>	1210	148	1223	146	16	<u>1222</u>	<u>146</u>	1210	148	1223	146
482.sphinx3	16	<u>2143</u>	<u>146</u>	2142	146	2145	145	16	2023	154	<u>2020</u>	<u>154</u>	2020	154

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
 numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

The system automatically configures the memory to run at 1067 MHz.
 BIOS configuration:

Data Reuse Optimization = Disable
 Performance/Power Setting = Traditional



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY BX920 S2, Intel Xeon L5630, 2.13 GHz

SPECfp_rate2006 = 141

CPU2006 license: 19

Test date: Jun-2010

Test sponsor: Fujitsu

Hardware Availability: Aug-2010

Tested by: Fujitsu

Software Availability: Jan-2010

General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:

 icc -m64

C++ benchmarks:

 icpc -m64

Fortran benchmarks:

 ifort -m64

Benchmarks using both Fortran and C:

 icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

 -xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:

 -xSSE4.2 -ipo -O3 -no-prec-div -static

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY BX920 S2, Intel Xeon L5630, 2.13 GHz

SPECfp_rate2006 = 141

CPU2006 license: 19

Test date: Jun-2010

Test sponsor: Fujitsu

Hardware Availability: Aug-2010

Tested by: Fujitsu

Software Availability: Jan-2010

Base Optimization Flags (Continued)

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY BX920 S2, Intel Xeon L5630, 2.13 GHz

SPECfp_rate2006 = 141

CPU2006 license: 19

Test date: Jun-2010

Test sponsor: Fujitsu

Hardware Availability: Aug-2010

Tested by: Fujitsu

Software Availability: Jan-2010

Peak Optimization Flags

C benchmarks:

```
433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -fno-alias -opt-prefetch
```

```
470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -opt-malloc-options=3 -ansi-alias -auto-ilp32
```

```
482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
```

C++ benchmarks:

```
444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -fno-alias -auto-ilp32
```

```
447.dealII: basepeak = yes
```

```
450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -opt-malloc-options=3
```

```
453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -unroll4 -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
```

```
416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -unroll2 -Ob0 -ansi-alias -scalar-rep-
```

```
434.zeusmp: basepeak = yes
```

```
437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static
```

```
459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -unroll2 -Ob0
```

```
465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -unroll4 -auto -inline-calloc -opt-malloc-options=3
```

Benchmarks using both Fortran and C:

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY BX920 S2, Intel Xeon L5630, 2.13 GHz

SPECfp_rate2006 = 141

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jun-2010

Hardware Availability: Aug-2010

Software Availability: Jan-2010

Peak Optimization Flags (Continued)

435.gromacs: -xSSE4_2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 11:24:40 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 20 July 2010.