SPEC® CINT2006 Result

Fujitsu

PRIMERGY BX922 S2, Intel Xeon X5667, 3.06 GHz

SPECint®_rate2006 = 145

SPECint_rate_base2006 = 137

CPU2006 license:	19	**Test date:**	May-2010
Test sponsor:	Fujitsu	**Hardware Availability:**	Mar-2010
Tested by:	Fujitsu	**Software Availability:**	Jan-2010

SPECint_rate_base2006 = 137

SPECint_rate2006 = 145

Hardware

CPU Name:	Intel Xeon X5667
CPU Characteristics:	Intel Turbo Boost Technology up to 3.46 GHz
CPU MHz:	3067
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	24 GB (6x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC)
Disk Subsystem:	1 x SSD SATA, 64 GB
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel:	No
File System:	ext3
System State:	Multi-User Run Level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY BX922 S2, Intel Xeon X5667, 3.06 GHz

SPECint_rate2006 = 145

SPECint_rate_base2006 = 137
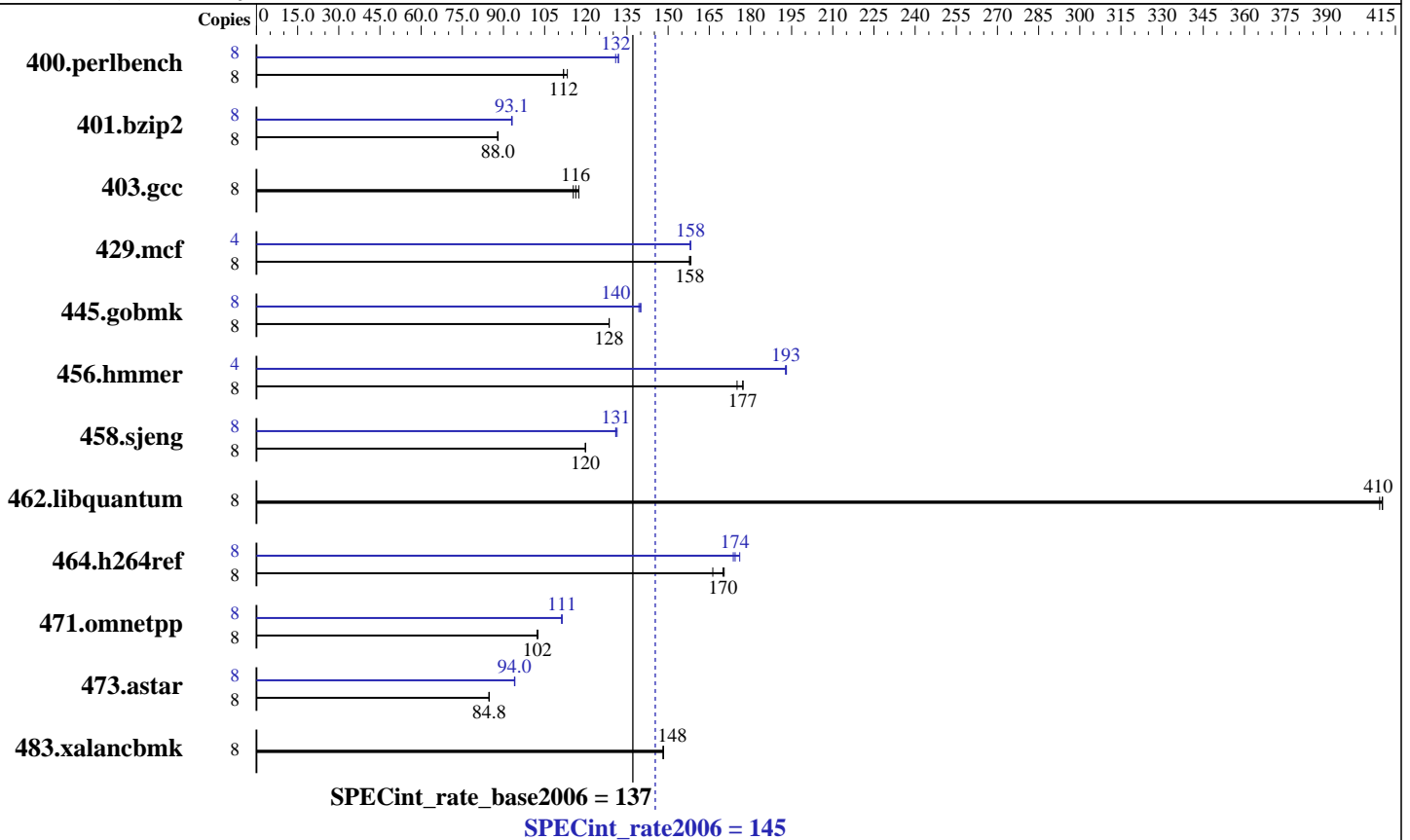
CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: May-2010
Hardware Availability: Mar-2010
Software Availability: Jan-2010

Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	690	113	699	112	**698**	**112**	8	597	131	593	132	**593**	**132**
401.bzip2	8	**877**	**88.0**	877	88.0	879	87.9	8	829	93.2	830	93.0	**829**	**93.1**
403.gcc	8	**553**	**116**	558	115	548	117	8	**553**	**116**	558	115	548	117
429.mcf	8	**462**	**158**	462	158	461	158	4	231	158	**231**	**158**	231	158
445.gobmk	8	653	128	**653**	**128**	653	129	8	**600**	**140**	602	139	599	140
456.hmmer	8	426	175	421	177	**421**	**177**	4	193	193	194	193	**193**	**193**
458.sjeng	8	808	120	807	120	**808**	**120**	8	739	131	737	131	**739**	**131**
462.libquantum	8	405	409	404	410	**404**	**410**	8	405	409	404	410	**404**	**410**
464.h264ref	8	**1041**	**170**	1039	170	1065	166	8	1005	176	1019	174	**1015**	**174**
471.omnetpp	8	488	103	489	102	**489**	**102**	8	450	111	**449**	**111**	449	111
473.astar	8	**662**	**84.8**	663	84.7	662	84.8	8	596	94.2	**597**	**94.0**	597	94.0
483.xalancbmk	8	372	148	**372**	**148**	373	148	8	372	148	**372**	**148**	373	148

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
     numactl was used to bind copies to the cores

Operating System Notes

     'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

    BIOS configuration:
    Data Reuse Optimization = Disable
    Performance/Power Setting = Traditional

General Notes

    For information about Fujitsu please visit: http://www.fujitsu.com
    Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:
    icc -m32

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY BX922 S2, Intel Xeon X5667, 3.06 GHz

$SPECint\_rate2006 =$ 145

$SPECint\_rate\_base2006 =$ 137

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: May-2010
Hardware Availability: Mar-2010
Software Availability: Jan-2010

Base Compiler Invocation (Continued)

C++ benchmarks:
    icpc -m32

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
    -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

Base Other Flags

C benchmarks:

    403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

    401.bzip2: icc -m64

    456.hmmer: icc -m64

    458.sjeng: icc -m64

C++ benchmarks (except as noted below):
    icpc -m32

    473.astar: icpc -m64

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY BX922 S2, Intel Xeon X5667, 3.06 GHz

SPECint_rate2006 = 145

SPECint_rate_base2006 = 137

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: May-2010
Hardware Availability: Mar-2010
Software Availability: Jan-2010

Peak Portability Flags

```
      400.perlbench: -DSPEC_CPU_LINUX_IA32
         401.bzip2: -DSPEC_CPU_LP64
         456.hmmer: -DSPEC_CPU_LP64
         458.sjeng: -DSPEC_CPU_LP64
     462.libquantum: -DSPEC_CPU_LINUX
         473.astar: -DSPEC_CPU_LP64
      483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
      400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                     -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                     -prof-use(pass 2) -ansi-alias

         401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                     -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                     -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

           403.gcc: basepeak = yes

           429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

         445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
                     -ipo -no-prec-div -ansi-alias

         456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
                     -ansi-alias -auto-ilp32

         458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                     -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                     -prof-use(pass 2) -unroll4 -auto-ilp32

     462.libquantum: basepeak = yes

         464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                     -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                     -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
        471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                     -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                     -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
                     -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap
```

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY BX922 S2, Intel Xeon X5667, 3.06 GHz

$SPECint\_rate2006 = \quad 145$

$SPECint\_rate\_base2006 = \quad 137$

CPU2006 license: 19	**Test date:** May-2010
Test sponsor: Fujitsu	**Hardware Availability:** Mar-2010
Tested by: Fujitsu	**Software Availability:** Jan-2010

Peak Optimization Flags (Continued)

473.astar: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
`-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64`

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 08:16:17 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 22 June 2010.