



# SPEC® CFP2006 Result

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**ACTION S.A.**

**SPECfp®\_rate2006 = 218**

ACTINA SOLAR 210 X3 (Intel Xeon X5650)

**SPECfp\_rate\_base2006 = 210**

CPU2006 license: 9008

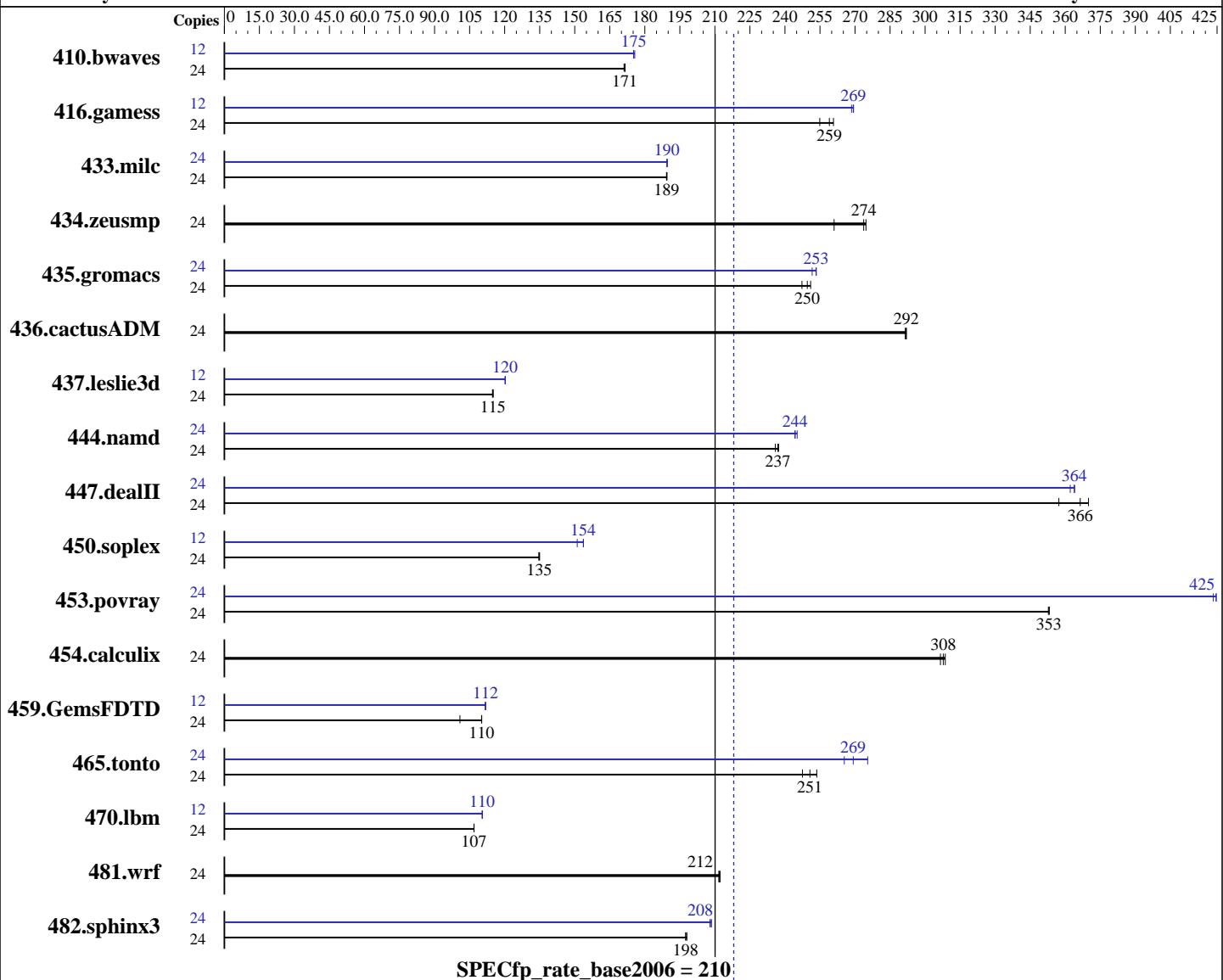
Test date: Apr-2010

Test sponsor: ACTION S.A.

Hardware Availability: Apr-2010

Tested by: ACTION S.A.

Software Availability: Jan-2010



## Hardware

CPU Name: Intel Xeon X5650  
CPU Characteristics: Intel Turbo Boost Technology up to 3.06 GHz  
CPU MHz: 2667  
FPU: Integrated  
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
CPU(s) orderable: 1,2 chips  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core

## Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-default  
Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l\_cproc\_p\_11.1.064, l\_cprof\_p\_11.1.064  
Auto Parallel: No  
File System: ReiserFS  
System State: Run level 3 (multi-user)

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L3 Cache: 12 MB I+D on chip per chip	Base Pointers: 64-bit	
Other Cache: None	Peak Pointers: 32/64-bit	
Memory: 48 GB (12 x 4 GB, PC3-8500R, ECC)	Other Software: Binutils 2.18.50.0.7.20080502	
Disk Subsystem: 1x 500 GB SATA, 7200 RPM		
Other Hardware: None		

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	1906	171	<b>1902</b>	<b>171</b>	1901	172	12	<b>931</b>	<b>175</b>	928	176	931	175
416.gamess	24	1802	261	1843	255	<b>1814</b>	<b>259</b>	12	875	269	872	269	<b>873</b>	<b>269</b>
433.milc	24	1163	190	1164	189	<b>1163</b>	<b>189</b>	24	<b>1163</b>	<b>190</b>	1163	189	1162	190
434.zeusmp	24	795	275	837	261	<b>798</b>	<b>274</b>	24	795	275	837	261	<b>798</b>	<b>274</b>
435.gromacs	24	693	247	<b>687</b>	<b>250</b>	683	251	24	<b>676</b>	<b>253</b>	676	253	681	252
436.cactusADM	24	984	292	<b>982</b>	<b>292</b>	982	292	24	984	292	<b>982</b>	<b>292</b>	982	292
437.leslie3d	24	1960	115	<b>1963</b>	<b>115</b>	1965	115	12	938	120	<b>938</b>	<b>120</b>	938	120
444.namd	24	811	237	816	236	<b>812</b>	<b>237</b>	24	788	244	<b>788</b>	<b>244</b>	785	245
447.dealII	24	<b>749</b>	<b>366</b>	742	370	768	357	24	754	364	<b>755</b>	<b>364</b>	758	362
450.soplex	24	1487	135	1483	135	<b>1484</b>	<b>135</b>	12	662	151	651	154	<b>651</b>	<b>154</b>
453.povray	24	<b>362</b>	<b>353</b>	362	353	362	353	24	302	423	301	425	<b>301</b>	<b>425</b>
454.calculix	24	<b>643</b>	<b>308</b>	642	309	646	307	24	<b>643</b>	<b>308</b>	642	309	646	307
459.GemsFDTD	24	2525	101	<b>2312</b>	<b>110</b>	2311	110	12	1140	112	<b>1139</b>	<b>112</b>	1138	112
465.tonto	24	<b>942</b>	<b>251</b>	954	247	931	254	24	890	265	857	275	<b>877</b>	<b>269</b>
470.lbm	24	3084	107	3086	107	<b>3086</b>	<b>107</b>	12	<b>1493</b>	<b>110</b>	1493	110	1493	110
481.wrf	24	<b>1266</b>	<b>212</b>	1266	212	1263	212	24	<b>1266</b>	<b>212</b>	1266	212	1263	212
482.sphinx3	24	2362	198	2368	198	<b>2368</b>	<b>198</b>	24	2244	208	<b>2244</b>	<b>208</b>	2250	208

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

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CPU2006 license: 9008	Test date:	Apr-2010
Test sponsor: ACTION S.A.	Hardware Availability:	Apr-2010
Tested by: ACTION S.A.	Software Availability:	Jan-2010

## Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

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## Peak Compiler Invocation (Continued)

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4\_2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-fno-alias -opt-prefetch

470.lbm: -xSSE4\_2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3 -ansi-alias -auto-ilp32

482.sphinx3: -xSSE4\_2 -ipo -O3 -no-prec-div -static -unroll12

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Tested by: ACTION S.A.	Software Availability:	Jan-2010

## Peak Optimization Flags (Continued)

C++ benchmarks:

```
444.namd: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -fno-alias -auto-ilp32

447.dealII: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
            -unroll12 -ansi-alias -scalar-rep

450.soplex: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
            -opt-malloc-options=3

453.povray: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
            -unroll14 -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: -xsSE4 .2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
            -unroll12 -Ob0 -ansi-alias -scalar-rep

434.zeusmp: basepeak = yes

437.leslie3d: -xsSE4 .2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
               -unroll12 -Ob0

465.tonto: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
            -unroll14 -auto -inline-calloc -opt-malloc-options=3
```

Benchmarks using both Fortran and C:

```
435.gromacs: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
              -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes
```



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

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