



# SPEC® CINT2006 Result

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**ACTION S.A.**

**SPECint®\_rate2006 = 146**

**ACTINA SOLAR 210 X3 (Intel Xeon L5609)**

**SPECint\_rate\_base2006 = 135**

CPU2006 license: 9008

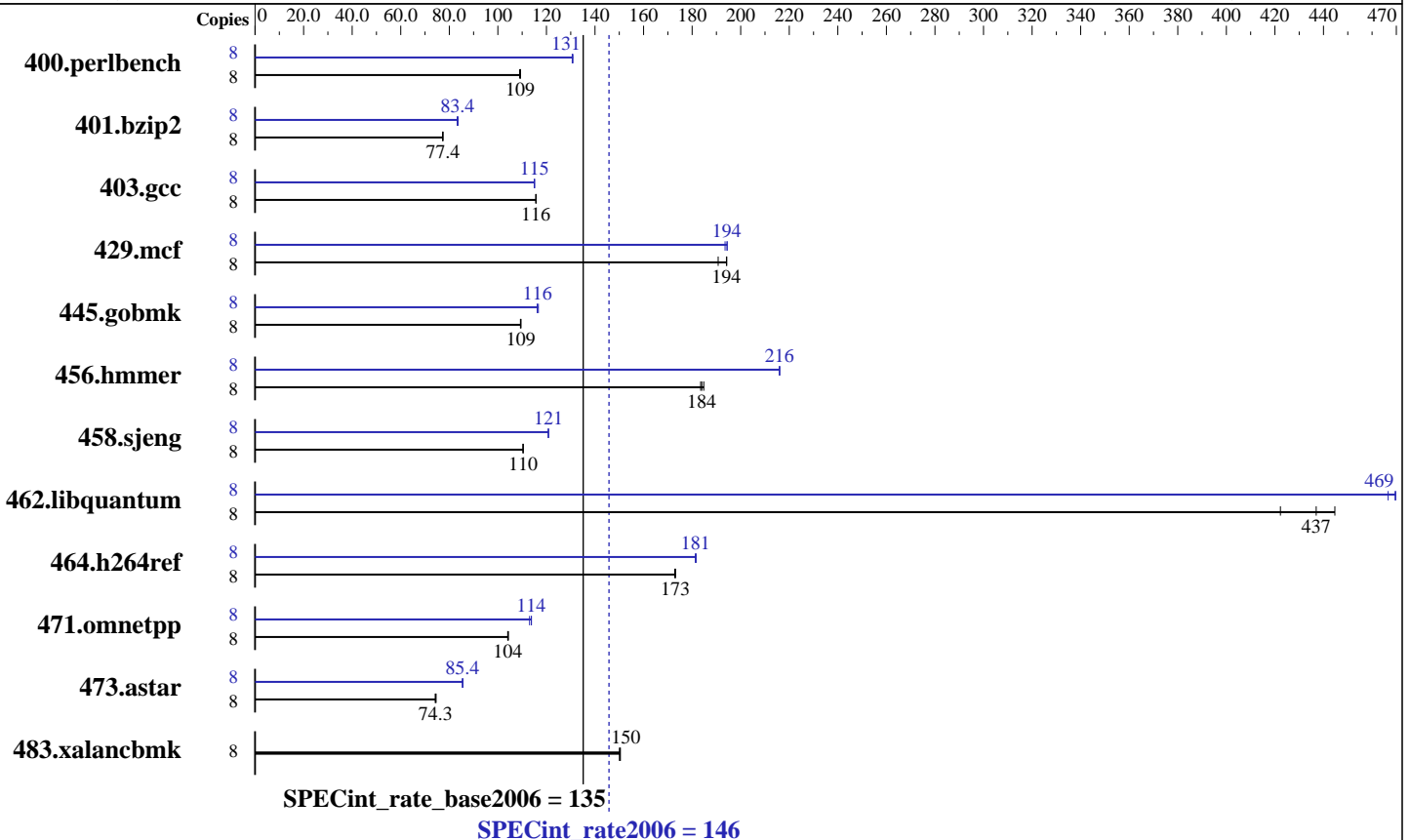
Test sponsor: ACTION S.A.

Tested by: ACTION S.A.

Test date: Apr-2010

Hardware Availability: Apr-2010

Software Availability: Jan-2010



## Hardware

CPU Name: Intel Xeon L5609  
 CPU Characteristics:  
 CPU MHz: 1867  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12 x 4 GB, PC3-8500R, ECC)  
 Disk Subsystem: 1x 500 GB SATA, 7200 RPM  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-default  
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1  
 Build 20091130 Package ID: l\_cproc\_p\_11.1.064  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1  
 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	<b><u>715</u></b>	<b><u>109</u></b>	715	109	718	109	8	597	131	598	131	<b><u>598</u></b>	<b><u>131</u></b>
401.bzip2	8	997	77.4	<b><u>998</u></b>	<b><u>77.4</u></b>	1000	77.2	8	<b><u>926</u></b>	<b><u>83.4</u></b>	923	83.6	927	83.3
403.gcc	8	<b><u>557</u></b>	<b><u>116</u></b>	557	116	556	116	8	559	115	560	115	<b><u>560</u></b>	<b><u>115</u></b>
429.mcf	8	383	191	<b><u>376</u></b>	<b><u>194</u></b>	376	194	8	375	194	377	194	<b><u>375</u></b>	<b><u>194</u></b>
445.gobmk	8	<b><u>767</u></b>	<b><u>109</u></b>	768	109	766	110	8	722	116	<b><u>722</u></b>	<b><u>116</u></b>	719	117
456.hammer	8	404	185	<b><u>406</u></b>	<b><u>184</u></b>	407	184	8	346	216	<b><u>346</u></b>	<b><u>216</u></b>	345	216
458.sjeng	8	877	110	877	110	<b><u>877</u></b>	<b><u>110</u></b>	8	<b><u>801</u></b>	<b><u>121</u></b>	801	121	802	121
462.libquantum	8	393	422	<b><u>379</u></b>	<b><u>437</u></b>	373	445	8	<b><u>353</u></b>	<b><u>469</u></b>	353	470	355	467
464.h264ref	8	1023	173	1024	173	<b><u>1023</u></b>	<b><u>173</u></b>	8	975	182	<b><u>976</u></b>	<b><u>181</u></b>	976	181
471.omnetpp	8	481	104	<b><u>480</u></b>	<b><u>104</u></b>	479	104	8	439	114	442	113	<b><u>439</u></b>	<b><u>114</u></b>
473.astar	8	757	74.2	753	74.6	<b><u>756</u></b>	<b><u>74.3</u></b>	8	656	85.6	658	85.4	<b><u>657</u></b>	<b><u>85.4</u></b>
483.xalanbmk	8	<b><u>367</u></b>	<b><u>150</u></b>	367	150	368	150	8	<b><u>367</u></b>	<b><u>150</u></b>	367	150	368	150

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Base Compiler Invocation

C benchmarks:  
icc -m32  
  
C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalanbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

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## Base Optimization Flags (Continued)

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmarheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m32
```

```
401.bzip2: icc -m64
```

```
456.hmmer: icc -m64
```

```
458.sjeng: icc -m64
```

```
462.libquantum: icc -m64
```

C++ benchmarks (except as noted below):

```
icpc -m32
```

```
473.astar: icpc -m64
```

## Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
```

```
401.bzip2: -DSPEC_CPU_LP64
```

```
456.hmmer: -DSPEC_CPU_LP64
```

```
458.sjeng: -DSPEC_CPU_LP64
```

```
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

```
473.astar: -DSPEC_CPU_LP64
```

```
483.xalancbmk: -DSPEC_CPU_LINUX
```

## Peak Optimization Flags

C benchmarks:

Continued on next page

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## Peak Optimization Flags (Continued)

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias

456.hmmcr: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32  
-opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

Continued on next page



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## Peak Other Flags (Continued)

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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