SPEC® CINT2006 Result
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Itautec
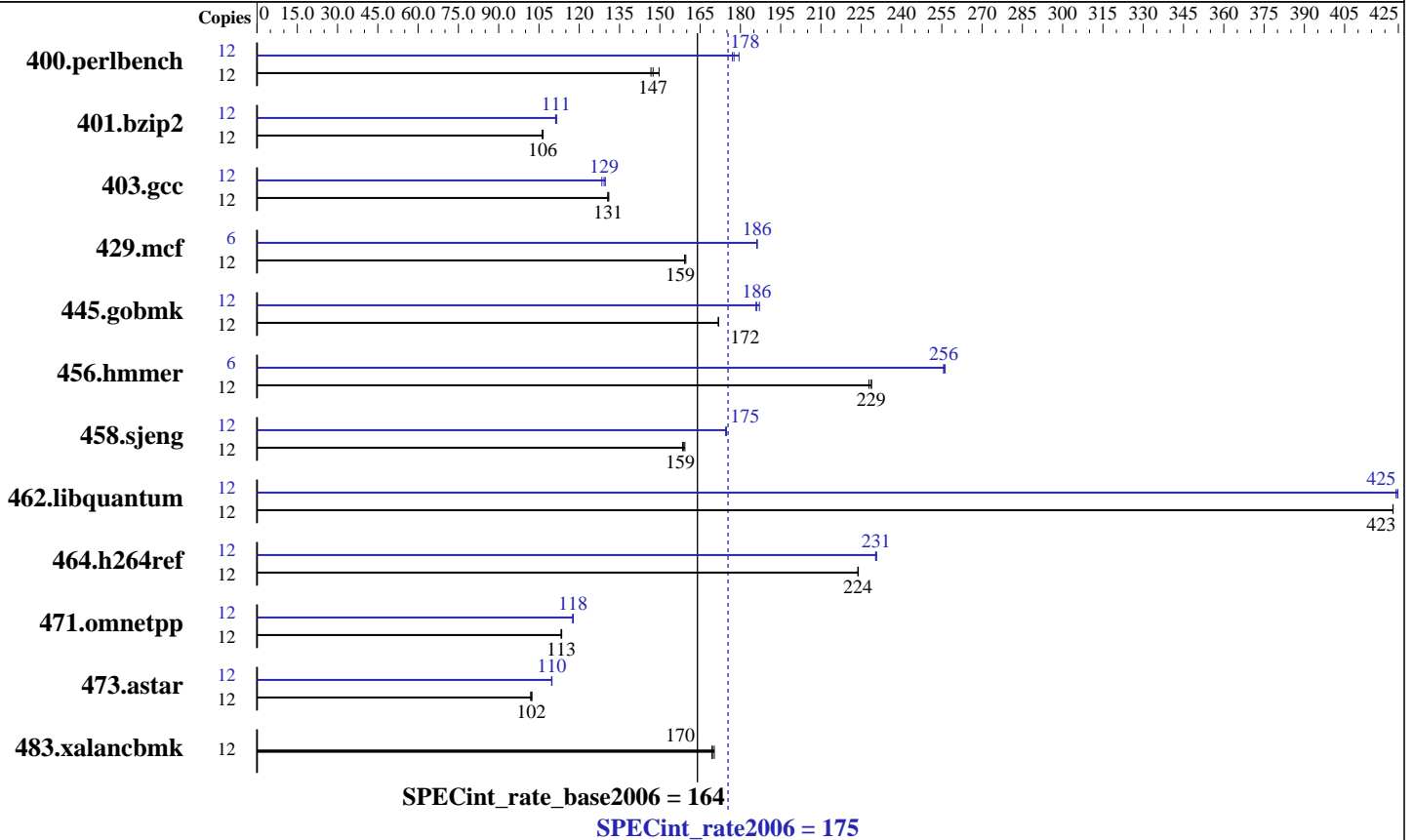
Servidor Itautec MX203 (Intel Xeon X5670)

SPECint®_rate2006 = 175

SPECint_rate_base2006 = 164

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Apr-2010
Hardware Availability: Apr-2010
Software Availability: Feb-2010

SPECint_rate_base2006 = 164

SPECint_rate2006 = 175

	Copies		
400.perlbench	12	178	
	12	147	
401.bzip2	12	111	
	12	106	
403.gcc	12	129	
	12	131	
429.mcf	6	186	
	12	159	
445.gobmk	12	186	
	12	172	
456.hmmer	6	256	
	12	229	
458.sjeng	12	175	
	12	159	
462.libquantum	12	425	
	12	423	
464.h264ref	12	231	
	12	224	
471.omnetpp	12	118	
	12	113	
473.astar	12	110	
	12	102	
483.xalancbmk	12	170	

Hardware

CPU Name:	Intel Xeon X5670
CPU Characteristics:	Intel Turbo Boost Technology up to 3.33 GHz
CPU MHz:	2933
FPU:	Integrated
CPU(s) enabled:	6 cores, 1 chip, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	12 GB (3 x 4GB, DDR3-1333, Dual Rank, CL 9, ECC)
Disk Subsystem:	1 x 160 GB SATA-2, 7200 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-smp
Compiler:	Intel C++ Professional Compiler 11.1 for Linux Build 20100203 Package ID: l_cproc_p_11.1.069
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	12	783	150	799	147	**795**	**147**	12	662	177	653	179	**660**	**178**
401.bzip2	12	1090	106	**1089**	**106**	1087	107	12	1042	111	**1040**	**111**	1037	112
403.gcc	12	**739**	**131**	740	131	737	131	12	752	128	**747**	**129**	745	130
429.mcf	12	685	160	687	159	**687**	**159**	6	294	186	**294**	**186**	294	186
445.gobmk	12	733	172	**733**	**172**	732	172	12	678	186	673	187	**676**	**186**
456.hmmer	12	491	228	489	229	**489**	**229**	6	219	256	218	256	**219**	**256**
458.sjeng	12	911	159	916	158	**914**	**159**	12	831	175	832	175	**831**	**175**
462.libquantum	12	**588**	**423**	588	423	588	423	12	**585**	**425**	585	425	586	424
464.h264ref	12	**1187**	**224**	1186	224	1187	224	12	**1152**	**231**	1153	230	1151	231
471.omnetpp	12	661	113	**662**	**113**	662	113	12	637	118	**638**	**118**	638	118
473.astar	12	823	102	**825**	**102**	827	102	12	**768**	**110**	768	110	767	110
483.xalancbmk	12	486	170	**488**	**170**	489	169	12	486	170	**488**	**170**	489	169

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
     numactl was used to bind copies to the cores

Operating System Notes

    'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.

General Notes

    This result was measured on the Servidor Itautec MX203.
    The Servidor Itautec MX223 and the Servidor Itautec MX203 are electronically equivalent.

Base Compiler Invocation

C benchmarks:
```
icc  -m32
```

C++ benchmarks:
```
icpc -m32
```

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Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc -m32
```

```
401.bzip2: icc -m64
```

```
456.hmmer: icc -m64
```

```
458.sjeng: icc -m64
```

```
462.libquantum: icc -m64
```

C++ benchmarks (except as noted below):
```
icpc -m32
```

```
473.astar: icpc -m64
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
```

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Peak Portability Flags (Continued)

```
    456.hmmer: -DSPEC_CPU_LP64
    458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
    473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
 400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                -prof-use(pass 2) -ansi-alias

     401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

       403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

       429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

     445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
                -ipo -no-prec-div -ansi-alias

     456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
                -ansi-alias -auto-ilp32

     458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
                -opt-prefetch

    464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
   471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
                -L/opt/sh/SmartHeap_8.1/lib -lsmartheap

     473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
```

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Peak Optimization Flags (Continued)

473.astar (continued):
`-L/opt/sh/SmartHeap_8.1/lib -lsmartheap64`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.1-linux64-revE.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.1-linux64-revE.xml

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