



SPEC® CFP2006 Result

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Fujitsu

PRIMERGY RX300 S6, Intel Xeon X5670, 2.93 GHz

SPECfp®2006 = 45.3

CPU2006 license: 19

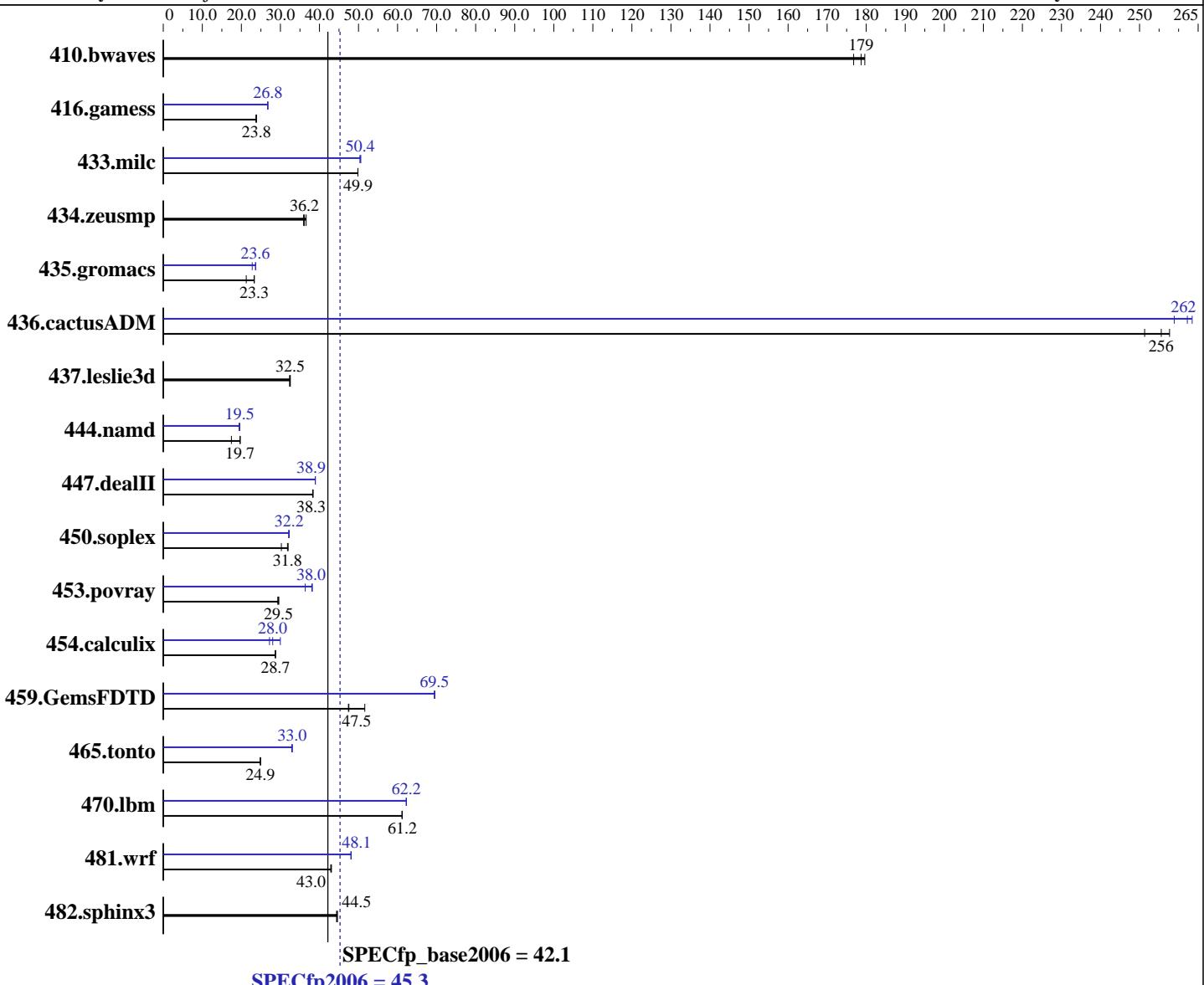
Test date: Mar-2010

Test sponsor: Fujitsu

Hardware Availability: Apr-2010

Tested by: Fujitsu

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon X5670
CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz
CPU MHz: 2933
FPU: Integrated
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
Auto Parallel: Yes
File System: ext3
System State: Multi-User Run Level 3

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L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 96 GB (12x8 GB PC3-10600R, 2 rank, CL9-9-9, ECC)
 Disk Subsystem: 1 x SATA, 160 GB, 5400 rpm
 Other Hardware: None

Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	76.9	177	76.0	<u>179</u>	75.7	180	76.9	177	76.0	<u>179</u>	75.7	180
416.gamess	825	23.7	821	23.8	821	<u>23.8</u>	732	26.8	731	26.8	731	<u>26.8</u>
433.milc	184	<u>49.9</u>	184	49.8	184	49.9	181	50.6	182	<u>50.4</u>	182	50.4
434.zeusmp	252	<u>36.2</u>	249	36.6	253	35.9	252	<u>36.2</u>	249	36.6	253	35.9
435.gromacs	306	23.3	307	<u>23.3</u>	336	21.3	302	23.7	313	22.8	302	<u>23.6</u>
436.cactusADM	46.8	<u>256</u>	46.4	258	47.6	251	45.6	<u>262</u>	46.2	259	45.4	263
437.leslie3d	289	<u>32.5</u>	291	32.3	289	32.5	289	<u>32.5</u>	291	32.3	289	32.5
444.namd	407	19.7	408	<u>19.7</u>	460	17.4	413	19.4	410	19.6	410	<u>19.5</u>
447.dealII	299	38.3	298	38.3	298	<u>38.3</u>	294	38.9	294	<u>38.9</u>	294	38.9
450.soplex	262	<u>31.8</u>	261	32.0	276	30.2	259	<u>32.2</u>	259	<u>32.2</u>	260	32.1
453.povray	180	29.6	181	29.3	180	<u>29.5</u>	139	38.2	146	36.4	140	<u>38.0</u>
454.calculix	287	<u>28.7</u>	287	28.7	287	28.7	275	30.0	294	<u>28.0</u>	303	27.2
459.GemsFDTD	223	<u>47.5</u>	224	47.4	206	51.6	153	<u>69.5</u>	153	69.4	153	69.5
465.tonto	396	<u>24.9</u>	396	24.9	396	24.9	298	33.0	299	32.9	298	<u>33.0</u>
470.lbm	224	61.2	224	<u>61.2</u>	225	61.1	221	<u>62.2</u>	221	62.2	221	62.3
481.wrf	260	42.9	259	43.1	260	<u>43.0</u>	232	48.1	232	48.1	232	<u>48.1</u>
482.sphinx3	438	<u>44.5</u>	439	44.4	437	44.6	438	<u>44.5</u>	439	44.4	437	44.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
 Data Reuse Optimization = Disable
 Intel HT Technology = Disable

General Notes

OMP_NUM_THREADS set to number of cores
 KMP_AFFINITY set to granularity=fine,scatter
 KMP_STACKSIZE set to 200M

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General Notes (Continued)

This result was measured on the PRIMERGY TX300 S6. The PRIMERGY TX300 S6 and the PRIMERGY RX300 S6 are electronically equivalent.

For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

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Base Optimization Flags (Continued)

Fortran benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -ansi-alias
```

```
470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -parallel -ansi-alias -auto-ilp32
```

```
482.sphinx3: basepeak = yes
```

C++ benchmarks:

```
444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -fno-alias -auto-ilp32
```

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PRIMERGY RX300 S6, Intel Xeon X5670, 2.93 GHz

SPECfp2006 =

45.3

SPECfp_base2006 =

42.1

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date:

Mar-2010

Hardware Availability: Apr-2010

Software Availability: Jan-2010

Peak Optimization Flags (Continued)

447.dealII: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep -auto-ilp32

450.soplex: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -auto-ilp32

453.povray: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll14 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-inline-calloc -opt-malloc-options=3 -auto -unroll14

Benchmarks using both Fortran and C:

435.gromacs: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xsSE4 .2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: Same as 454.calculix

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.xml>

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