



# SPEC® CFP2006 Result

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## Supermicro

### SPECfp®\_rate2006 = 248

Motherboard X8DTN+ (Intel Xeon X5670, 2.93 GHz)

### SPECfp\_rate\_base2006 = 239

CPU2006 license: 001176

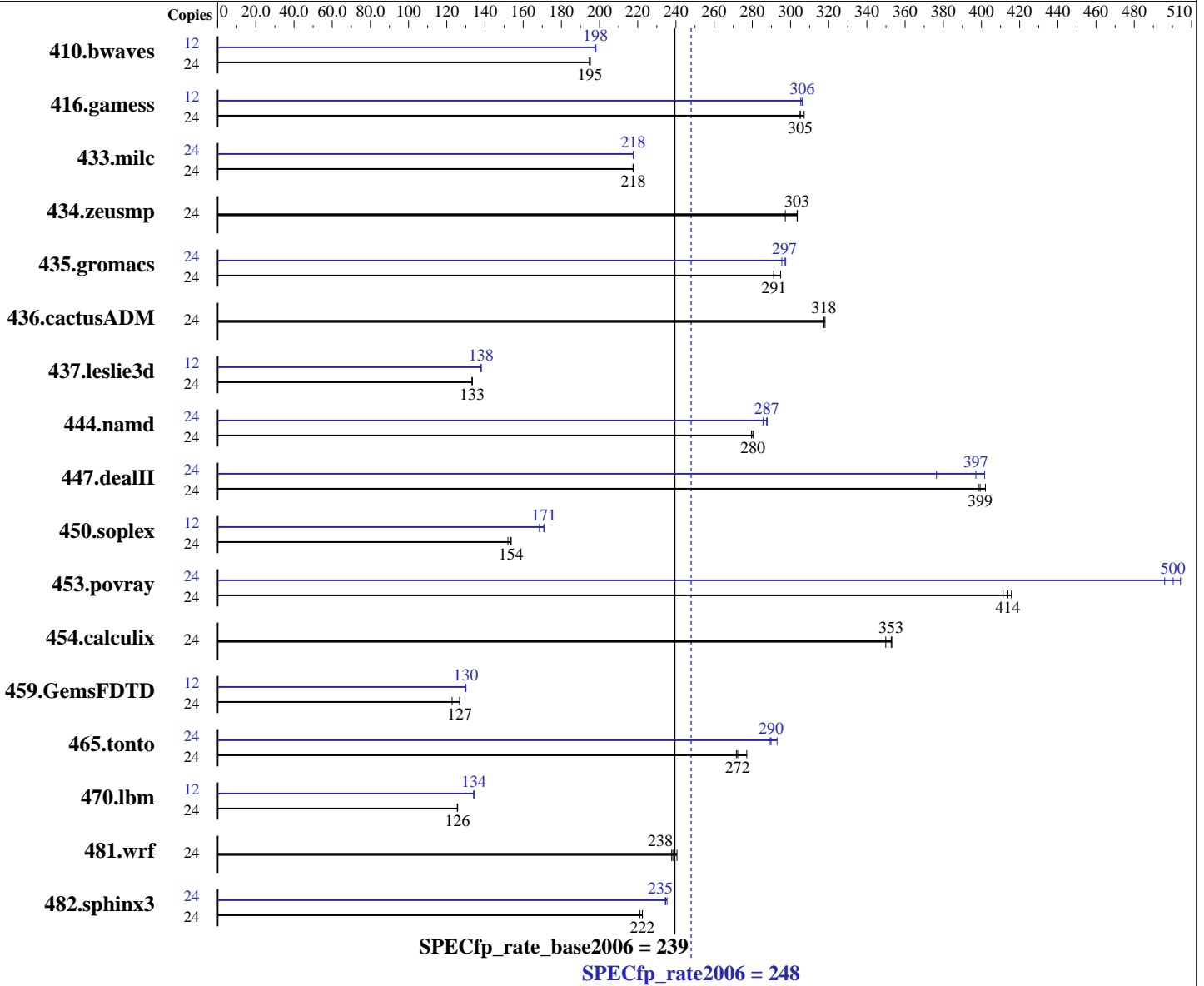
Test date: Mar-2010

Test sponsor: Supermicro

Hardware Availability: Mar-2010

Tested by: Supermicro

Software Availability: Jan-2010



### Hardware

CPU Name: Intel Xeon X5670  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz  
 CPU MHz: 2933  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 11 x86\_64  
 Kernel 2.6.27.19-5-default  
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1  
 Build 20091130 Package ID: l\_cproc\_p\_11.1.064, l\_cprof\_p\_11.1.064  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)

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L3 Cache: 12 MB I+D on chip per chip  
Other Cache: None  
Memory: 48 GB (12 x 4 GB DDR3-1333 RDIMM, ECC, CL9)  
Disk Subsystem: 1 x 500 GB SATA II, 7200 RPM  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	1676	195	1671	195	<b>1673</b>	<b>195</b>	12	825	198	823	198	<b>824</b>	<b>198</b>
416.gamess	24	1530	307	<b>1540</b>	<b>305</b>	1540	305	12	<b>768</b>	<b>306</b>	769	305	767	307
433.milc	24	1012	218	<b>1012</b>	<b>218</b>	1013	218	24	1012	218	<b>1012</b>	<b>218</b>	1013	218
434.zeusmp	24	<b>720</b>	<b>303</b>	735	297	719	304	24	<b>720</b>	<b>303</b>	735	297	719	304
435.gromacs	24	581	295	588	291	<b>588</b>	<b>291</b>	24	580	295	<b>577</b>	<b>297</b>	576	297
436.cactusADM	24	904	317	<b>903</b>	<b>318</b>	902	318	24	904	317	<b>903</b>	<b>318</b>	902	318
437.leslie3d	24	<b>1693</b>	<b>133</b>	1692	133	1693	133	12	817	138	818	138	<b>818</b>	<b>138</b>
444.namd	24	<b>687</b>	<b>280</b>	686	281	689	280	24	674	286	<b>670</b>	<b>287</b>	669	288
447.dealII	24	689	398	<b>688</b>	<b>399</b>	683	402	24	684	402	<b>692</b>	<b>397</b>	729	376
450.soplex	24	1317	152	1303	154	<b>1303</b>	<b>154</b>	12	594	169	<b>586</b>	<b>171</b>	586	171
453.povray	24	<b>309</b>	<b>414</b>	310	411	307	416	24	257	496	253	504	<b>255</b>	<b>500</b>
454.calculix	24	561	353	<b>561</b>	<b>353</b>	566	350	24	561	353	<b>561</b>	<b>353</b>	566	350
459.GemsFDTD	24	2074	123	<b>2009</b>	<b>127</b>	2008	127	12	979	130	981	130	<b>979</b>	<b>130</b>
465.tonto	24	<b>867</b>	<b>272</b>	869	272	852	277	24	816	289	806	293	<b>815</b>	<b>290</b>
470.lbm	24	2623	126	<b>2625</b>	<b>126</b>	2628	125	12	1227	134	<b>1229</b>	<b>134</b>	1229	134
481.wrf	24	1115	241	1128	238	<b>1124</b>	<b>238</b>	24	1115	241	1128	238	<b>1124</b>	<b>238</b>
482.sphinx3	24	2115	221	2102	222	<b>2103</b>	<b>222</b>	24	1988	235	1995	234	<b>1994</b>	<b>235</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

## Platform Notes

Fan speed set to Full Speed in BIOS Setup.  
As tested, the system used a Supermicro  
PWS-865-PQ power supply, 2 SNK-P0038P heatsinks,  
and 2 FAN-0077L cooling fans.



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## General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static

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## Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:  
-xSSE4.2 -ipo -O3 -no-prec-div -static

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m64  
  
482.sphinx3: icc -m32  
  
C++ benchmarks (except as noted below):  
icpc -m64  
  
450.soplex: icpc -m32  
  
Fortran benchmarks:  
ifort -m64  
  
Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.deallI: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

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## Peak Optimization Flags (Continued)

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-fno-alias -opt-prefetch

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3 -ansi-alias -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

### C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-fno-alias -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias -scalar-rep-

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -Ob0

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll4 -auto -inline-calloc -opt-malloc-options=3

### Benchmarks using both Fortran and C:

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## Peak Optimization Flags (Continued)

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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