SPEC® CFP2006 Result

Intel Corporation

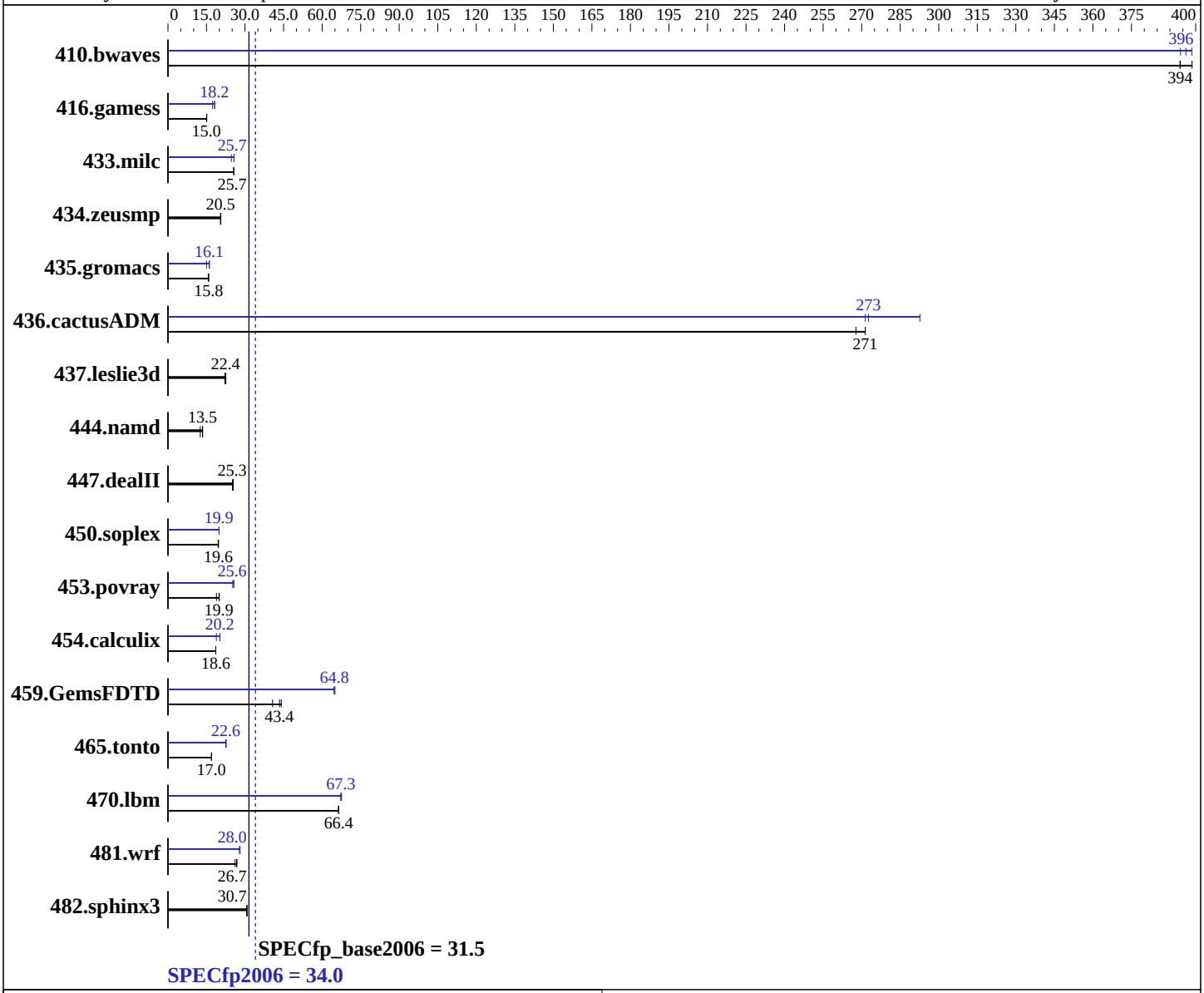
Intel QSSC-S4R (Intel Xeon E7540, 2.00 GHz)

SPECfp®2006 = 34.0

SPECfp_base2006 = 31.5

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Mar-2010
Hardware Availability: Mar-2010
Software Availability: Jan-2010



Hardware

CPU Name:	Intel Xeon E7540
CPU Characteristics:	Intel Turbo Boost Technology up to 2.26 GHz
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	24 cores, 4 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	SuSe Linux Enterprise Server 11 Kernel 2.6.27.19-5 on x86_64
Compiler:	Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multi-user)

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L3 Cache: 18 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (64x 4GB Quad-Rank DDR3-1066, ECC, CL9)
Disk Subsystem: 146 GB SAS, 10000RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	34.5	394	34.1	399	**34.5**	**394**	34.5	394	34.1	399	**34.3**	**396**
416.gamess	1295	15.1	**1301**	**15.0**	1302	15.0	**1078**	**18.2**	1128	17.4	1078	18.2
433.milc	**357**	**25.7**	360	25.5	357	25.7	356	25.8	372	24.7	**357**	**25.7**
434.zeusmp	445	20.5	**444**	**20.5**	442	20.6	445	20.5	**444**	**20.5**	442	20.6
435.gromacs	451	15.8	**451**	**15.8**	455	15.7	**444**	**16.1**	443	16.1	475	15.0
436.cactusADM	44.6	268	44.0	271	**44.0**	**271**	44.0	271	40.8	293	**43.8**	**273**
437.leslie3d	**419**	**22.4**	417	22.5	424	22.1	**419**	**22.4**	417	22.5	424	22.1
444.namd	**596**	**13.5**	594	13.5	638	12.6	**596**	**13.5**	594	13.5	638	12.6
447.dealII	**451**	**25.3**	455	25.1	451	25.4	**451**	**25.3**	455	25.1	451	25.4
450.soplex	427	19.5	**425**	**19.6**	424	19.7	419	19.9	420	19.9	**419**	**19.9**
453.povray	**268**	**19.9**	282	18.9	266	20.0	207	25.7	**207**	**25.6**	211	25.2
454.calculix	443	18.6	443	18.6	**443**	**18.6**	**408**	**20.2**	438	18.8	407	20.3
459.GemsFDTD	261	40.7	**244**	**43.4**	241	44.1	164	64.7	163	65.0	**164**	**64.8**
465.tonto	**580**	**17.0**	579	17.0	581	16.9	436	22.6	**436**	**22.6**	436	22.6
470.lbm	207	66.4	207	66.4	**207**	**66.4**	204	67.3	203	67.6	**204**	**67.3**
481.wrf	428	26.1	**418**	**26.7**	416	26.9	401	27.8	398	28.0	**399**	**28.0**
482.sphinx3	634	30.7	**634**	**30.7**	634	30.7	634	30.7	**634**	**30.7**	634	30.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M
```

Base Compiler Invocation

C benchmarks:

```
icc   -m64
```

C++ benchmarks:

```
icpc  -m64
```

Continued on next page

Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Peak Compiler Invocation

C benchmarks:
icc -m64

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Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -ansi-alias

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -parallel -ansi-alias -auto-ilp32

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: basepeak = yes

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-malloc-options=3 -auto-ilp32

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel

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Peak Optimization Flags (Continued)

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: Same as 454.calculix

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revG.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revG.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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