



# SPEC® CFP2006 Result

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Fujitsu

PRIMERGY TX150 S7, Intel Xeon L3406, 2.26 GHz

**SPECfp®2006 = 25.6**

**SPECfp\_base2006 = 24.1**

CPU2006 license: 19

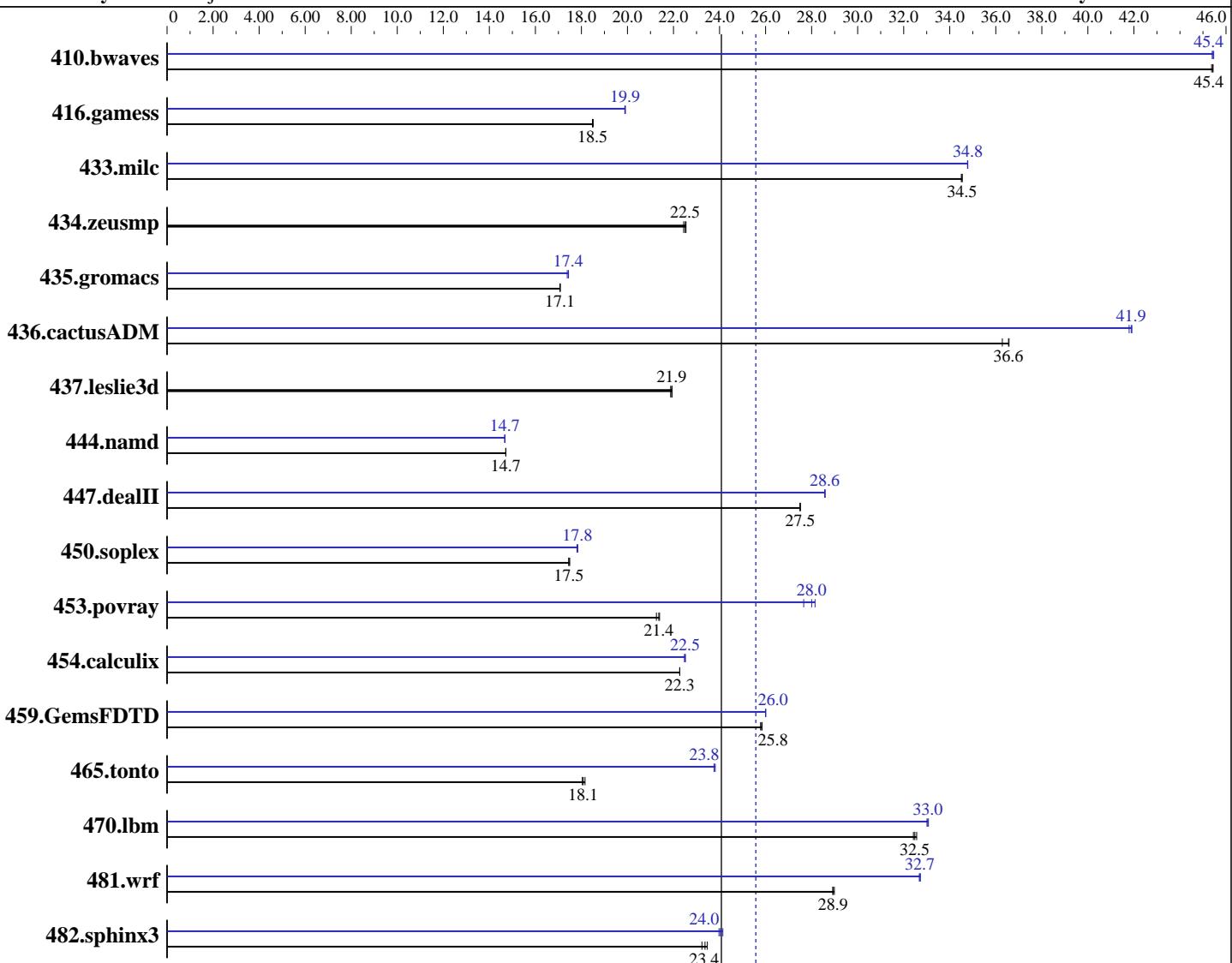
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jan-2010

Hardware Availability: Mar-2010

Software Availability: Nov-2009



## Hardware

CPU Name: Intel Xeon L3406  
CPU Characteristics: Intel Turbo Boost Technology up to 2.53 GHz  
CPU MHz: 2267  
FPU: Integrated  
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
CPU(s) orderable: 1 chip  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core

## Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-smp  
Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091012 Package ID: l\_cproc\_p\_11.1.059, l\_cprof\_p\_11.1.059  
Auto Parallel: Yes  
File System: ext3  
System State: Multi-User Run Level 3

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L3 Cache: 4 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 8 GB (2x4 GB PC3-10600E, 2 rank, CL9-9-9, ECC)  
 Disk Subsystem: 1 x SATA, 250 GB, 7200 RPM  
 Other Hardware: None

Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Binutils 2.18.50.0.7.20080502

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<b>299</b>	<b>45.4</b>	300	45.4	299	45.4	<b>299</b>	<b>45.5</b>	299	45.4	<b>299</b>	<b>45.4</b>
416.gamess	1058	18.5	<b>1060</b>	<b>18.5</b>	1060	18.5	<b>985</b>	<b>19.9</b>	983	19.9	<b>984</b>	<b>19.9</b>
433.milc	266	34.5	<b>266</b>	<b>34.5</b>	266	34.5	<b>264</b>	<b>34.8</b>	<b>264</b>	<b>34.8</b>	264	34.8
434.zeusmp	404	22.5	406	22.4	<b>404</b>	<b>22.5</b>	404	22.5	406	22.4	<b>404</b>	<b>22.5</b>
435.gromacs	418	17.1	418	17.1	<b>418</b>	<b>17.1</b>	409	17.4	411	17.4	<b>410</b>	<b>17.4</b>
436.cactusADM	<b>327</b>	<b>36.6</b>	327	36.6	329	36.3	<b>285</b>	<b>41.9</b>	<b>285</b>	<b>41.9</b>	286	41.8
437.leslie3d	429	21.9	430	21.9	<b>429</b>	<b>21.9</b>	429	21.9	430	21.9	<b>429</b>	<b>21.9</b>
444.namd	545	14.7	<b>545</b>	<b>14.7</b>	545	14.7	546	14.7	547	14.7	<b>547</b>	<b>14.7</b>
447.dealII	416	27.5	416	27.5	<b>416</b>	<b>27.5</b>	<b>400</b>	<b>28.6</b>	400	28.6	400	28.6
450.soplex	477	17.5	<b>478</b>	<b>17.5</b>	478	17.4	<b>468</b>	<b>17.8</b>	468	17.8	468	17.8
453.povray	<b>249</b>	<b>21.4</b>	249	21.4	250	21.3	189	28.1	192	27.7	<b>190</b>	<b>28.0</b>
454.calculix	370	22.3	370	22.3	<b>370</b>	<b>22.3</b>	367	22.5	366	22.5	<b>367</b>	<b>22.5</b>
459.GemsFDTD	<b>411</b>	<b>25.8</b>	412	25.8	410	25.8	408	26.0	<b>408</b>	<b>26.0</b>	408	26.0
465.tonto	<b>544</b>	<b>18.1</b>	542	18.2	546	18.0	<b>413</b>	<b>23.8</b>	414	23.8	<b>414</b>	<b>23.8</b>
470.lbm	422	32.6	<b>423</b>	<b>32.5</b>	424	32.4	<b>416</b>	<b>33.0</b>	415	33.1	<b>416</b>	<b>33.0</b>
481.wrf	385	29.0	<b>386</b>	<b>28.9</b>	386	28.9	<b>342</b>	<b>32.7</b>	342	32.7	341	32.7
482.sphinx3	<b>834</b>	<b>23.4</b>	839	23.2	830	23.5	<b>807</b>	<b>24.1</b>	<b>811</b>	<b>24.0</b>	813	24.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## General Notes

OMP\_NUM\_THREADS set to number of cores  
 KMP\_AFFINITY set to granularity=fine,scatter  
 KMP\_STACKSIZE set to 200M  
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## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch



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## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4\_2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-ansi-alias

470.lbm: -xSSE4\_2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-parallel -ansi-alias -auto-ilp32

Continued on next page



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## Peak Optimization Flags (Continued)

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll12

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
 -fno-alias -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
 -unroll12 -ansi-alias -scalar-rep -auto-ilp32

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
 -opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
 -unroll14 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch  
 -parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
 -unroll12 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
 -unroll12 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
 -inline-calloc -opt-malloc-options=3 -auto -unroll14

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
 -opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
 -unroll12 -opt-prefetch -parallel -auto-ilp32

Continued on next page



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## Peak Optimization Flags (Continued)

454.calculix: -xsse4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: Same as 454.calculix

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-fp-linux64-revF.20100202.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-fp-linux64-revF.20100202.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 09:58:39 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 14 April 2010.