



# SPEC® CINT2006 Result

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## Supermicro

SPECint®\_rate2006 = 95.8

Motherboard X8SIE-F (Intel Xeon L3426, 1.86 GHz)

SPECint\_rate\_base2006 = 90.0

CPU2006 license: 001176

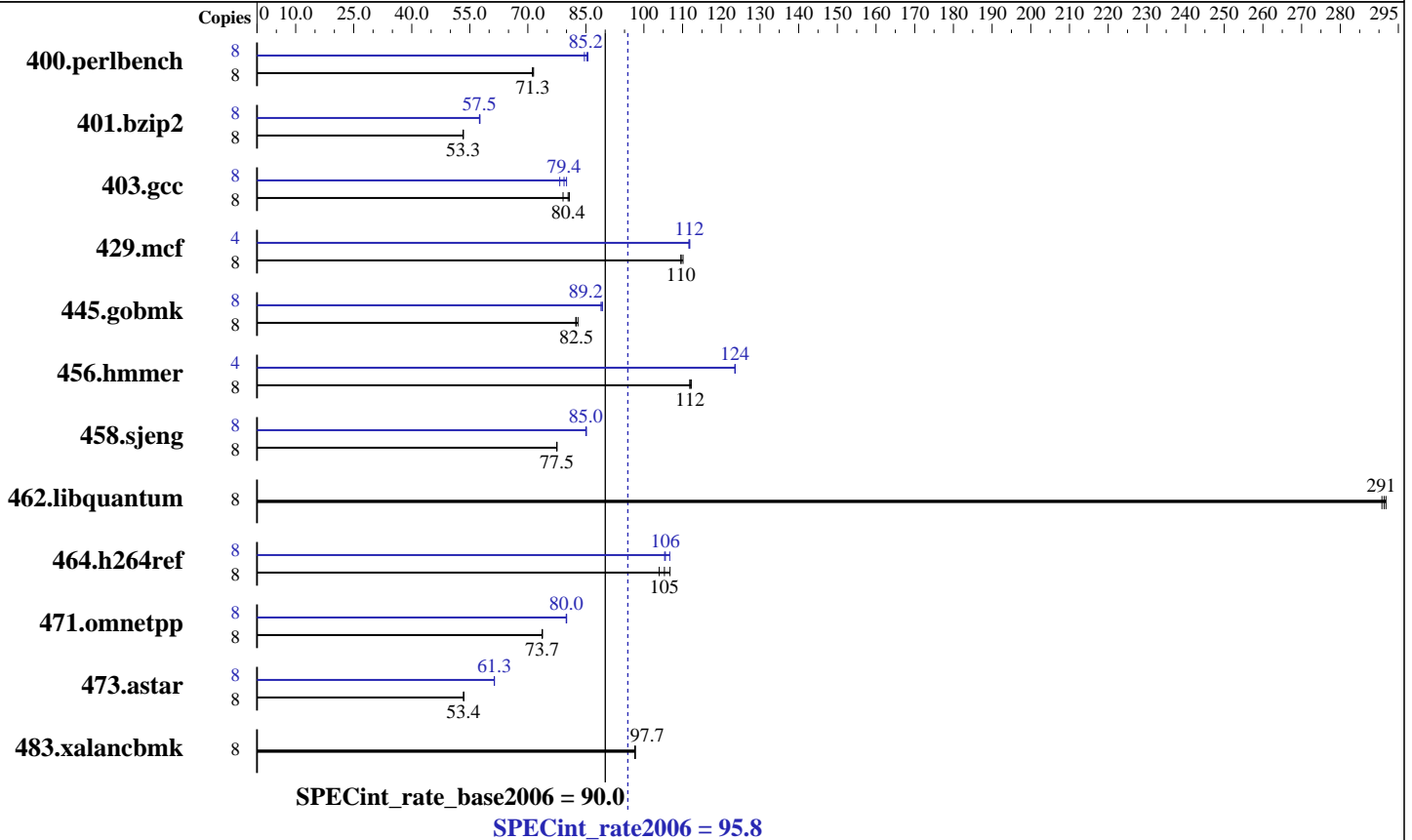
Test date: Feb-2010

Test sponsor: Supermicro

Hardware Availability: Sep-2009

Tested by: Supermicro

Software Availability: Oct-2009



### Hardware

CPU Name: Intel Xeon L3426  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz  
 CPU MHz: 1866  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 16 GB (4 x 4 GB DDR3-1333 RDIMM, ECC, CL9)  
 Disk Subsystem: 1 x 160 GB SATA II, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64)  
 Kernel 2.6.27.19-5-default  
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1  
 Build 20091012 Package ID: l\_cproc\_p\_11.1.059  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1  
 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	<b>1096</b>	<b>71.3</b>	1093	71.5	1098	71.2	8	924	84.6	914	85.5	<b>917</b>	<b>85.2</b>
401.bzip2	8	<b>1448</b>	<b>53.3</b>	1448	53.3	1450	53.2	8	1339	57.6	<b>1342</b>	<b>57.5</b>	1342	57.5
403.gcc	8	814	79.1	<b>801</b>	<b>80.4</b>	798	80.7	8	823	78.3	<b>812</b>	<b>79.4</b>	805	80.0
429.mcf	8	663	110	666	110	<b>666</b>	<b>110</b>	4	<b>326</b>	<b>112</b>	326	112	327	112
445.gobmk	8	1019	82.4	1012	83.0	<b>1017</b>	<b>82.5</b>	8	944	88.9	940	89.3	<b>941</b>	<b>89.2</b>
456.hammer	8	<b>667</b>	<b>112</b>	667	112	665	112	4	302	124	302	124	<b>302</b>	<b>124</b>
458.sjeng	8	<b>1249</b>	<b>77.5</b>	1248	77.5	1250	77.5	8	<b>1138</b>	<b>85.0</b>	1137	85.1	1139	85.0
462.libquantum	8	568	292	<b>569</b>	<b>291</b>	570	291	8	568	292	<b>569</b>	<b>291</b>	570	291
464.h264ref	8	1703	104	<b>1681</b>	<b>105</b>	1659	107	8	<b>1677</b>	<b>106</b>	1660	107	1680	105
471.omnetpp	8	678	73.8	<b>678</b>	<b>73.7</b>	679	73.7	8	<b>625</b>	<b>80.0</b>	625	80.0	626	79.9
473.astar	8	<b>1051</b>	<b>53.4</b>	1052	53.4	1050	53.5	8	915	61.4	916	61.3	<b>915</b>	<b>61.3</b>
483.xalancbmk	8	564	97.8	<b>565</b>	<b>97.7</b>	565	97.6	8	564	97.8	<b>565</b>	<b>97.7</b>	565	97.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Platform Notes

Fan speed set to Full Speed in BIOS Setup.  
As tested, the system used a Supermicro  
PWS-665-PQ power supply, SNK-P0046P heatsink,  
and FAN-0077L cooling fan.

## Base Compiler Invocation

C benchmarks:  
icc -m32  
  
C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX

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## Base Portability Flags (Continued)

483.xalanbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.icl1.1/libic11.1-32bit -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

473.astar: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias

401.bzp2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalanbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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