



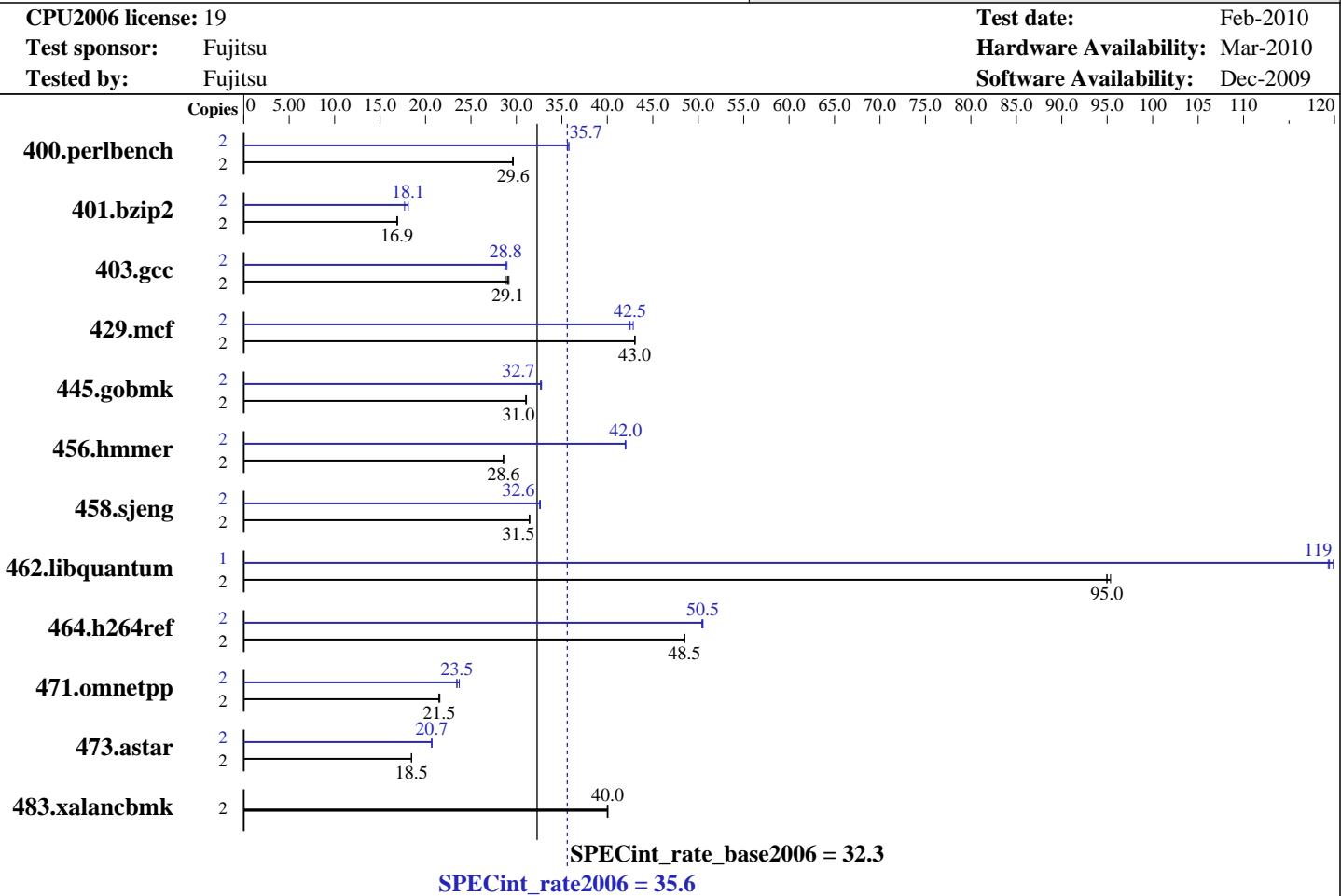
SPEC® CINT2006 Result

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Fujitsu

CELSIUS W380, Intel Celeron G1101

SPECint_rate2006 = 35.6



Hardware

CPU Name:	Intel Celeron G1101
CPU Characteristics:	
CPU MHz:	2267
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	2 MB I+D on chip per chip
Other Cache:	None
Memory:	4 GB (2x 2 GB PC3 10600E, 2 rank, CL9, with ECC, running at 1066 MHz)
Disk Subsystem:	1 x SATA II, 400 GB, 7200 rpm
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), kernel 2.6.27.19-5-default
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Software Availability: Dec-2009

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	659	29.6	659	29.6	661	29.6	2	548	35.7	546	35.8	548	35.6
401.bzip2	2	1144	16.9	1141	16.9	1145	16.9	2	1091	17.7	1066	18.1	1068	18.1
403.gcc	2	557	28.9	554	29.1	552	29.2	2	559	28.8	556	29.0	560	28.7
429.mcf	2	424	43.0	424	43.0	424	43.0	2	429	42.5	430	42.4	426	42.8
445.gobmk	2	674	31.1	676	31.0	676	31.0	2	642	32.7	642	32.7	640	32.8
456.hammer	2	653	28.6	653	28.6	653	28.6	2	444	42.0	445	42.0	444	42.0
458.sjeng	2	769	31.5	771	31.4	769	31.5	2	742	32.6	743	32.6	742	32.6
462.libquantum	2	436	95.0	436	95.0	435	95.4	1	173	120	174	119	173	119
464.h264ref	2	913	48.5	913	48.5	912	48.5	2	878	50.4	876	50.5	876	50.5
471.omnetpp	2	579	21.6	582	21.5	582	21.5	2	527	23.7	533	23.5	533	23.5
473.astar	2	760	18.5	762	18.4	760	18.5	2	678	20.7	679	20.7	679	20.7
483.xalancbmk	2	345	40.1	345	40.0	345	40.0	2	345	40.1	345	40.0	345	40.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter

Base Compiler Invocation

C benchmarks:

icc -m32

C++ benchmarks:

icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

```
-xSSSE3 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:

```
-xSSSE3 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/cmpllr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m32
```

```
401.bzip2: icc -m64
```

```
456.hmmmer: icc -m64
```

```
462.libquantum: icc -m64
```

C++ benchmarks (except as noted below):

```
icpc -m32
```

```
473.astar: icpc -m64
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
```

```
401.bzip2: -DSPEC_CPU_LP64
```

```
456.hmmmer: -DSPEC_CPU_LP64
```

```
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

```
473.astar: -DSPEC_CPU_LP64
```

```
483.xalancbmk: -DSPEC_CPU_LINUX
```



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Peak Optimization Flags

C benchmarks:

```
400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3  
               -no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3  
               -no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: -xSSSE3 -ipo -O3 -no-prec-div -static -inline-calloc  
          -opt-malloc-options=3

429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3  
          -no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -O2 -ipo  
            -no-prec-div -ansi-alias

456.hmmr: -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2  
            -ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3  
            -no-prec-div -static -unroll4

462.libquantum: -xSSSE3 -ipo -O3 -no-prec-div -static  
               -opt-malloc-options=3 -parallel -auto-ilp32 -opt-prefetch

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3  
               -no-prec-div -static -unroll2 -ansi-alias
```

C++ benchmarks:

```
471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3  
              -no-prec-div -ansi-alias -opt-ra-region-strategy=block  
              -Wl,-z,muldefs  
              -L/home/cmpllr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias  
            -opt-ra-region-strategy=routine -Wl,-z,muldefs  
            -L/home/cmpllr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml>

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