



SPEC® CINT2006 Result

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Intel Corporation

SPECint®_rate2006 = 49.1

Asus P5E3 Pro motherboard (Intel Core 2 Duo E8600)

SPECint_rate_base2006 = 47.1

CPU2006 license: 13

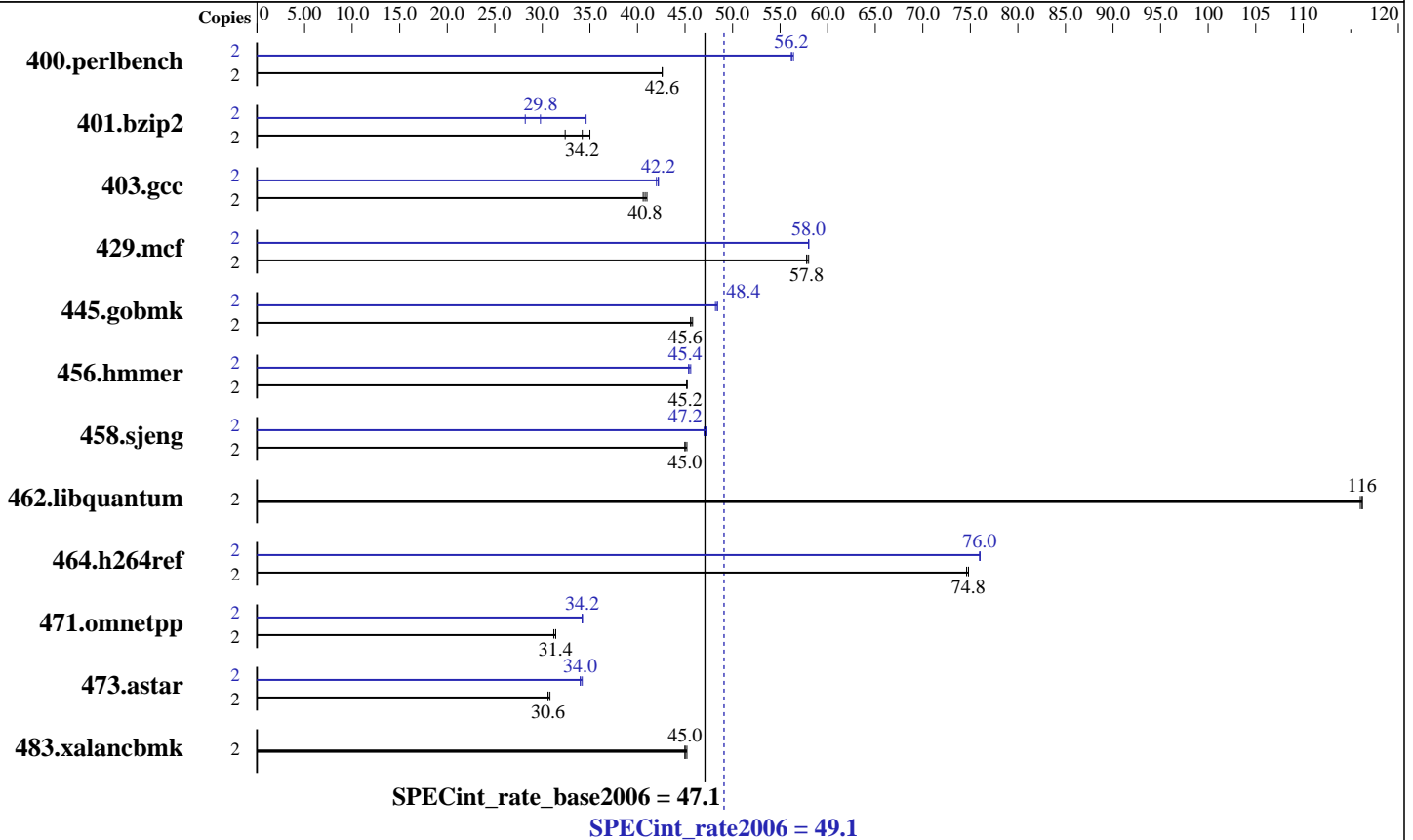
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Feb-2010

Hardware Availability: Jun-2009

Software Availability: Oct-2009



Hardware

CPU Name: Intel Core 2 Duo E8600
 CPU Characteristics: 3333
 CPU MHz: 3333
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 4 GB (2x2GB Micron MT16JTF25664AZ-1G4 DDR3-1333 CL9)
 Disk Subsystem: Intel X25-M 80GB SSD
 Other Hardware: None

Software

Operating System: Windows Vista Ultimate w/ SP1 (64-bit)
 Compiler: Intel C++ Compiler Professional 11.1 for IA32 Build 20090903 Package ID: w_cproc_p_11.1.045 Microsoft Visual Studio 2008 Professional SP1 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: None
 SmartHeap Library Version 8.1 from <http://www.microquill.com/>



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	460	42.6	459	42.6	459	42.6	2	347	56.4	347	56.2	348	56.2
401.bzip2	2	595	32.4	563	34.2	551	35.0	2	559	34.6	650	29.8	685	28.2
403.gcc	2	393	41.0	396	40.6	394	40.8	2	381	42.2	381	42.2	383	42.0
429.mcf	2	314	58.0	315	57.8	316	57.8	2	315	58.0	314	58.0	315	58.0
445.gobmk	2	460	45.6	459	45.8	460	45.6	2	433	48.4	435	48.2	433	48.4
456.hammer	2	413	45.2	414	45.2	413	45.2	2	410	45.6	411	45.4	411	45.4
458.sjeng	2	537	45.0	536	45.2	537	45.0	2	513	47.2	515	47.0	514	47.2
462.libquantum	2	357	116	357	116	357	116	2	357	116	357	116	357	116
464.h264ref	2	592	74.8	593	74.6	592	74.8	2	583	76.0	583	76.0	583	76.0
471.omnetpp	2	400	31.2	399	31.4	399	31.4	2	366	34.2	365	34.2	366	34.2
473.astar	2	458	30.6	457	30.8	458	30.6	2	413	34.0	412	34.2	412	34.0
483.xalanbmk	2	306	45.2	307	45.0	306	45.0	2	306	45.2	307	45.0	306	45.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
Windows start command was used to bind copies to processors

General Notes

Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply
System was configured with an ATI 5970 discrete graphics card

Base Compiler Invocation

C benchmarks:
icl -Qvc9 -Qstd=c99

C++ benchmarks:
icl -Qvc9

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DWIN32 -DSPEC_CPU_NO_INTTYPES
483.xalanbmk: -Qoption, cpp, --no_wchar_t_keyword



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Base Optimization Flags

C benchmarks:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

C++ benchmarks:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F512000000 shlw32M.lib -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:

icl -Qvc9 -Qstd=c99

C++ benchmarks:

icl -Qvc9

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DWIN32 -DSPEC_CPU_NO_INTTYPES
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
/F512000000 shlw32M.lib -link /FORCE:MULTIPLE

401.bzip2: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias
/F512000000

403.gcc: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

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Peak Optimization Flags (Continued)

429.mcf: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch
/F512000000

445.gobmk: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O2 -Qprec-div- -Qansi-alias /F512000000

456.hmmcr: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
/F512000000

458.sjeng: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll4 /F512000000

462.libquantum: basepeak = yes

464.h264ref: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=block /F512000000 shlW32M.lib
-link /FORCE:MULTIPLE

473.astar: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=routine /F512000000 shlW32M.lib
-link /FORCE:MULTIPLE

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20100302.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20100302.02.xml>



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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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