



SPEC® CFP2006 Result

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Intel Corporation

SPECfp®2006 = 25.4

Asus P5E3 Pro motherboard (Intel Core 2 Duo E8600)

SPECfp_base2006 = 24.5

CPU2006 license: 13

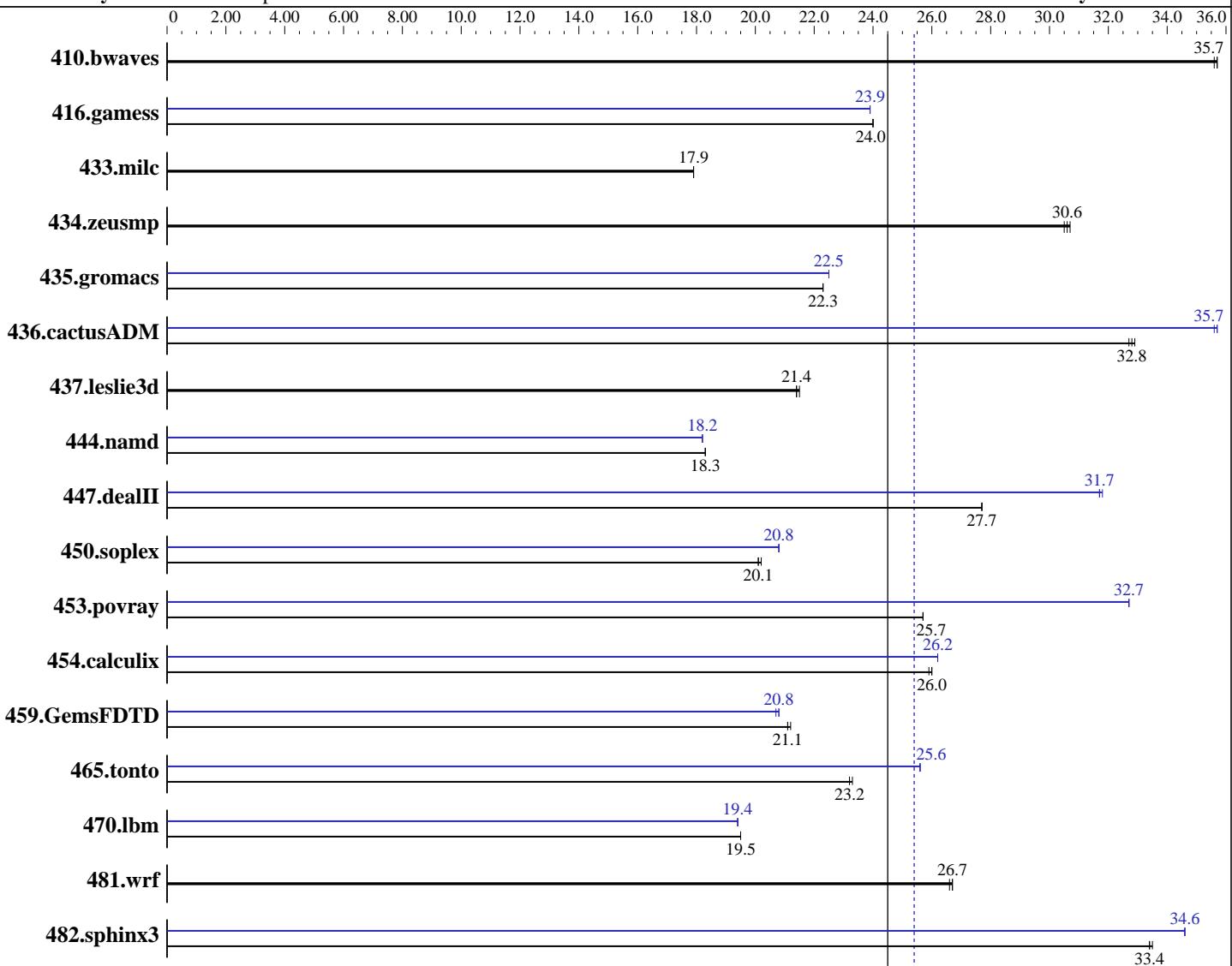
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Feb-2010

Hardware Availability: Jun-2009

Software Availability: Oct-2009



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Hardware

CPU Name: Intel Core 2 Duo E8600
 CPU Characteristics:
 CPU MHz: 3333
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip

Software

Operating System: Windows Vista Ultimate w/ SP1 (64-bit)
 Compiler: Intel C++ Compiler Professional 11.1 for Intel 64 Build 20090903 Package ID: w_cproc_p_11.1.045
 Intel Visual Fortran Compiler Professional 11.1 for Intel 64 Build 20090903 Package ID: w_cproc_p_11.1.045, w_cprof_p_11.1.045 Microsoft Visual Studio 2008 Professional SP1 (for libraries)
 Auto Parallel: Yes

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L3 Cache:	None	File System:	NTFS
Other Cache:	None	System State:	Default
Memory:	4 GB (2x2GB Micron MT16JTF25664AZ-1G4 DDR3-1333 CL9)	Base Pointers:	64-bit
Disk Subsystem:	Intel X25-M 80GB SSD	Peak Pointers:	64-bit
Other Hardware:	None	Other Software:	None SmartHeap Library Version 8.1 from http://www.microquill.com/

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	381	35.7	381	35.7	381	35.6	381	35.7	381	35.7	381	35.6
416.gamess	816	24.0	816	24.0	817	24.0	820	23.9	820	23.9	820	23.9
433.milc	512	17.9	512	17.9	512	17.9	512	17.9	512	17.9	512	17.9
434.zeusmp	298	30.5	297	30.7	298	30.6	298	30.5	297	30.7	298	30.6
435.gromacs	320	22.3	320	22.3	320	22.3	318	22.5	318	22.5	318	22.5
436.cactusADM	365	32.7	364	32.9	364	32.8	335	35.7	336	35.6	335	35.7
437.leslie3d	438	21.5	439	21.4	439	21.4	438	21.5	439	21.4	439	21.4
444.namd	439	18.3	439	18.3	439	18.3	440	18.2	440	18.2	440	18.2
447.dealII	413	27.7	413	27.7	413	27.7	361	31.7	360	31.8	360	31.7
450.soplex	414	20.1	414	20.2	414	20.1	402	20.8	401	20.8	401	20.8
453.povray	207	25.7	207	25.7	207	25.7	163	32.7	163	32.7	163	32.7
454.calculix	318	26.0	318	26.0	318	25.9	315	26.2	315	26.2	314	26.2
459.GemsFDTD	501	21.2	503	21.1	503	21.1	511	20.7	509	20.8	510	20.8
465.tonto	424	23.2	424	23.2	423	23.3	385	25.6	385	25.6	385	25.6
470.lbm	704	19.5	703	19.5	704	19.5	708	19.4	708	19.4	709	19.4
481.wrf	419	26.7	419	26.6	418	26.7	419	26.7	419	26.6	418	26.7
482.sphinx3	583	33.5	583	33.4	584	33.4	564	34.6	563	34.6	563	34.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with Shin-G ATX case,
 PC Power and Cooling 1200W power supply
 OMP_NUM_THREADS set to number of processors cores
 KMP_AFFINITY set to granularity=fine,scatter
 System was configured with an ATI 5970 discrete graphics card

Base Compiler Invocation

C benchmarks:

icl -Qvc9 -Qstd=c99

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Base Compiler Invocation (Continued)

C++ benchmarks:

 icl -Qvc9

Fortran benchmarks:

 ifort

Benchmarks using both Fortran and C:

 icl -Qvc9 -Qstd=c99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64 /Qlowercase
416.games: -DSPEC_CPU_P64
 433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 -Qlowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
 444.namd: -DSPEC_CPU_P64 /TP
 447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
 453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
 454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -Qlowercase
459.GemsFDTD: -DSPEC_CPU_P64
 465.tonto: -DSPEC_CPU_P64
 470.lbm: -DSPEC_CPU_P64
 481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:

 -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qparallel -Qopt-prefetch
 -Qauto-ilp32 /F1000000000

C++ benchmarks:

 -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qparallel -Qopt-prefetch
 -Qcxx-features -Qauto-ilp32 /F1000000000 shlw64M.lib
 -link /FORCE:MULTIPLE

Fortran benchmarks:

 -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qparallel -Qopt-prefetch
 /F1000000000

Benchmarks using both Fortran and C:

 -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qparallel -Qopt-prefetch
 -Qauto-ilp32 /F1000000000



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Peak Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

Fortran benchmarks:

```
ifort
```

Benchmarks using both Fortran and C:

```
icl -Qvc9 -Qstd=c99 ifort
```

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
433.milc: basepeak = yes
```

```
470.lbm: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
         -Qipo -O3 -Qprec-div- -Qansi-alias -Qparallel
         -Qauto-ilp32 /F1000000000
```

```
482.sphinx3: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
             -Qipo -O3 -Qprec-div- -Qunroll12 -Qauto-ilp32 /F1000000000
```

C++ benchmarks:

```
444.namd: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
           -Qipo -O3 -Qprec-div- -Oa -Qauto-ilp32 /F1000000000
           shlw64M.lib -link /FORCE:MULTIPLE
```

```
447.dealII: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
            -Qipo -O3 -Qprec-div- -Qunroll12 -Qopt-prefetch
            -Qansi-alias -Qscalar-rep- -Qauto-ilp32 /F1000000000
            shlw64M.lib -link /FORCE:MULTIPLE
```

```
450.soplex: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
            -Qipo -O3 -Qauto-ilp32 /F1000000000 shlw64M.lib
            -link /FORCE:MULTIPLE
```

```
453.povray: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
            -Qipo -O3 -Qprec-div- -Qunroll14 -Qansi-alias -Qauto-ilp32
            /F1000000000 shlw64M.lib -link /FORCE:MULTIPLE
```

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Peak Optimization Flags (Continued)

Fortran benchmarks:

```
410.bwaves: basepeak = yes  
  
416.gamess: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)  
             -Qipo -O3 -Qprec-div- -Qunroll12 -Ob0 -Qansi-alias  
             -Qscalar-rep- /F1000000000  
  
434.zeusmp: basepeak = yes  
  
437.leslie3d: basepeak = yes  
  
459.GemsFDTD: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)  
               -Qipo -O3 -Qprec-div- -Qunroll12 -Qopt-prefetch -Qparallel  
               /F1000000000  
  
465.tonto: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)  
            -Qipo -O3 -Qprec-div- -Qunroll14 -Qauto -Qinline-calloc  
            /F1000000000
```

Benchmarks using both Fortran and C:

```
435.gromacs: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)  
              -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32  
              /F1000000000  
  
436.cactusADM: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)  
                -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel -Qunroll12  
                -Qauto-ilp32 /F1000000000  
  
454.calculix: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qauto-ilp32 /F1000000000  
  
481.wrf: basepeak = yes
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20100302.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20100302.01.xml>



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