



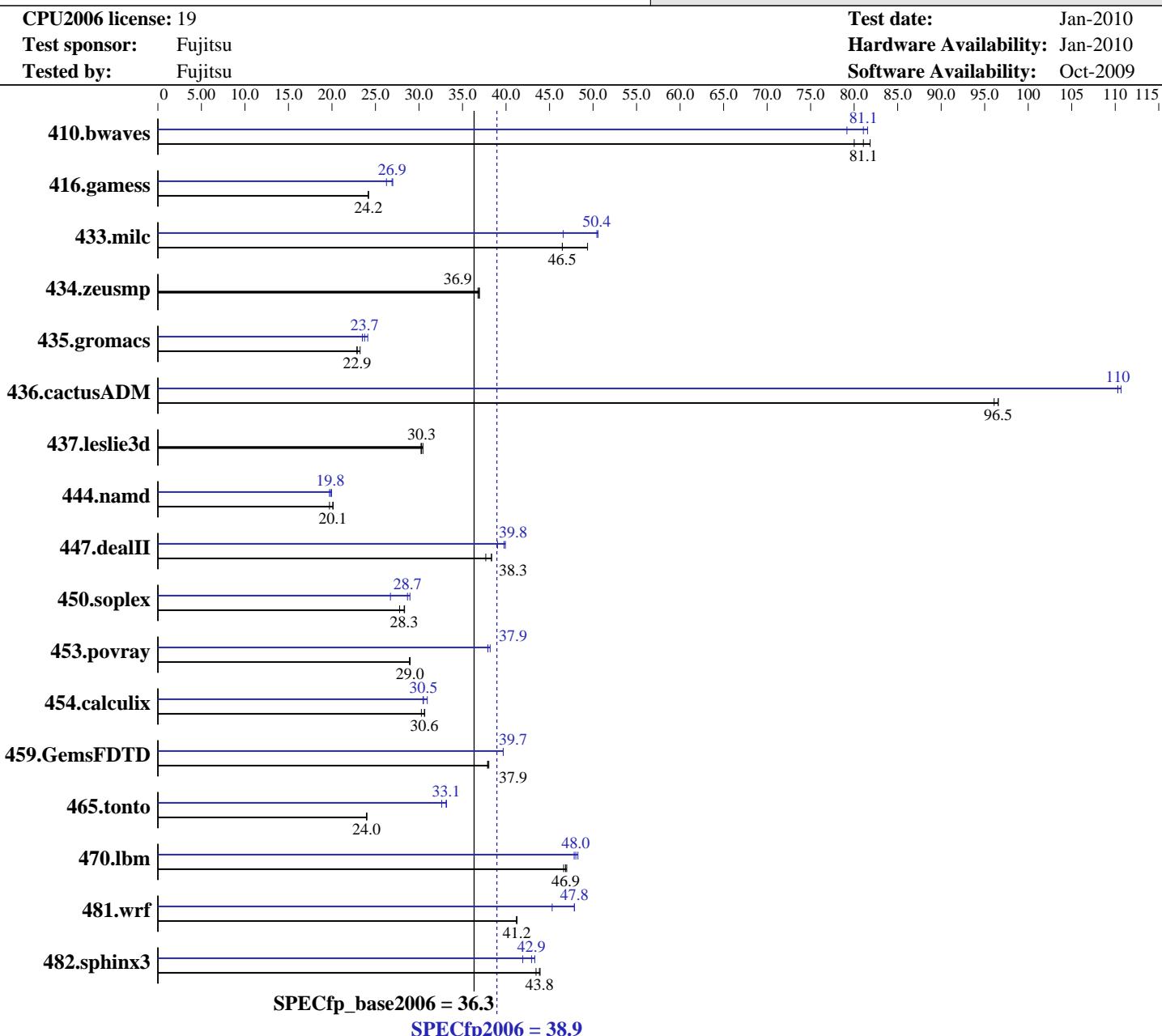
SPEC® CFP2006 Result

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Fujitsu

CELSIUS W280, Intel Core i7-860

SPECfp®2006 = 38.9



Hardware

CPU Name: Intel Core i7-860
CPU Characteristics: Intel Turbo Boost Technology up to 3.46 GHz
CPU MHz: 2800
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), kernel 2.6.27.19-5-default
Compiler: Intel C++ and Fortran Compiler for IA32 and Intel 64, Version 11.1 Build 20091012 Package ID: l_cproc_p_11.1.059, l_cprof_p_11.1.059
Auto Parallel: Yes
File System: ext3
System State: Multi-User Run Level 3

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Software Availability: Oct-2009

L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 8 GB (2x4 GB PC3-10600U, 2 rank, CL9)
 Disk Subsystem: 1 x SATA II, 400 GB, 7200 rpm
 Other Hardware: None

Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	170	80.0	168	81.1	166	81.8	168	81.1	172	79.2	167	81.5
416.gamess	809	24.2	810	24.2	810	24.2	746	26.3	726	27.0	728	26.9
433.milc	198	46.5	186	49.4	198	46.5	182	50.6	197	46.6	182	50.4
434.zeusmp	247	36.8	247	36.9	246	36.9	247	36.8	247	36.9	246	36.9
435.gromacs	307	23.2	312	22.9	312	22.9	296	24.1	301	23.7	304	23.5
436.cactusADM	124	96.6	124	96.1	124	96.5	108	110	108	110	108	111
437.leslie3d	309	30.5	311	30.2	310	30.3	309	30.5	311	30.2	310	30.3
444.namd	398	20.1	399	20.1	407	19.7	404	19.8	402	20.0	407	19.7
447.dealII	304	37.7	299	38.3	298	38.3	293	39.0	287	39.9	288	39.8
450.soplex	300	27.8	295	28.3	294	28.3	288	29.0	291	28.7	312	26.7
453.povray	184	29.0	184	29.0	184	28.9	140	37.9	139	38.2	140	37.9
454.calculix	269	30.7	269	30.6	273	30.3	271	30.5	267	31.0	270	30.5
459.GemsFDTD	280	37.9	280	37.9	279	38.0	267	39.7	267	39.7	267	39.7
465.tonto	410	24.0	411	24.0	410	24.0	297	33.1	302	32.6	297	33.2
470.lbm	293	46.9	292	47.0	295	46.6	285	48.3	286	48.0	287	47.8
481.wrf	271	41.3	271	41.2	271	41.2	233	47.8	233	47.9	247	45.3
482.sphinx3	449	43.4	444	43.9	445	43.8	454	42.9	450	43.3	465	41.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

OMP_NUM_THREADS set to number of cores
 KMP_AFFINITY set to granularity=fine,scatter
 KMP_STACKSIZE set to 200M

Base Compiler Invocation

C benchmarks:
 icc -m64

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Base Compiler Invocation (Continued)

C++ benchmarks:

icpc -m64

Fortran benchmarks:

fort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.games: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4_2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-ansi-alias

470.lbm: -xSSE4_2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-parallel -ansi-alias -auto-ilp32

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Peak Optimization Flags (Continued)

482.sphinx3: -xsse4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -fno-alias -auto-ilp32

447.dealII: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -unroll2 -ansi-alias -scalar-rep -auto-ilp32

450.soplex: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -opt-malloc-options=3

453.povray: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xsse4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
 -parallel

416.gamess: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -opt-prefetch -auto-ilp32

436.cactusADM: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -unroll2 -opt-prefetch -parallel -auto-ilp32

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Peak Optimization Flags (Continued)

454.calculix: -xsse4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: Same as 454.calculix

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revD.20100119.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revD.20100119.xml>

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