



SPEC[®] CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Cisco Systems

SPECfp[®]_rate2006 = 192

UCS C210 M1 (Intel Xeon X5550)

SPECfp_rate_base2006 = 185

CPU2006 license: 9019

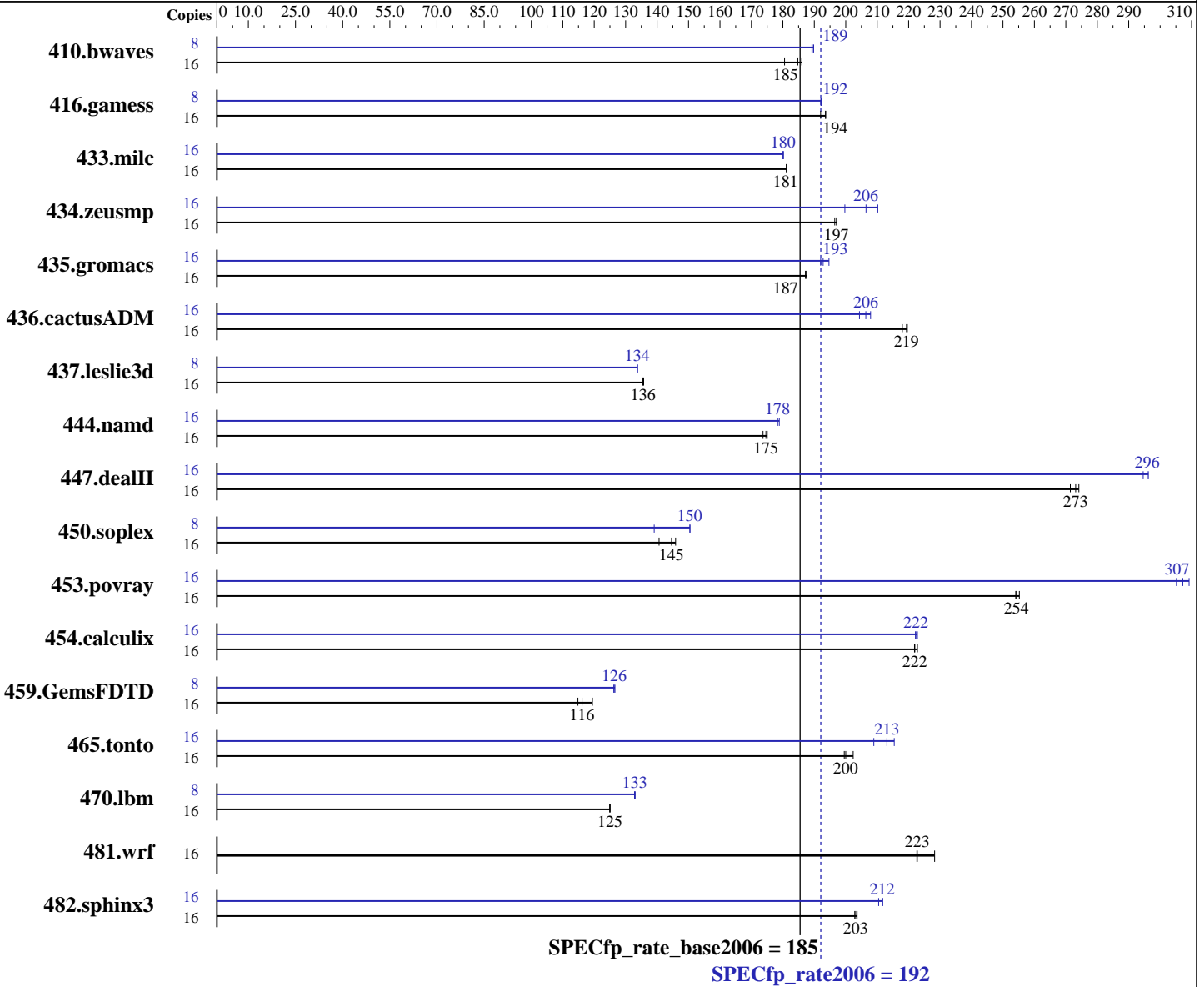
Test sponsor: Cisco Systems

Tested by: Cisco Systems

Test date: Dec-2009

Hardware Availability: Oct-2009

Software Availability: Mar-2009



Hardware

CPU Name: Intel Xeon X5550
 CPU Characteristics: Intel Turbo Boost Technology up to 3.06 GHz
 CPU MHz: 2667
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 Chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SuSe Linux Enterprise Server 11 (x86_64), Kernel 2.6.27-15-2-default, RC4
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080, l_cprof_p_11.0.080
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Cisco Systems

SPECfp_rate2006 = 192

UCS C210 M1 (Intel Xeon X5550)

SPECfp_rate_base2006 = 185

CPU2006 license: 9019

Test date: Dec-2009

Test sponsor: Cisco Systems

Hardware Availability: Oct-2009

Tested by: Cisco Systems

Software Availability: Mar-2009

L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 24 GB (6 * 4GB DDR3-1333 MHz)
Disk Subsystem: 73 GB SATA, 15kRPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	1204	181	1169	186	<u>1177</u>	<u>185</u>	8	574	189	573	190	<u>574</u>	<u>189</u>
416.gamess	16	1618	194	<u>1618</u>	<u>194</u>	1632	192	8	815	192	<u>815</u>	<u>192</u>	815	192
433.milc	16	810	181	811	181	<u>811</u>	<u>181</u>	16	<u>816</u>	<u>180</u>	816	180	815	180
434.zeusmp	16	741	197	738	197	<u>739</u>	<u>197</u>	16	729	200	693	210	<u>705</u>	<u>206</u>
435.gromacs	16	610	187	609	188	<u>610</u>	<u>187</u>	16	587	195	595	192	<u>593</u>	<u>193</u>
436.cactusADM	16	<u>872</u>	<u>219</u>	877	218	871	220	16	936	204	920	208	<u>926</u>	<u>206</u>
437.leslie3d	16	1110	135	<u>1109</u>	<u>136</u>	1108	136	8	<u>562</u>	<u>134</u>	563	134	562	134
444.namd	16	<u>735</u>	<u>175</u>	739	174	733	175	16	<u>720</u>	<u>178</u>	717	179	720	178
447.dealII	16	674	271	668	274	<u>670</u>	<u>273</u>	16	<u>619</u>	<u>296</u>	618	296	621	295
450.soplex	16	949	141	<u>923</u>	<u>145</u>	915	146	8	480	139	<u>444</u>	<u>150</u>	443	150
453.povray	16	335	254	<u>335</u>	<u>254</u>	334	255	16	<u>277</u>	<u>307</u>	275	309	279	305
454.calculix	16	<u>595</u>	<u>222</u>	595	222	592	223	16	592	223	594	222	<u>594</u>	<u>222</u>
459.GemsFDTD	16	1479	115	<u>1462</u>	<u>116</u>	1421	119	8	672	126	670	127	<u>672</u>	<u>126</u>
465.tonto	16	789	200	<u>787</u>	<u>200</u>	778	202	16	731	215	754	209	<u>739</u>	<u>213</u>
470.lbm	16	<u>1759</u>	<u>125</u>	1760	125	1758	125	8	828	133	826	133	<u>827</u>	<u>133</u>
481.wrf	16	783	228	<u>802</u>	<u>223</u>	803	223	16	783	228	<u>802</u>	<u>223</u>	803	223
482.sphinx3	16	1537	203	<u>1535</u>	<u>203</u>	1532	204	16	1482	210	<u>1473</u>	<u>212</u>	1473	212

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

ulimit -s unlimited was used to set the stacksize to unlimited prior to run

Base Compiler Invocation

C benchmarks:
icc

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Cisco Systems

SPECfp_rate2006 = 192

UCS C210 M1 (Intel Xeon X5550)

SPECfp_rate_base2006 = 185

CPU2006 license: 9019

Test date: Dec-2009

Test sponsor: Cisco Systems

Hardware Availability: Oct-2009

Tested by: Cisco Systems

Software Availability: Mar-2009

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Cisco Systems

SPECfp_rate2006 = 192

UCS C210 M1 (Intel Xeon X5550)

SPECfp_rate_base2006 = 185

CPU2006 license: 9019

Test sponsor: Cisco Systems

Tested by: Cisco Systems

Test date: Dec-2009

Hardware Availability: Oct-2009

Software Availability: Mar-2009

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc

450.soplex: icpc -m32

Fortran benchmarks (except as noted below):

ifort

437.leslie3d: ifort -m32

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 444.namd: -DSPEC_CPU_LP64
 447.deallI: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -fno-alias

470.lbm: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
 -auto-ilp32

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Cisco Systems

SPECfp_rate2006 = 192

UCS C210 M1 (Intel Xeon X5550)

SPECfp_rate_base2006 = 185

CPU2006 license: 9019

Test date: Dec-2009

Test sponsor: Cisco Systems

Hardware Availability: Oct-2009

Tested by: Cisco Systems

Software Availability: Mar-2009

Peak Optimization Flags (Continued)

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep-

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)

437.leslie3d: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -opt-prefetch

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -opt-prefetch

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Cisco Systems

SPECfp_rate2006 = 192

UCS C210 M1 (Intel Xeon X5550)

SPECfp_rate_base2006 = 185

CPU2006 license: 9019

Test date: Dec-2009

Test sponsor: Cisco Systems

Hardware Availability: Oct-2009

Tested by: Cisco Systems

Software Availability: Mar-2009

Peak Optimization Flags (Continued)

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -opt-prefetch -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revH.20100317.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revH.20100317.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 06:19:34 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 17 March 2010.