



SPEC® CINT2006 Result

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Supermicro Motherboard X8DT6-F

SPECint®2006 = 19.4
SPECint_base2006 = 17.4

CPU2006 license: 001176

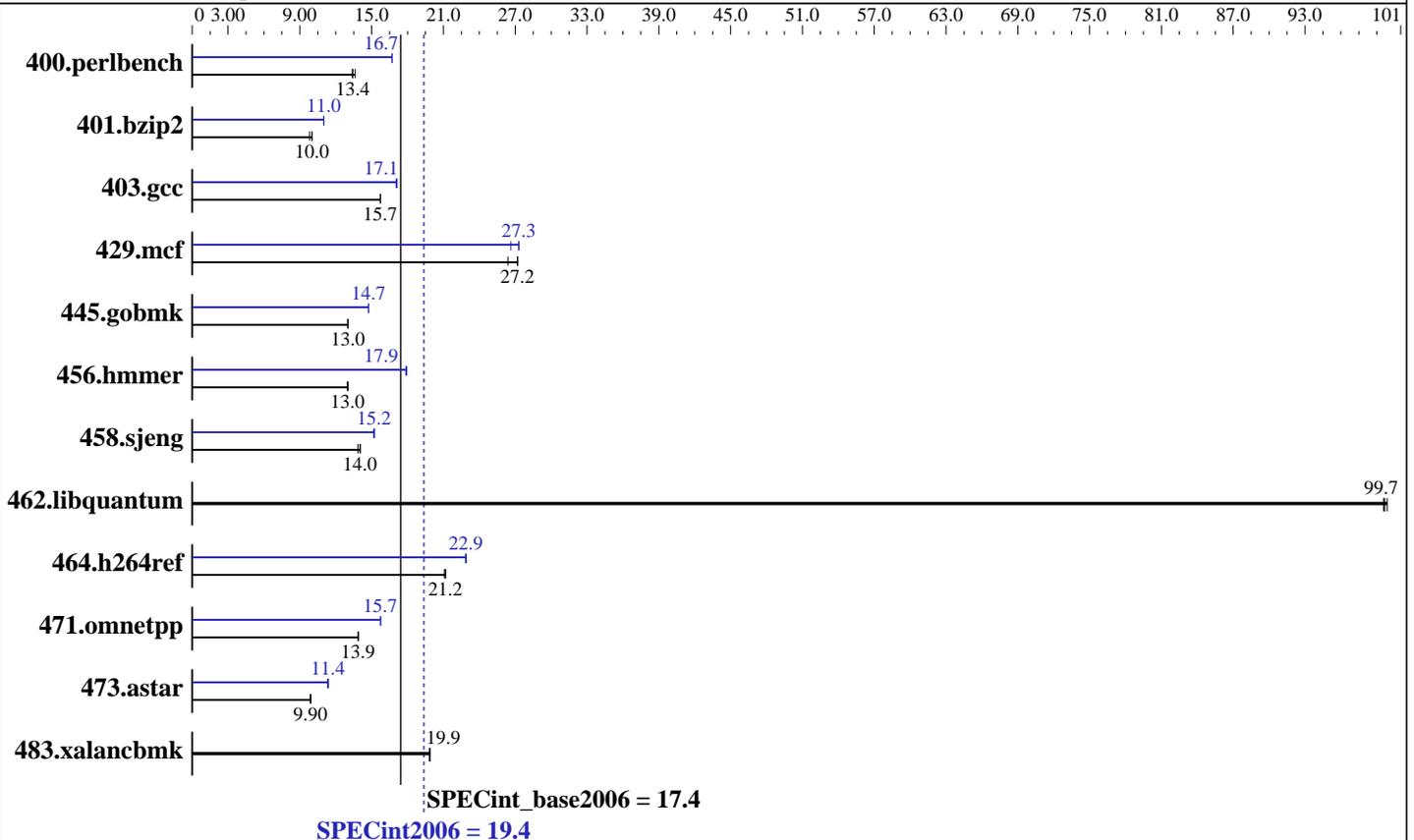
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Dec-2009

Hardware Availability: Aug-2009

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon E5502
 CPU Characteristics:
 CPU MHz: 1867
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 4 MB I+D on chip per chip
 Other Cache: None
 Memory: 24 GB (6 x 4 GB DDR3-1333 RDIMM,CL9 running at 800MHz)
 Disk Subsystem: 1 x 750 GB SATA II, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
 Compiler: Intel C++ Compiler Professional 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1
 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	729	13.4	<u>728</u>	<u>13.4</u>	717	13.6	585	16.7	<u>585</u>	<u>16.7</u>	584	16.7
401.bzip2	983	9.81	963	10.0	<u>963</u>	<u>10.0</u>	877	11.0	878	11.0	<u>877</u>	<u>11.0</u>
403.gcc	<u>512</u>	<u>15.7</u>	511	15.7	512	15.7	471	17.1	471	17.1	<u>471</u>	<u>17.1</u>
429.mcf	346	26.4	<u>336</u>	<u>27.2</u>	335	27.2	342	26.6	334	27.3	<u>334</u>	<u>27.3</u>
445.gobmk	806	13.0	806	13.0	<u>806</u>	<u>13.0</u>	712	14.7	711	14.7	<u>712</u>	<u>14.7</u>
456.hmmmer	718	13.0	717	13.0	<u>718</u>	<u>13.0</u>	522	17.9	521	17.9	<u>521</u>	<u>17.9</u>
458.sjeng	873	13.9	861	14.0	<u>862</u>	<u>14.0</u>	<u>796</u>	<u>15.2</u>	796	15.2	795	15.2
462.libquantum	208	99.6	<u>208</u>	<u>99.7</u>	207	99.9	208	99.6	<u>208</u>	<u>99.7</u>	207	99.9
464.h264ref	1046	21.2	1049	21.1	<u>1046</u>	<u>21.2</u>	968	22.9	<u>967</u>	<u>22.9</u>	967	22.9
471.omnetpp	451	13.9	450	13.9	<u>451</u>	<u>13.9</u>	396	15.8	<u>397</u>	<u>15.7</u>	397	15.7
473.astar	710	9.89	<u>709</u>	<u>9.90</u>	709	9.91	621	11.3	617	11.4	<u>618</u>	<u>11.4</u>
483.xalancbmk	347	19.9	<u>347</u>	<u>19.9</u>	348	19.8	347	19.9	<u>347</u>	<u>19.9</u>	348	19.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

Default BIOS Settings

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
<http://www.supermicro.com/products/motherboard/QPI/5500/X8DT6-F.cfm?IPMI=Y&SAS=Y>

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel  
-par-runtime-control -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc
```

```
401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
456.hmmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

C++ benchmarks (except as noted below):

```
icpc
```

```
473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32  
401.bzip2: -DSPEC_CPU_LP64  
456.hmmmer: -DSPEC_CPU_LP64  
458.sjeng: -DSPEC_CPU_LP64  
462.libquantum: -DSPEC_CPU_LINUX  
473.astar: -DSPEC_CPU_LP64  
483.xalancbmk: -DSPEC_CPU_LINUX
```



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Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzips2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc
-opt-malloc-options=3

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revE.20100119.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revE.20100119.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 06:24:43 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 19 January 2010.