



SPEC® CFP2006 Result

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Acer Incorporated

SPECfp®_rate2006 = 70.4

Acer AR160 F1 (Intel Xeon E5502)

SPECfp_rate_base2006 = 67.8

CPU2006 license: 97

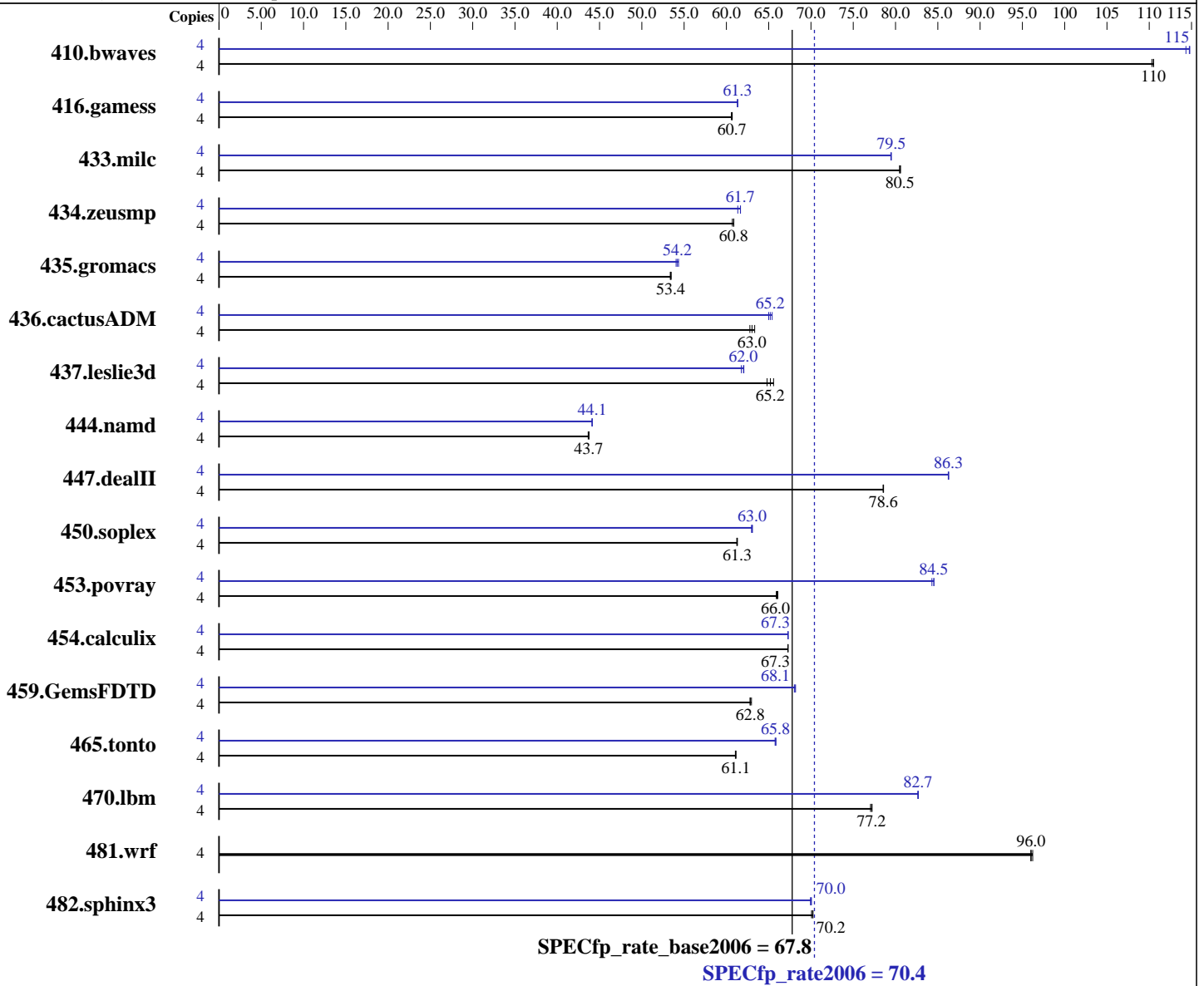
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Nov-2009

Hardware Availability: Jan-2010

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon E5502
 CPU Characteristics:
 CPU MHz: 1867
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64)
 Kernel 2.6.27.19-5
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux
 Build 20090131 Package ID: I_cproc_p_11.0.080,
 I_cprof_p_11.0.080
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

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L3 Cache: 4 MB I+D on chip per chip
Other Cache: None
Memory: 24 GB (6 x 4 GB DDR3-1333 RDIMM)
Disk Subsystem: 300 GB SATA II, 10000 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	493	110	<u>492</u>	<u>110</u>	492	111	4	474	115	<u>474</u>	<u>115</u>	475	114
416.gamess	4	1293	60.6	1290	60.7	<u>1291</u>	<u>60.7</u>	4	<u>1277</u>	<u>61.3</u>	1278	61.3	1277	61.3
433.milc	4	<u>456</u>	<u>80.5</u>	456	80.6	456	80.5	4	<u>462</u>	<u>79.5</u>	462	79.5	462	79.4
434.zeusmp	4	598	60.9	<u>598</u>	<u>60.8</u>	600	60.7	4	590	61.7	<u>590</u>	<u>61.7</u>	593	61.3
435.gromacs	4	534	53.5	535	53.4	<u>535</u>	<u>53.4</u>	4	528	54.0	<u>527</u>	<u>54.2</u>	526	54.3
436.cactusADM	4	<u>758</u>	<u>63.0</u>	761	62.8	755	63.3	4	731	65.4	<u>733</u>	<u>65.2</u>	735	65.0
437.leslie3d	4	580	64.8	573	65.6	<u>577</u>	<u>65.2</u>	4	<u>606</u>	<u>62.0</u>	606	62.1	609	61.8
444.namd	4	733	43.8	735	43.7	<u>734</u>	<u>43.7</u>	4	<u>727</u>	<u>44.1</u>	727	44.1	727	44.1
447.dealII	4	<u>582</u>	<u>78.6</u>	582	78.6	583	78.5	4	<u>530</u>	<u>86.3</u>	530	86.3	531	86.2
450.soplex	4	544	61.3	<u>544</u>	<u>61.3</u>	545	61.2	4	530	63.0	529	63.1	<u>529</u>	<u>63.0</u>
453.povray	4	322	66.1	<u>323</u>	<u>66.0</u>	323	65.9	4	<u>252</u>	<u>84.5</u>	252	84.3	252	84.5
454.calculix	4	490	67.3	<u>491</u>	<u>67.3</u>	491	67.3	4	490	67.3	491	67.3	<u>491</u>	<u>67.3</u>
459.GemsFDTD	4	676	62.8	674	63.0	<u>676</u>	<u>62.8</u>	4	<u>623</u>	<u>68.1</u>	622	68.2	624	68.0
465.tonto	4	644	61.1	<u>644</u>	<u>61.1</u>	645	61.1	4	598	65.9	<u>598</u>	<u>65.8</u>	599	65.8
470.lbm	4	712	77.2	713	77.0	<u>712</u>	<u>77.2</u>	4	<u>665</u>	<u>82.7</u>	665	82.6	665	82.7
481.wrf	4	465	96.0	464	96.2	<u>465</u>	<u>96.0</u>	4	465	96.0	464	96.2	<u>465</u>	<u>96.0</u>
482.sphinx3	4	<u>1111</u>	<u>70.2</u>	1110	70.2	1113	70.1	4	1115	69.9	1113	70.1	<u>1114</u>	<u>70.0</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

General Notes

This result was measured on the Gateway GR160 F1.
The Acer AR160 F1 and Gateway GR160 F1 are electronically equivalent.



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Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc

450.soplex: icpc -m32

Fortran benchmarks (except as noted below):

ifort

437.leslie3d: ifort -m32

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 444.namd: -DSPEC_CPU_LP64
 447.deallI: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -fno-alias

470.lbm: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
 -auto-ilp32

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Peak Optimization Flags (Continued)

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep-

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)

437.leslie3d: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -opt-prefetch

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -opt-prefetch

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

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Peak Optimization Flags (Continued)

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -opt-prefetch -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revF.20100201.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revF.20100201.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 06:41:39 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 1 February 2010.