



# SPEC<sup>®</sup> CINT2006 Result

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## Acer Incorporated

SPECint<sup>®</sup>\_rate2006 = 70.7

## Gateway GR160 F1 (Intel Xeon E5502)

SPECint\_rate\_base2006 = 66.1

CPU2006 license: 97

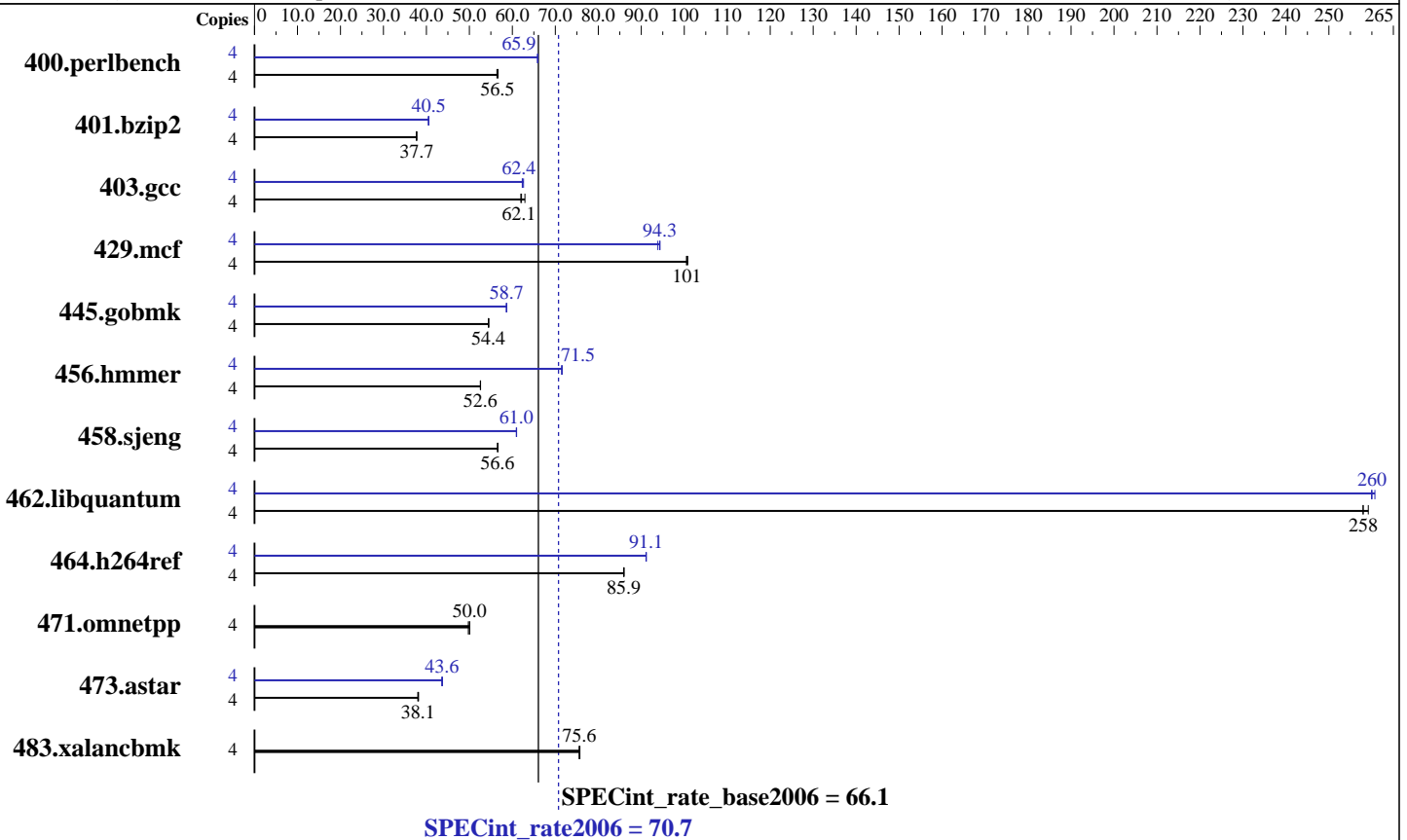
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Nov-2009

Hardware Availability: Jan-2010

Software Availability: Feb-2009



### Hardware

CPU Name: Intel Xeon E5502  
 CPU Characteristics:  
 CPU MHz: 1867  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 4 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 24 GB (6 x 4 GB DDR3-1333 RDIMM)  
 Disk Subsystem: 300 GB SATA II, 10000 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64)  
 Kernel 2.6.27.19-5  
 Compiler: Intel C++ Compiler 11.0 for Linux  
 Build 20090131 Package ID: l\_cproc\_p\_11.0.080  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1  
 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	693	56.4	<b>691</b>	<b>56.5</b>	690	56.7	4	<b>593</b>	<b>65.9</b>	593	65.9	593	65.9
401.bzip2	4	<b>1023</b>	<b>37.7</b>	1022	37.8	1025	37.7	4	955	40.4	952	40.6	<b>952</b>	<b>40.5</b>
403.gcc	4	<b>518</b>	<b>62.1</b>	512	62.9	520	62.0	4	<b>516</b>	<b>62.4</b>	517	62.3	514	62.6
429.mcf	4	362	101	<b>362</b>	<b>101</b>	363	101	4	389	93.9	387	94.4	<b>387</b>	<b>94.3</b>
445.gobmk	4	770	54.5	<b>771</b>	<b>54.4</b>	771	54.4	4	<b>715</b>	<b>58.7</b>	715	58.7	716	58.6
456.hammer	4	709	52.6	<b>710</b>	<b>52.6</b>	710	52.6	4	<b>522</b>	<b>71.5</b>	522	71.5	521	71.6
458.sjeng	4	856	56.6	855	56.6	<b>855</b>	<b>56.6</b>	4	<b>794</b>	<b>61.0</b>	794	61.0	794	60.9
462.libquantum	4	320	259	<b>321</b>	<b>258</b>	321	258	4	319	260	318	261	<b>319</b>	<b>260</b>
464.h264ref	4	<b>1030</b>	<b>85.9</b>	1029	86.0	1031	85.9	4	971	91.2	<b>971</b>	<b>91.1</b>	972	91.1
471.omnetpp	4	<b>500</b>	<b>50.0</b>	502	49.8	500	50.0	4	<b>500</b>	<b>50.0</b>	502	49.8	500	50.0
473.astar	4	736	38.2	<b>737</b>	<b>38.1</b>	739	38.0	4	642	43.8	<b>643</b>	<b>43.6</b>	644	43.6
483.xalanbmk	4	<b>365</b>	<b>75.6</b>	365	75.7	365	75.5	4	<b>365</b>	<b>75.6</b>	365	75.7	365	75.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## General Notes

This result was measured on the Gateway GR160 F1.  
The Acer AR160 F1 and Gateway GR160 F1 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalanbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

```
401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

C++ benchmarks (except as noted below):

icpc

```
473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc
```

## Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
```

```
401.bzip2: -DSPEC_CPU_LP64
```

```
456.hmmer: -DSPEC_CPU_LP64
```

```
458.sjeng: -DSPEC_CPU_LP64
```

```
462.libquantum: -DSPEC_CPU_LINUX
```

```
473.astar: -DSPEC_CPU_LP64
```

```
483.xalancbmk: -DSPEC_CPU_LINUX
```



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static  
-opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap64

483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revE.20100216.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revE.20100216.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.  
Report generated on Wed Jul 23 06:29:45 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 16 February 2010.