



# SPEC<sup>®</sup> CINT2006 Result

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Itaotec

SPECint<sup>®</sup>\_rate2006 = 127

Servidor Itaotec MX223 (Intel Xeon X5560)

SPECint\_rate\_base2006 = 119

CPU2006 license: 9001

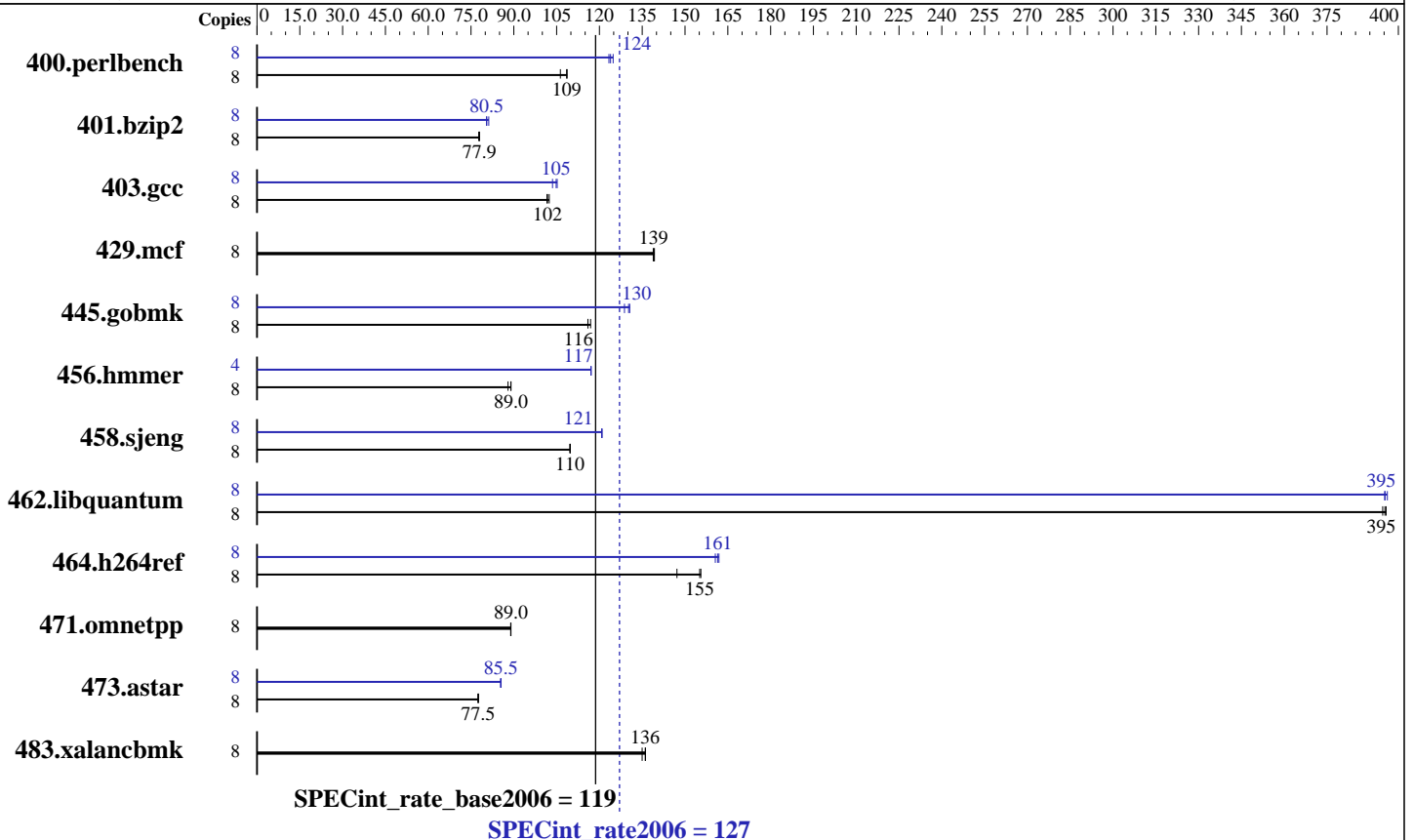
Test date: Oct-2009

Test sponsor: Itaotec

Hardware Availability: Mar-2009

Tested by: Itaotec

Software Availability: Feb-2009



### Hardware

CPU Name: Intel Xeon X5560  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz  
 CPU MHz: 2800  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 12 GB (3 x 4GB DDR3-1333, CL 9, ECC)  
 Disk Subsystem: 1 x 160 GB SATA-2, 7200RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2 with patch Linux kernel 20090119, Kernel 2.6.16.60-0.34-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l\_cproc\_p\_11.0.081  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	719	109	735	106	<b>720</b>	<b>109</b>	8	<b>631</b>	<b>124</b>	626	125	634	123
401.bzip2	8	994	77.7	990	78.0	<b>991</b>	<b>77.9</b>	8	<b>959</b>	<b>80.5</b>	959	80.5	950	81.2
403.gcc	8	<b>633</b>	<b>102</b>	629	102	634	102	8	622	104	<b>614</b>	<b>105</b>	612	105
429.mcf	8	525	139	<b>525</b>	<b>139</b>	524	139	8	525	139	<b>525</b>	<b>139</b>	524	139
445.gobmk	8	718	117	<b>724</b>	<b>116</b>	724	116	8	642	131	<b>644</b>	<b>130</b>	652	129
456.hammer	8	<b>839</b>	<b>89.0</b>	838	89.0	848	88.0	4	<b>319</b>	<b>117</b>	319	117	319	117
458.sjeng	8	<b>882</b>	<b>110</b>	881	110	883	110	8	<b>801</b>	<b>121</b>	801	121	801	121
462.libquantum	8	420	395	<b>419</b>	<b>395</b>	419	396	8	<b>419</b>	<b>395</b>	419	395	418	396
464.h264ref	8	1203	147	1138	156	<b>1141</b>	<b>155</b>	8	1094	162	<b>1097</b>	<b>161</b>	1102	161
471.omnetpp	8	562	89.0	<b>562</b>	<b>89.0</b>	562	88.9	8	562	89.0	<b>562</b>	<b>89.0</b>	562	88.9
473.astar	8	723	77.6	725	77.5	<b>725</b>	<b>77.5</b>	8	658	85.3	<b>657</b>	<b>85.5</b>	656	85.5
483.xalanbmk	8	405	136	<b>406</b>	<b>136</b>	409	135	8	405	136	<b>406</b>	<b>136</b>	409	135

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## General Notes

This result was measured on the Servidor Itautec MX223.  
The Servidor Itautec MX223 and the Servidor Itautec MX203 are electronically equivalent.  
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.

## Base Compiler Invocation

C benchmarks:  
icc  
  
C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalanbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/richard/sh/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/081/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/081/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/081/bin/intel64/icc

C++ benchmarks (except as noted below):

icpc

473.astar: /opt/intel/Compiler/11.0/081/bin/intel64/icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static  
-opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32  
-Wl,-z,muldefs  
-L/home/richard/sh/SmartHeap\_8.1/lib -lsmartheap64

483.xalanbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

Continued on next page



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## Peak Other Flags (Continued)

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.0-int-linux64-revA.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.0-int-linux64-revA.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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