



SPEC® CFP2006 Result

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Dell Inc.

SPECfp®2006 = 31.3

PowerEdge R510 (Intel Xeon E5520, 2.26 GHz)

SPECfp_base2006 = 29.5

CPU2006 license: 55

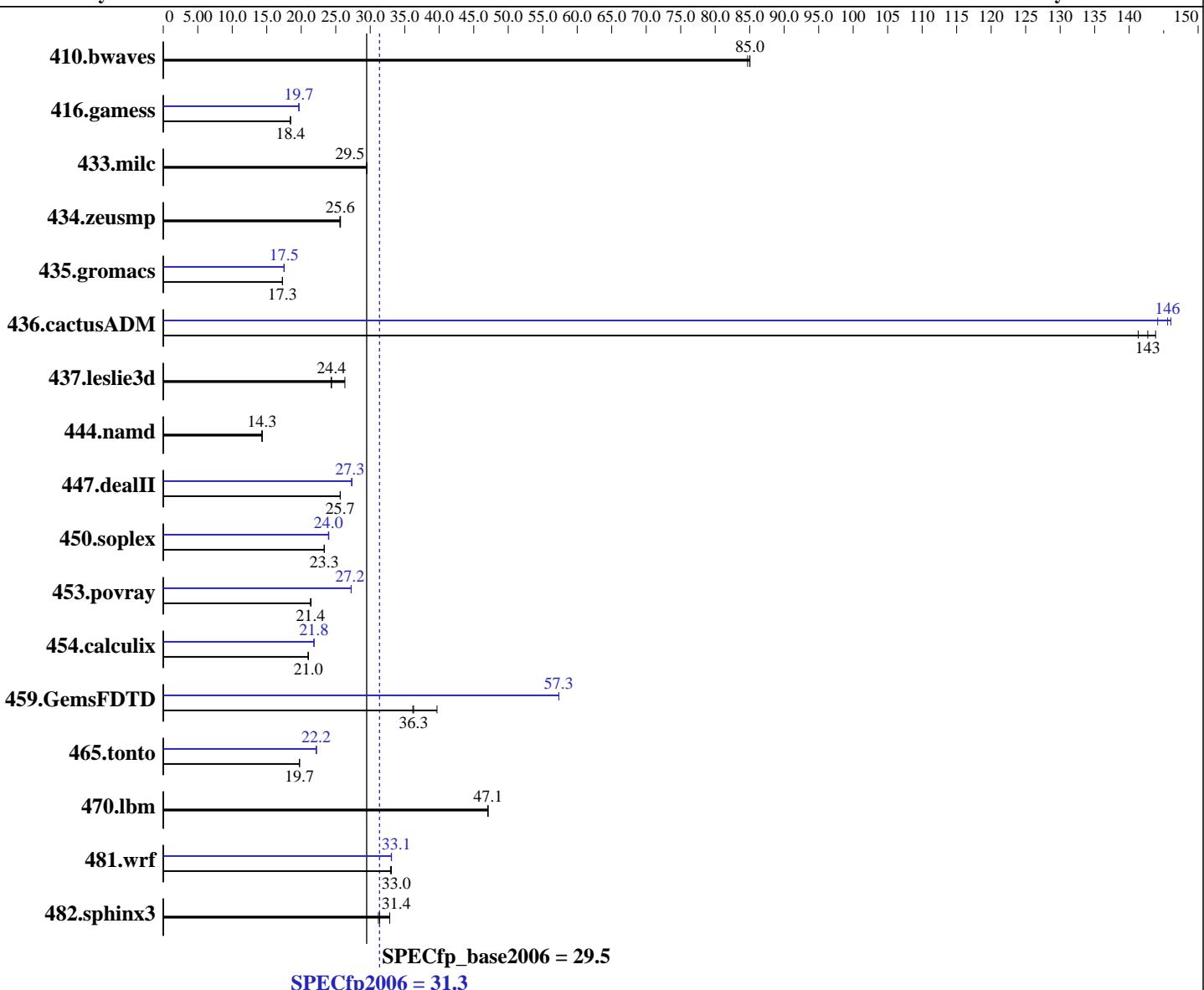
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Aug-2009

Hardware Availability: Oct-2009

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon E5520
CPU Characteristics: Intel Turbo Boost Technology up to 2.53 GHz
CPU MHz: 2267
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler: Intel C++ and Fortran Compiler Professional 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080, l_cprof_p_11.0.080
Auto Parallel: Yes
File System: ReiserFS
System State: Run level 3 (multi-user)

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L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 24 GB (6 x 4 GB DDR3-1333 DR RDIMM downclocked to 1066 MHz)
 Disk Subsystem: 1 x 500 GB 7200 RPM SATA
 Other Hardware: None

Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	160	85.0	160	84.7	160	85.0	160	85.0	160	84.7	160	85.0
416.gamess	1062	18.4	1061	18.5	1063	18.4	996	19.7	997	19.6	995	19.7
433.milc	312	29.5	311	29.5	311	29.5	312	29.5	311	29.5	311	29.5
434.zeusmp	355	25.6	355	25.6	354	25.7	355	25.6	355	25.6	354	25.7
435.gromacs	413	17.3	413	17.3	413	17.3	408	17.5	408	17.5	408	17.5
436.cactusADM	84.6	141	83.7	143	83.1	144	81.8	146	82.9	144	82.1	146
437.leslie3d	357	26.3	385	24.4	386	24.3	357	26.3	385	24.4	386	24.3
444.namd	559	14.3	561	14.3	560	14.3	559	14.3	561	14.3	560	14.3
447.dealII	446	25.7	446	25.7	446	25.7	418	27.3	419	27.3	419	27.3
450.soplex	358	23.3	357	23.3	357	23.4	348	24.0	347	24.0	348	23.9
453.povray	248	21.4	249	21.4	249	21.4	195	27.2	195	27.2	195	27.2
454.calculix	392	21.0	393	21.0	392	21.0	378	21.8	378	21.9	378	21.8
459.GemsFDTD	267	39.7	292	36.3	293	36.2	185	57.3	185	57.4	185	57.3
465.tonto	498	19.7	499	19.7	498	19.8	443	22.2	444	22.2	444	22.2
470.lbm	292	47.0	292	47.1	292	47.1	292	47.0	292	47.1	292	47.1
481.wrf	339	32.9	338	33.1	338	33.0	338	33.1	338	33.1	338	33.1
482.sphinx3	625	31.2	621	31.4	594	32.8	625	31.2	621	31.4	594	32.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

OMP_NUM_THREADS set to number of cores
 KMP_AFFINITY set to granularity=fine,scatter
 KMP_STACKSIZE set to 200M



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Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
  433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
  444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
  465.tonto: -DSPEC_CPU_LP64
  470.lbm: -DSPEC_CPU_LP64
  481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch



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Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks (except as noted below):
icpc

450.soplex: icpc -m32

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

C++ benchmarks:

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Peak Optimization Flags (Continued)

444.namd: basepeak = yes

```
447.dealII: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
             -unroll2 -ansi-alias -scalar-rep -opt-prefetch
```

```
450.soplex: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
             -opt-malloc-options=3
```

```
453.povray: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
              -unroll4 -ansi-alias
```

Fortran benchmarks:

410.bwaves: basepeak = yes

```
416.gamess: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
              -unroll2 -Ob0 -ansi-alias -scalar-rep-
```

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

```
459.GemsFDTD: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
                -unroll2 -Ob0 -opt-prefetch -parallel
```

```
465.tonto: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
              -unroll4 -auto
```

Benchmarks using both Fortran and C:

```
435.gromacs: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
               -opt-prefetch -auto-ilp32
```

```
436.cactusADM: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                  -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
                  -unroll2 -opt-prefetch -parallel -auto-ilp32
```

454.calculix: -xsSE4 .2 -ipo -O3 -no-prec-div -static -auto-ilp32

```
481.wrf: -xsSE4 .2 -ipo -O3 -no-prec-div -static -opt-prefetch
           -parallel -auto-ilp32
```



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090805.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090805.01.xml>

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For other inquiries, please contact webmaster@spec.org.

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