



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint®_rate2006 = Not Run

PowerEdge T610 (Intel Xeon E5502, 1.86 GHz)

SPECint_rate_base2006 = 58.5

CPU2006 license: 55

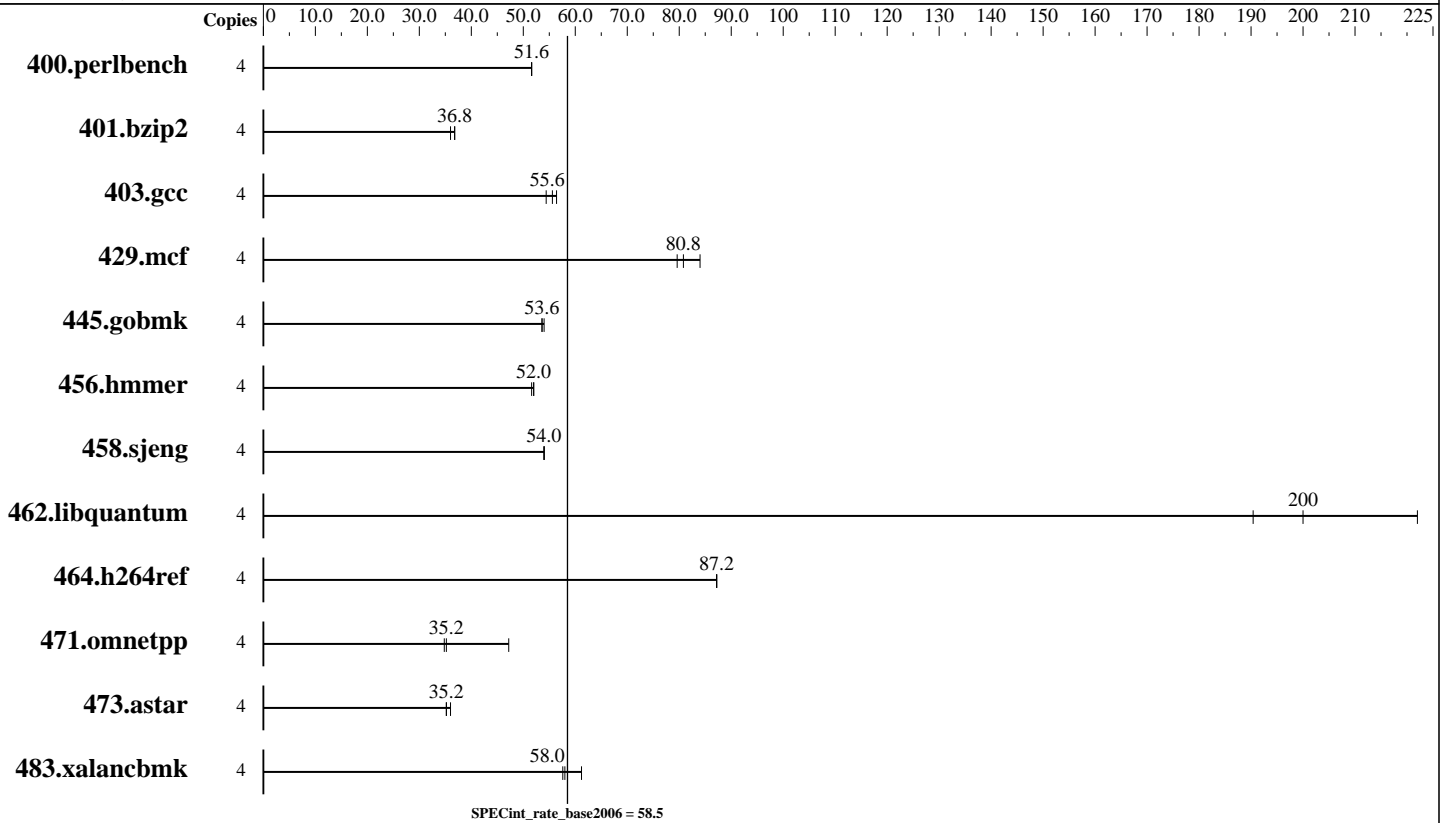
Test date: Sep-2009

Test sponsor: Dell Inc.

Hardware Availability: Mar-2009

Tested by: Dell Inc.

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon E5502
 CPU Characteristics:
 CPU MHz: 1867
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 4 MB I+D on chip per chip
 Other Cache: None
 Memory: 4 GB (4 x 1 GB DDR3-1066 DR RDIMM downclocked to 800 MHz)
 Disk Subsystem: 1 x 73 GB 15000 RPM SAS
 Other Hardware: None

Software

Operating System: Microsoft Windows Server 2008 Enterprise x64 Edition SP2
 Compiler: Intel C++ Compiler for IA-32, Version 11.0 Build 20090131 Package ID: w_eproc_p_11.0.072 Microsoft Visual Studio 2008 SP1
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: MicroQuill SmartHeap Library 8.1



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint_rate2006 = Not Run

PowerEdge T610 (Intel Xeon E5502, 1.86 GHz)

SPECint_rate_base2006 = 58.5

CPU2006 license: 55

Test date: Sep-2009

Test sponsor: Dell Inc.

Hardware Availability: Mar-2009

Tested by: Dell Inc.

Software Availability: Feb-2009

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	<u>757</u>	<u>51.6</u>	756	51.6	758	51.6							
401.bzip2	4	1051	36.8	1068	36.0	<u>1051</u>	<u>36.8</u>							
403.gcc	4	570	56.4	593	54.4	<u>579</u>	<u>55.6</u>							
429.mcf	4	434	84.0	458	79.6	<u>451</u>	<u>80.8</u>							
445.gobmk	4	782	53.6	<u>781</u>	<u>53.6</u>	779	54.0							
456.hammer	4	<u>720</u>	<u>52.0</u>	720	52.0	722	51.6							
458.sjeng	4	896	54.0	<u>897</u>	<u>54.0</u>	897	54.0							
462.libquantum	4	<u>414</u>	<u>200</u>	373	222	435	190							
464.h264ref	4	<u>1014</u>	<u>87.2</u>	1013	87.2	1014	87.2							
471.omnetpp	4	<u>712</u>	<u>35.2</u>	528	47.2	715	34.8							
473.astar	4	777	36.0	797	35.2	<u>797</u>	<u>35.2</u>							
483.xalancbmk	4	450	61.2	479	57.6	<u>477</u>	<u>58.0</u>							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Binaries were built on Windows Vista Business (64-bit)

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

Base Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Base Optimization Flags

C benchmarks:

```
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000
```

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint_rate2006 = Not Run

PowerEdge T610 (Intel Xeon E5502, 1.86 GHz)

SPECint_rate_base2006 = 58.5

CPU2006 license: 55

Test date: Sep-2009

Test sponsor: Dell Inc.

Hardware Availability: Mar-2009

Tested by: Dell Inc.

Software Availability: Feb-2009

Base Optimization Flags (Continued)

C++ benchmarks:

-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/dell.flags.ic11.0.win.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/dell.flags.ic11.0.win.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 04:50:04 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 13 October 2009.